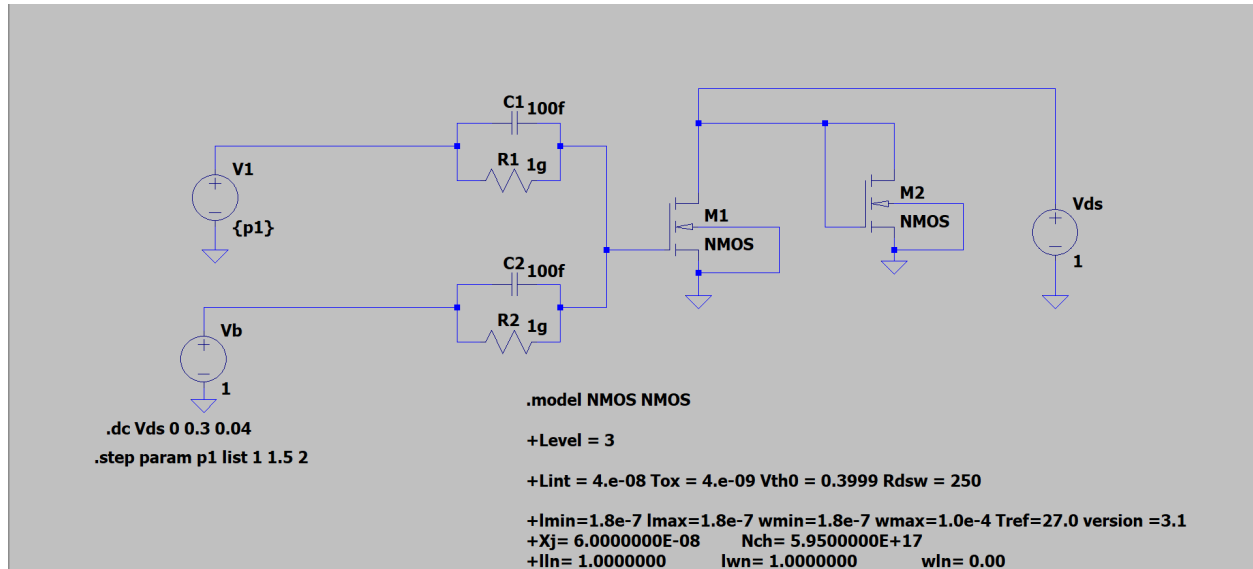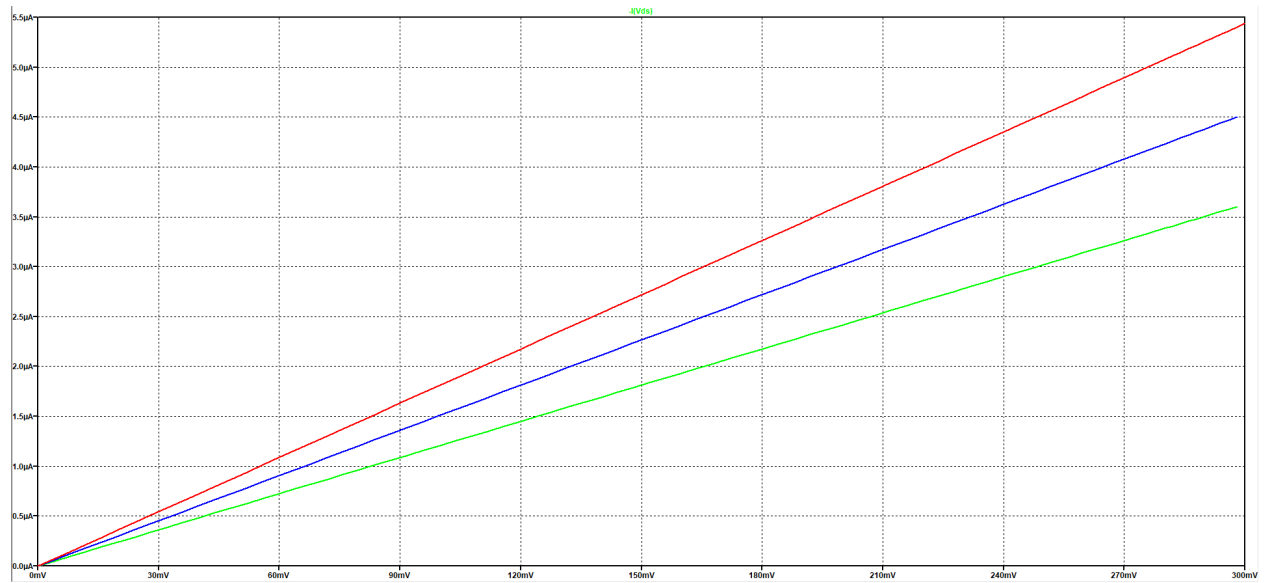**Schematic:**

Netlist:

```
M1 1 3 0 0 NMOS l=0.02u w=8u
M2 1 1 0 0 NMOS l=0.02u w=8u

C1 3 2 100f
C2 3 4 100f
R1 3 2 1g
R2 3 4 1g

Vb 4 0 1
Vds 1 0 1
V1 2 0 {p1}

.step param p1 list 1 1.5 2
.dc Vds 0 0.3 0.04
.probe
.end
```

Outputs:

Vb (mV)	R (k)
1 (green)	82.77
1.5 (blue)	66.16
2 (red)	55.17