

**Netlist:**

```
.include 180.txt

*.model N_180 NMOS
*.model P_180 PMOS

M1 1 2 7 7 P_180 W=2.5u L=0.27u
M3 3 1 7 7 P_180 W=2.5u L=0.27u
M5 4 3 7 7 P_180 W=2.5u L=0.27u
M7 5 4 7 7 P_180 W=2.5u L=0.27u
M9 6 5 7 7 P_180 W=2.5u L=0.27u

M2 1 2 0 0 N_180 W=1u L=0.27u
M4 3 1 0 0 N_180 W=1u L=0.27u
M6 4 3 0 0 N_180 W=1u L=0.27u
M8 5 4 0 0 N_180 W=1u L=0.27u
M10 6 5 0 0 N_180 W=1u L=0.27u

Cl 6 0 {p1}

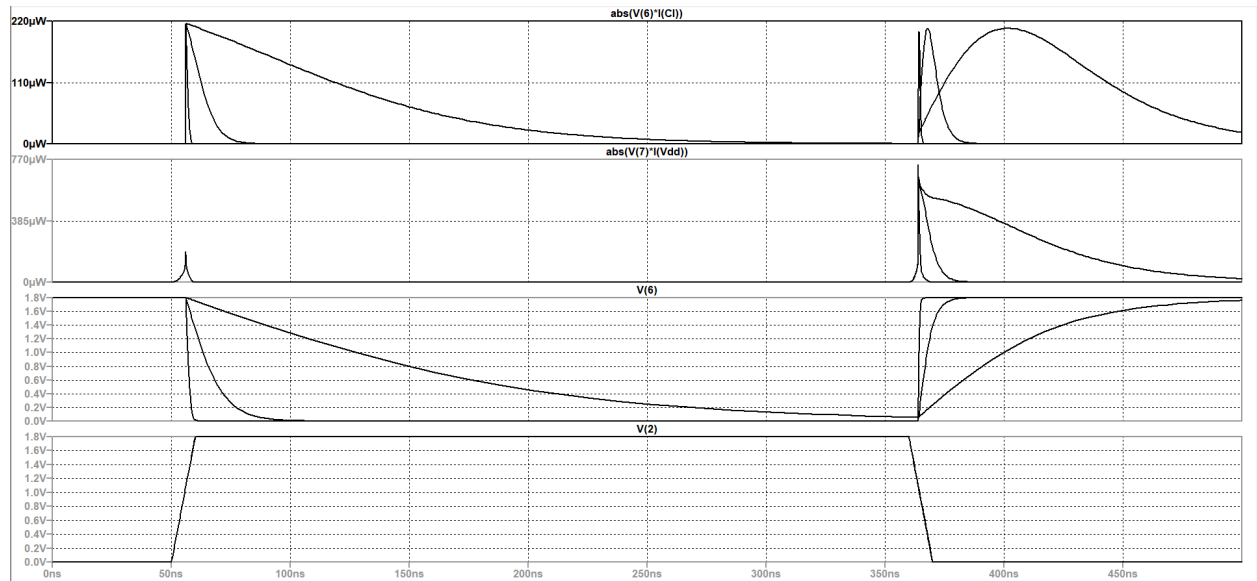
.step param p1 list 0.1p 1p 10p
Vdd 7 0 DC 1.8
Vin 2 0 PULSE(0 1.8 50n 10n 10n 300n 700n 6)

.tran 0 500n 0.001n
.probe
.end
```

**Outputs:**

For load capacitance (Cl) 0.1p, 1p and 10p.

Using standard mosfets (NMOS: W/L=1/0.27, PMOS: W/L=2.5/0.27)



Using 180 mosfets (NMOS: W/L=1/0.27, PMOS: W/L=2.5/0.27)

