

# MICROPROCESSOR LAB EXPERIMENT 8

GROUP - 18

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## Introduction :

- In this experiment, we will observe various aspects relating to the ARM assembly language.
- We will also write a few programs in ARM assembly. We will use Keil  $\mu$ Vision software to run the ARM assembly language programs
- ARM stands for **Advanced RISC Machine** which is bit more advanced than AVR.

## ARM and Keil $\mu$ vision :

- ARM architectures are the most common electronic design in the world.
- It is known for its speed.
- Using an ARM architecture gives hardware designers more control over their designs and performance, as well as more control over their supply chains.
- In this experiment , we use **LPC2378** microcontroller for operations.
- The tasks that we are going to perform here will help us know the basic operations in ARM.
- By using these we will analyse the various instructions available in ARM and get familiar with those.

## Instructions used :

Instruction	Usage
<b>LDR Rx, Rd</b>	Load the value at memory <b>Rd</b> into register <b>Rx</b> .
<b>ADD Rx, Rm, k</b>	Add values in register <b>Rm</b> and constant <b>k</b> ; store in register <b>Rx</b> .
<b>MUL Rx, Rm, K</b>	Multiply values in register <b>Rm</b> and constant <b>K</b> ; store in register <b>Rx</b> .
<b>STR Rm, Rx</b>	Store address of register <b>Rx</b> in pointer <b>Rm</b> .
<b>MOV Rm1, Rm2</b>	Copy value from register <b>Rm1</b> to register <b>Rm2</b> .
<b>SUB Rm1, Rm2, k</b>	Subtract value of <b>Rm2</b> or constant <b>k</b> from <b>Rm1</b> ; store in <b>Rm1</b> .
<b>CMP Rm1, Rm2</b>	Compare values of <b>Rm1</b> and <b>Rm2</b> , setting flags if <b>Rm1</b> ; <b>Rm2</b> : Sign, Negative, Carry.
<b>DEC Rm</b>	Decrease value in register <b>Rm</b> by one.
<b>BEQ</b>	Conditional jump if zero flag is set; branches to pointer.
<b>SWI &amp;11</b>	Terminates the program by breaking the program counter.

## Tasks 1 & 2

### Code - Task-1

```
1 PINSEL10 EQU 0xE002C028
2 FIO2DIR EQU 0x3FFFC040
3 PINSEL4 EQU 0xE002C010
4 FIO2PIN EQU 0x3FFFC054
5 AREA LED, CODE, READONLY
6 ENTRY
7 EXPORT SystemInit
8 EXPORT __main
```

### Explanation

- AREA , CODE , READONLY are assembler directives that direct the code to begin the program and run through it.
- EQU is used to assign the values on the right wherever the values on the left are present in the code.

### Code - Task-2

```
1 SystemInit
2     LDR R0, PINSEL10;
3     LDR R1, [R0]
4     MOV R2, #0x0;
5     STR R2, [R0]
```

### Explanation

- Loads the value at PINSEL10 into R1.
- Loads the value at the address stored in R0.
- Moving 0x0 in R2.
- Storing the value in R2 to the address stored in R0.

## Task 3 & 4

### Code - Task-3

```
1  LDR R0, =PINSEL4;  
2  LDR R2, =0xFFFF0000;  
3  STR R2, [R0]
```

### Explanation :

- Loading into R0 from the address in PINSEL4
- Loading into R2 a constant value (0xFFFF0000)
- Storing the value in R2 to the address stored in R0.

### Code - Task-4

```
1  LDR R0, =FIO2DIR  
2  LDR R2, =0x0000FFFF  
3  STR R2, [R0]
```

### Explanation :

- Loading into R0 from the address in FIO2DIR
- Loading into R2 a constant value (0x0000FFFF)
- Storing the value in R2 to the address stored in R0.

## Code - Task-5

```

1  __main
2  forever
3      LDR R0, =FIO2PIN;
4      MOV R2, #0xF0
5      STR R2, [R0]
6      b forever
7      END

```

### Explanation :

- Loading into R0 from the address in FIO2PIN.
- Moving #0xF0 into R2
- Storing the value in R2 to the address stored in R0.

## Code - Task-6

```

1  __main
2  forever
3      LDR R0, =FIO2PIN;
4      MOV R2, #0xF0
5      STR R2, [R0]
6      LDR R3, =0x0000FFFF
7  loop
8      SUBS R3, R3, #1
9      BNE loop
10     MOV R2, #0x00
11     STR R2, [R0]
12     LDR R3, =0x0000FFFF
13 loop1
14     SUBS R3, R3, #1
15     BNE loop1
16
17     b forever
18     END

```

### Explanation :

- R3 is a counter using for adding delays
- loop and loop1 add delay