



Pin NUM	DEF	Function Description	Note
1	VDD	Power Supply 3.3V	RF VDD
2	GND	GND	
3	SI	MOSI (SPI)	
4	SCLK	CLOCK (SPI)	
5	SO	MISO (SPI)	
6	GDO2	Digital output pin for general use: <ul style="list-style-type: none"> • Test signals • FIFO status signals • Clear channel indicator • Clock output, down-divided from XOSC • Serial output RX data 	
7	GDO0	Digital output pin for general use: <ul style="list-style-type: none"> • Test signals • FIFO status signals • Clear Channel Indicator • Clock output, down-divided from XOSC • Serial output RX data • Serial input TX data Also used as analog test I/O for prototype and production testing	
8	CSN	CS (SPI)	
9	ANT	Antenna interface, the standard 50 Ω antenna	