



VIT

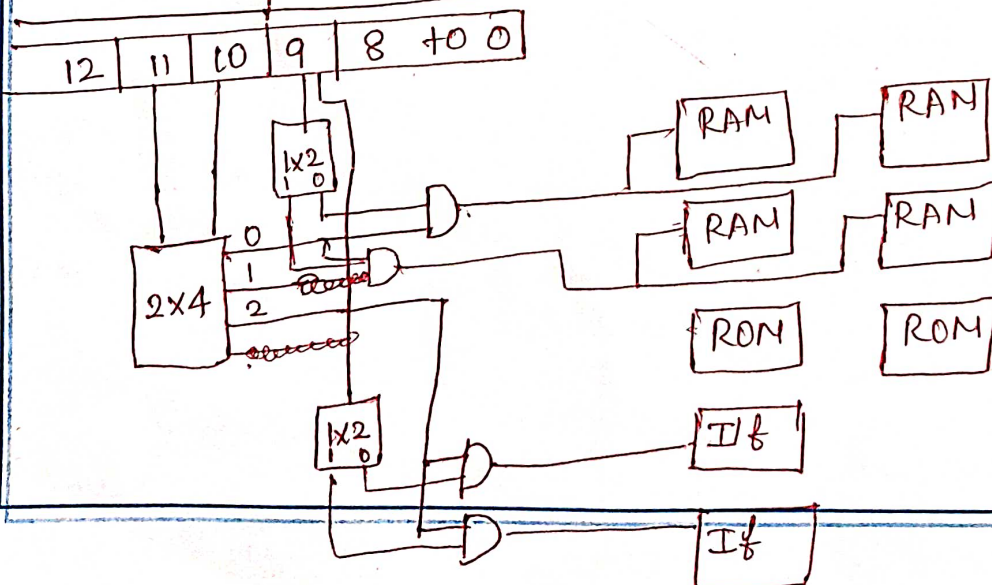
Vellore Institute of Technology

ADDITIONAL SHEET

Initial of the Hall Supervisor with date
Emp. ID.:

	P	Q	P \times Q	x	y	Z	total
RAM	2	2	4	9	1	2	12
ROM	1	2	2	10	0	2	12
I/f	2	1	2	8	1	2	11

			15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
RAM 0.1	0000	01 FF	0	0	0	0	0	0	0	0	x	x	x	x	x	x	x	x
RAM 1.2	0000	01 FF	0	0	0	0	0	0	0	0	x	x	x	x	x	x	x	x
RAM 2.1	0200	03 FF	0	0	0	0	0	0	0	1	x	x	x	x	x	x	x	x
RAM 2.2	0200	03 FF	0	0	0	0	0	0	0	1	x	x	x	x	x	x	x	x
ROM 1.1	0400	07 FF	0	0	0	0	0	0	0	1	x	x	x	x	x	x	x	x
ROM 1.2	0400	07 FF	0	0	0	0	0	0	0	1	x	x	x	x	x	x	x	x
I/f 1.1	0800	08 FF	0	0	0	0	1	0	0	0	x	x	x	x	x	x	x	x
I/f 2.1	08A00	0A FF	0	0	0	0	1	0	1	0	x	x	x	x	x	x	x	x





$$\text{word} = 32\text{-bits} = 4\text{ bytes} = 2^2\text{ bytes}$$

$$\text{cache block} = 2048\text{ bits} = 256\text{ bytes} = 2^8\text{ bytes}$$

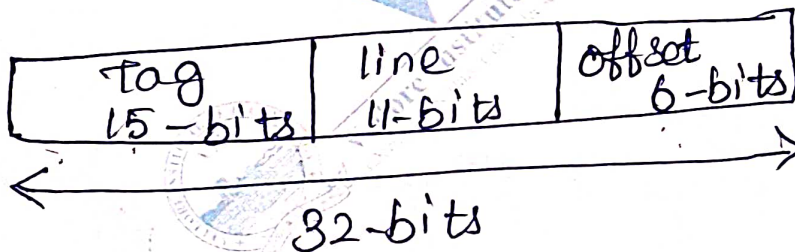
$$\text{addr} = 32\text{-bits}$$

$$\text{cache} = 2048\text{ blocks} = 2^{11}\text{ blocks}$$

$$\text{Block (no. of words)} = \frac{2^8\text{ bytes}}{2^2\text{ bytes}} = 2^6\text{ words}$$

$$\text{word} = 6\text{-bits}$$

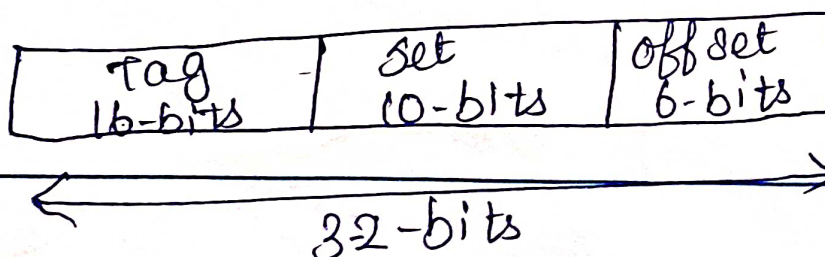
Direct mapped cache



2-way set

$$\text{set} = 2^1\text{ blocks}$$

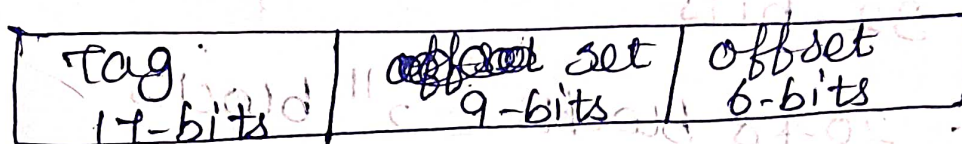
$$\text{cache (no. of sets)} = \frac{2^{11}\text{ blocks}}{2^1\text{ blocks}} = 2^{10}\text{ sets}$$



4-way set also

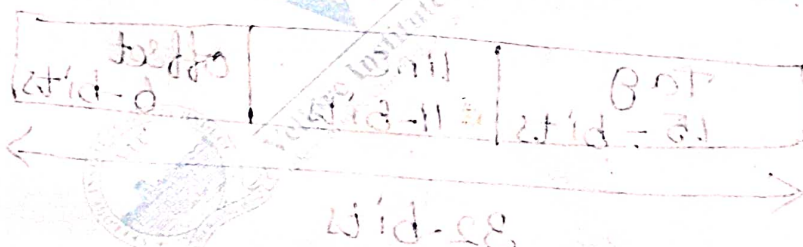
Set = 4 blocks = 2^2 blocks

no. of sets in cache = $\frac{2^{11} \text{ blocks}}{2^2 \text{ blocks}} = 2^9$ sets

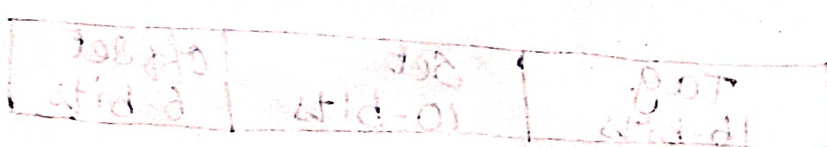


← 82-bits →

word = 8-bits



no. of sets = $\frac{2^{11} \text{ blocks}}{2^2 \text{ blocks}} = 2^9$



addr = 8-bit

page = 16-byte = 2^4 bytes

process = 4 pages

frames = 3

0 2

1 0

2 1

3

LRU

TLB \rightarrow optimal

logical addresses - 7, 17, 37, 20, 40, 60

7 = 000000111

= 0

17 = 00010001

= 1

37 = 00100101

= 2

20 = 00010000

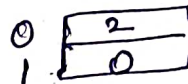
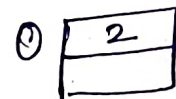
= 1

40 = 00101000

= 2

60 = 00111100

= 3



X

X



no fault



Initial of the Hall Supervisor with date
Emp. ID.:

$\Rightarrow 18 = \begin{array}{r} 11101010 \\ -21 = \underline{\quad\quad\quad} \end{array}$

$19 \div 21 = \text{100000011101011}$

$$Q = 17 = 00010001$$

Q = 17 = 00010001
Bit reversed multiplier =

0	0	0	1	0	0	0	1	0
0	0	1	1	0	0	1	1	

[illegible]

(1) 1 1 1 1 1 1 0 1 0 0 1 1 0 1 1 = -357

$$-13.275 = -1101.010011$$

$$E = 3 + 1023 = 1026$$

$E = 3 + 1023 = 1026$
 $1 \quad 100000000010 \quad 101010011000 \dots$
 $12.48 = 1100.011101 \Rightarrow 1.100011101 \times 2^3$
 $0 \quad 100000000010 \quad 1000111101000 \dots$

$$+21 \times 17$$

$$BR = -21 = \overline{00010101}$$

$$\overline{BR} + 1 = 00010101$$

Comment

AC

QR

Out

SC

$$AC + \overline{BR} + 1$$

$$00000000$$

$$00010001 \quad \underline{0}$$

8

$$00010101$$

$$00010101$$

$$10001000 \quad \underline{1}$$

7

AShr

$$00001010$$

$$AC + BR$$

$$11000001$$

$$11000100 \quad \underline{0}$$

6

AShr

$$11000100$$

$$01100010 \quad \underline{0}$$

5

AShr

$$11000101$$

$$10110001 \quad \underline{0}$$

4

AShr

$$11000100$$

$$AC + \overline{BR} + 1$$

$$00010101$$

$$01011000 \quad \underline{1}$$

3

AShr

$$00010000$$

$$AC + BR$$

$$00001000$$

$$00101100 \quad \underline{0}$$

2

AShr

$$11000100$$

$$00010110 \quad \underline{0}$$

1

AShr

$$11000101$$

$$10001011 \quad \underline{0}$$

0

AShr

$$11000100$$

$$-27 / 22$$

$$22 = 0010110$$

2M

$$27 = 011011$$

$$Q = 100101$$

A

Q

$$1111111$$

$$100101$$

$$\begin{array}{r} 011011 \\ 100100 \\ \hline 100101 \end{array}$$