642303003

Assignment No:02

```
rohan@rohanlaptop: ~/yashppl Q = - D ×

rohan@rohanlaptop: ~/yashppl$ gedit ppl2.c

^V^C
rohan@rohanlaptop: ~/yashppl$ gcc --save-temps ppl2.c
rohan@rohanlaptop: ~/yashppl$ ls
a.out a-ppl1.o a-ppl2.i a-ppl2.s ppl1.c
a-ppl1.i a-ppl1.s a-ppl2.o ppl1 ppl2.c
rohan@rohanlaptop: ~/yashppl$
```

Objedump ppl2.o

```
rohan@rohanlaptop:-/yeshpp $ objdump ppl2.0

Usage: objdump coption(s)> <file(s)>
Display information from object <file(s)>
At least one of the following switches must be given:
-a, --archive-headers
-f, --file-headers
-f, --file-headers
-f, --file-headers
-f, --provate-headers
-f, --provate-headers
-f, --provate-headers
-f, --provate-headers
-f, --file-headers
-f, --file
```

objdump -f -a-ppl2.o:

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```
rohan@rohanlaptop:-/yashppl$ objdump -f a-ppl2.0

a-ppl2.0: file format elf64-x86-64
architecture: l386:x86-64, flags 0x00000011:
HAS_RELOC, HAS_SYMS
start address 0x0000000000000000
rohan@rohanlaptop:-/yashppl$
```

objdump -x -a-ppl2.o:-

```
rohan@rohanlaptop: ~/yashppl
                                                                                                                 Q = - 0
rohan@rohanlaptop:~/yashppl$ objdump -x a-ppl2.o
           file format elf64-x86-64
a-ppl2.o:
Sections:
                 Idx Name
0 .text
 1 .data
 2 .bss
SYMBOL TABLE:
                  df *ABS* 000000000000000 ppl2.c
d .text 000000000000000 .text
F .text 000000000000032 func
*UND* 0000000000000000 _stack_chk_fail
F .text 000000000000032 main
000000000000000000 g
0000000000000032 q
RELOCATION RECORDS FOR [.text]:
OFFSET TYPE
000000000000000000000 R_X86_64_PLT32
00000000000000000000000000 R_X86_64_PLT32
```

objdump -disassemble-all ppl2.o:

```
rohan@rohanlaptop: ~/yashppl
                                                                                                                                                                                                                                                 Q = -
 rohan@rohanlaptop:~/washpplS obidump --disassemble-all a-ppl2.o
a-ppl2.o: file format elf64-x86-64
Disassembly of section .text:
0000000000000000 <func>:
              53 0f 1e fa
55
48 89 e5
48 83 ec 20
64 48 8b 04 25 28 00
   0:
4:
5:
8:
c:
13:
15:
19:
1b:
                                                               endbr64
                                                                            4
%rbp
%rsp,%rbp
$0x20,%rsp
%fs:0x28,%rax
                                                               push
                                                               mov
                                                               sub
mov
              64 48 8b 04 25 28 00
00 00
48 89 45 f8
31 c0
90
48 8b 45 f8
64 48 2b 04 25 28 00
00 00
74 05
88 00 00 00 00
c9
                                                                             %rax,-0x8(%rbp)
%eax,%eax
                                                               хог
                                                                             -0x8(%rbp),%rax
%fs:0x28,%rax
                                                               sub
   20:
27:
29:
2b:
30:
                                                               je
call
leave
                                                                             30 <func+0x30>
30 <func+0x30>
 0000000000000032 <main>:
32: f3 Of 1e fa
36: 55
                                                              endbr64

push %rbp
mov %rsp,%rbp
sub $0x10,%rsp
movl $0x5,-0xc(%rbp)
movl $0x7,-0x8(%rbp)
movl $0x8,-0x4(%rbp)
mov $0x0,%eax
call $d <main+0x2b>
mov $0x0,%eax
leave
              37:
3a:
3e:
45:
4c:
53:
58:
5d:
                                                               mov
leave
```

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```
Disassembly of section .eh frame:
 0000000000000000 <.eh_frame>:
                                                                                                                                                                 $0x0,%al
%al,(%rax)
%al,(%rax)
%al,(%rax)
%edi,0x52(%rdx)
%al,(%rcx)
If <.eh_frame+0x1f>
%ebx,(%rbx)
$0x7,%al
%dl,0x1c000001(%rax)
%al.(%rax)
                               14 00
00 00
00 00
00 00
01 7a 52
                                                                                                                                     adc
add
add
add
add
add
       01 78 32
00 01
78 10
01 1b
0c 07
08 90 01 00 00 1c
                                                                                                                                     js
add
                                                                                                                                                                $9X,%al

%dl,0x1c000001(%rax)

%al,(%rax)

%al,(%rax)

%al,(%rax)

%al,(%rax)

%al,(%rax)

%al,(%rax)

%al,(%rax)

%al,0xe(%rbp)

%al,0xe(%rbp)

$0x8,(%rdi,%rax,1),%ecx

$0x0,%al

%al,(%rax)

%al,0xe(%rbp)

%al,0x60d4302(%rsi)

$0x8,(%rdi,%rax,1),%ecx
                               00 00
00 1c 00
00 00
00 00
00 00
                                                                                                                                     add
add
add
add
add
                               xor
add
add
                                                                                                                                      adc
imul
sbb
add
                                                                                                                                     cmp
add
add
add
xor
add
     40: 00 00

42: 00 00

44: 32 00

46: 00 00

48: 00 45 00

4b: 10 86 02 43 0d 06

51: 69 0c 07 08 00 00 00

ohan@rohanlaptop:-/yashppl$
                                                                                                                                      adc
imul
```