

Training Neural Networks on Embedded Devices

Targeting Embedded Environments

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Outline

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- Design
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Neural Network Applications on Embedded Devices

- Embedded Linux
- Federated Learning



Build Systems

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Yocto



Developing ARM Boards

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■ Shown on the second and the third slide.

Shown on all slides.

You get fine-grain control over which elements are visible at each time.



Developing ARM Boards

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Developing ARM Boards

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Developing ARM Boards

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You get fine-grain control over which elements are visible at each time.



Using TikZ for Drawings

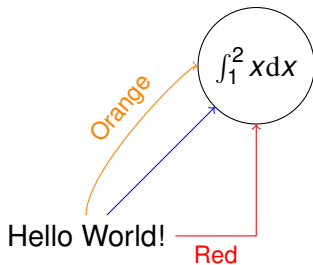
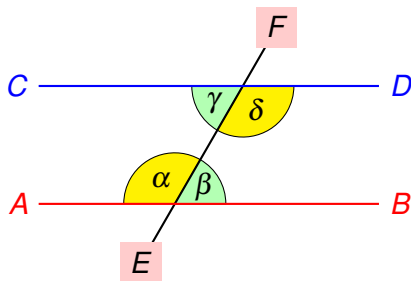
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Using TikZ for Petri-Net

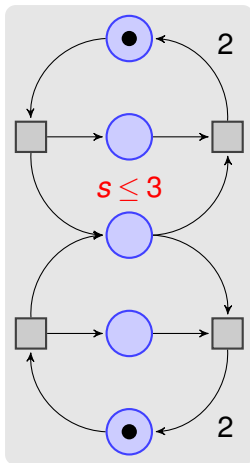
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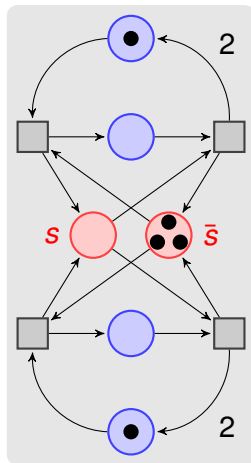
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replacement
of the **capacity**
by **two places**





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Different implementations



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Add the Algorithm



C based HDR-NN

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Add some code maybe



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