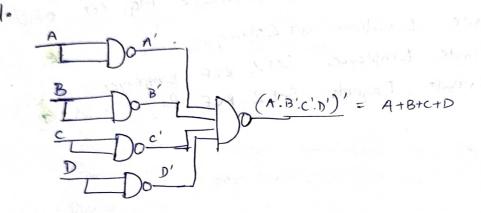
UIT2304 - Digital Logic & Computer Organization

Assignment -1

Deepitha p 3122225002028 IT-A

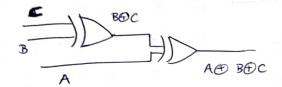


It can be implemented using one 3 input And gate

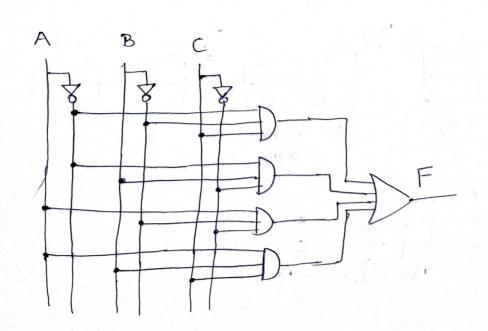
3. ABC 01-01-01-01

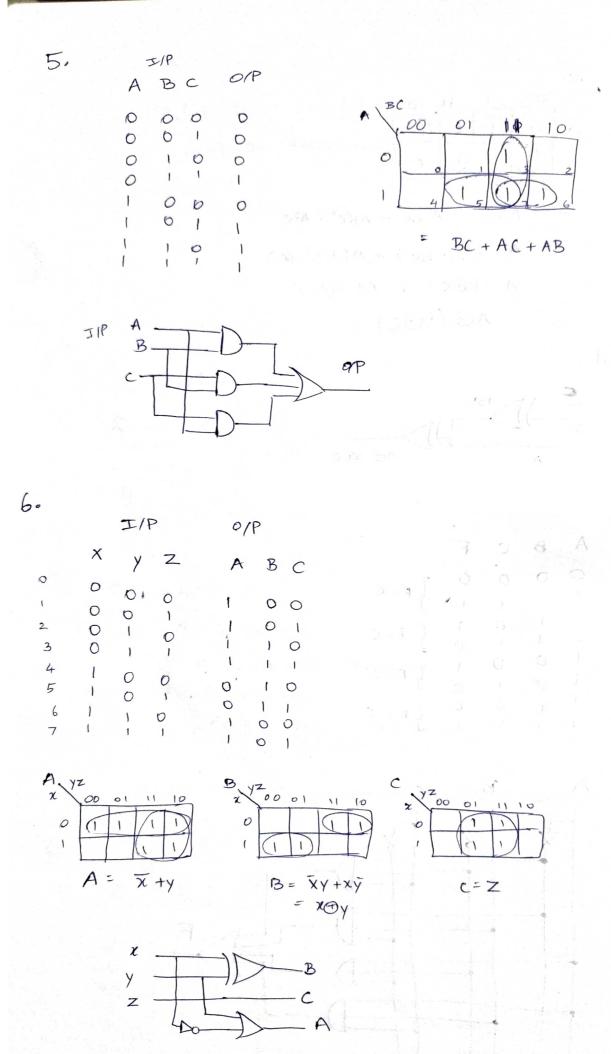
K-Map.

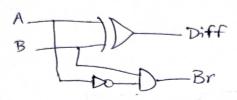
Circuit:



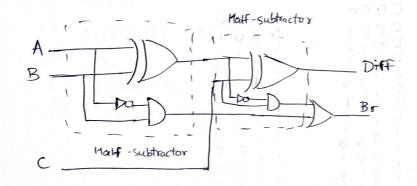
4







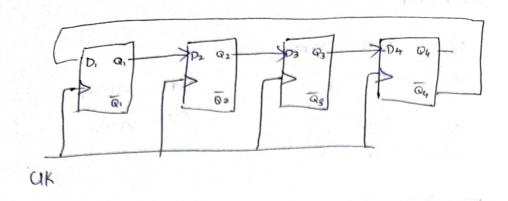
Full Subtractor:



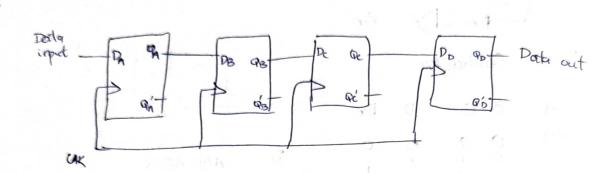
8.

CIK CIK	Q _i	Q2	93	Q4	
0	0	0	0	0	
A Lo	1	0	0	0	
2	1	1	0	0	.45*
3	1		1	0	
4	1	1		· · · · · · · · · · · · · · · · · · ·	
5	0			1 1	
6	0	C		1	1
7	. c) (D	0	1

Inverted output Q' of last flipflop is passed as input to the first flipflop.



9.



10.

Decimal No.	Binary ABCD	Gray Code G116263614
01734567890116345	0 00000000	000000000000000000000000000000000000000

EN = (AGE)C + AB

$$\begin{array}{c|c}
A & G_{14} \\
B & G_{13} \\
C & G_{12} \\
D & G_{11}
\end{array}$$

W NO 10

$$G_{14} = C'D + cD' = cOD$$