MAHESHWARI PATHANGI

Student-Electronics and Communication Engineering

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OBJECTIVE :

A highly motivated and detail-oriented Electronics and Communication graduate seeking a Software Engineer role to apply my programming skills and passion for problem-solving. Eager to contribute to innovative projects and grow in a dynamic development environment.

EDUCATION:

B V Raju Institute of Technology , Narsapur **2021-2025**
Bachelor of technology in Electronics and Communication Engineering. CGPA-8.4

SV Junior College , Sangareddy **2019-2021**
Intermediate education in MPC Score-97%

Vishnu High School , Narsapur **2018-2019**
SSC GPA-9.3

PROJECT:

➤Image recognition from Input Data

- Objective: Developed a Verilog-based system for image detection.
- Role: Solely responsible for RTL design, simulation, and verification.
- Tools Used: Verilog - Xilinx Vivado.
- Outcome: Successfully demonstrated image detection with high accuracy.

➤Power Optimization of RISC-V Processor

- Objective: Reduced power consumption in a RISC-V processor through architectural optimizations.
- Role: Led the design and verification phases, focusing on RTL coding and simulation.
- Tools Used: Synopsys Design Compiler, Synopsys ICCII
- Outcome: Achieved 15% lower power consumption using low-power design techniques.

EXPERIENCE:

★ **Embedded System Developer Virtual Internship-Microchip** 06/2024-09/2024
Worked on microcontroller interfacing, peripheral configuration, and Embedded C programming for real-time applications.

★ **AI-ML Virtual Internship - Google for Developers (AICTE)** 01/2024-03/2024
Developed NLP models and machine learning solutions using Python and TensorFlow.

CERTIFICATIONS:

✦ VLSI SoC Design using Verilog HDL -Issued by **MAVEN SILICON**

✦ VLSI for Beginners -Issued by **NIELIT Calicut**

✦ LOR From ENTREPRENEURSHIP DEVELOPMENT CELL-Issued by **IIT DELHI**

✦ Python & JavaScript -Issued by **HACKERRANK**

✦ C Programming - Issued by **GREAT LEARNING**

SKILLS:

● **Technical Skills:** Python, C, JavaScript , HTML , CSS, Java

● **EDA Tools:** Synopsys Design Compiler, Synopsys IC Compiler , Xilinx Vivado

● **Hardware Description Languages:** Verilog, System verilog

● **VLSI Design:** ASIC Design Flow (RTL to GDSII), Physical Design, low Power Design

● **Design & Simulation Tools:** Microwind , MATLAB, Turbo C

● **Documentation Tools:**MS Word , Google Sheets

● **Communication Skills:** Skilled in Problem Solving , presenting ideas and team collaboration.

EXTRACURRICULAR ACTIVITIES:

➤ Student Member of Center for VLSI Design (CVD) laboratory

➤ I am proficient in badminton and chess, and I actively engage in both indoor and outdoor games

ACHIEVEMENT:

➤ Selected for Samsung Fellowship Cohort 4 of ISWDP