This document explains each option available in the memory compiler. The next sections explain the options and the ranges for Single Port. The user also has the option of selecting two redundant quad-rows, two redundant Columns (IO’s) or both for the Single Port. The compiler is capable of generating the fuse box logic as well if the user wishes to use the redundancy feature. The compiler comes complete with tool tips for ease of use and is capable of providing suggestions for the most optimal configuration based on input criteria specified by the user.