

1. Description

1.1. Project

Project Name	STM32F103VCT6_LCD4bit
Board Name	custom
Generated with:	STM32CubeMX 6.7.0
Date	04/05/2023

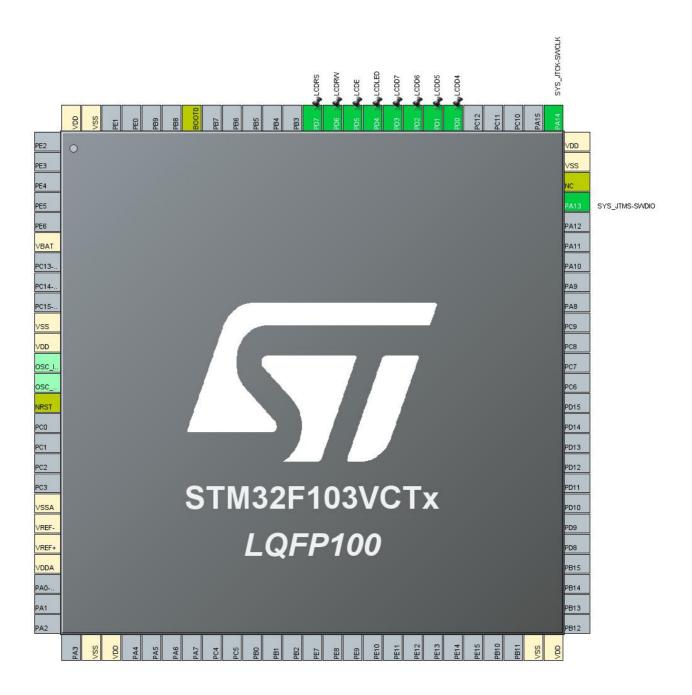
1.2. MCU

MCU Series	STM32F1
MCU Line	STM32F103
MCU name	STM32F103VCTx
MCU Package	LQFP100
MCU Pin number	100

1.3. Core(s) information

Core(s)	Arm Cortex-M3

2. Pinout Configuration

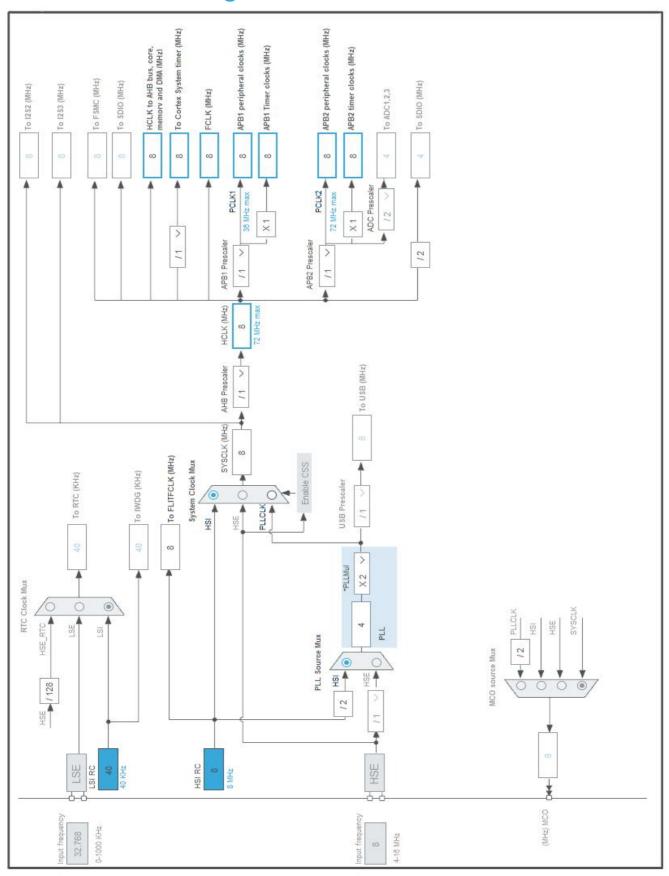


3. Pins Configuration

Pin Number LQFP100	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
6	VBAT	Power		
10	VSS	Power		
11	VDD	Power		
14	NRST	Reset		
19	VSSA	Power		
20	VREF-	Power		
21	VREF+	Power		
22	VDDA	Power		
27	VSS	Power		
28	VDD	Power		
49	VSS	Power		
50	VDD	Power		
72	PA13	I/O	SYS_JTMS-SWDIO	
73	NC	NC		
74	VSS	Power		
75	VDD	Power		
76	PA14	I/O	SYS_JTCK-SWCLK	
81	PD0 *	I/O	GPIO_Output	LCDD4
82	PD1 *	I/O	GPIO_Output	LCDD5
83	PD2 *	I/O	GPIO_Output	LCDD6
84	PD3 *	I/O	GPIO_Output	LCDD7
85	PD4 *	I/O	GPIO_Output	LCDLED
86	PD5 *	I/O	GPIO_Output	LCDE
87	PD6 *	I/O	GPIO_Output	LCDRW
88	PD7 *	I/O	GPIO_Output	LCDRS
94	BOOT0	Boot		
99	VSS	Power		
100	VDD	Power		

^{*} The pin is affected with an I/O function

4. Clock Tree Configuration



5. Software Project

5.1. Project Settings

Name	Value	
Project Name	STM32F103VCT6_LCD4bit	
Project Folder	Y:\COURSE - THUCTAP VI DIEU KHIEN ARM\STM32F103VCT6_LCD4bit	
Toolchain / IDE	MDK-ARM V5.32	
Firmware Package Name and Version	STM32Cube FW_F1 V1.8.4	
Application Structure	Advanced	
Generate Under Root	No	
Do not generate the main()	No	
Minimum Heap Size	0x200	
Minimum Stack Size	0x400	

5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	Yes
consumption)	
Enable Full Assert	No

5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO

6. Power Consumption Calculator report

6.1. Microcontroller Selection

Series	STM32F1
Line	STM32F103
MCU	STM32F103VCTx
Datasheet	DS5792_Rev12

6.2. Parameter Selection

Temperature	25
Vdd	3.3

6.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

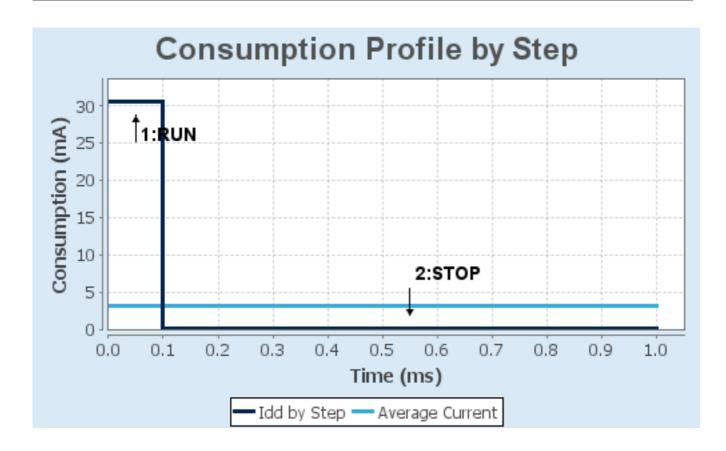
6.4. Sequence

	I	
Step	Step1	Step2
Mode	RUN	STOP
Vdd	3.3	3.3
Voltage Source	Battery	Battery
Range	No Scale	No Scale
Fetch Type	FLASH	n/a
CPU Frequency	72 MHz	0 Hz
Clock Configuration	HSE PLL	Regulator LP
Clock Source Frequency	8 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	30.5 mA	25 μΑ
Duration	0.1 ms	0.9 ms
DMIPS	90.0	0.0
Ta Max	100.37	105
Category	In DS Table	In DS Table

6.5. Results

Sequence Time	1 ms	Average Current	3.07 mA
Battery Life	1 month, 15 days,	Average DMIPS	61.0 DMIPS
	15 hours		

6.6. Chart



7. Peripherals and Middlewares Configuration

7.1. RCC

7.1.1. Parameter Settings:

System Parameters:

VDD voltage (V) 3.3
Prefetch Buffer Enabled

Flash Latency(WS) 0 WS (1 CPU cycle)

RCC Parameters:

HSI Calibration Value 16
HSE Startup Timout Value (ms) 100
LSE Startup Timout Value (ms) 5000

7.2. SYS

Debug: Serial Wire

Timebase Source: SysTick

^{*} User modified value

8. System Configuration

8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
SYS	PA13	SYS_JTMS- SWDIO	n/a	n/a	n/a	
	PA14	SYS_JTCK- SWCLK	n/a	n/a	n/a	
GPIO	PD0	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LCDD4
	PD1	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LCDD5
	PD2	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LCDD6
	PD3	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LCDD7
	PD4	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LCDLED
	PD5	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LCDE
	PD6	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LCDRW
	PD7	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LCDRS

8.2. DMA configuration

nothing configured in DMA service

8.3. NVIC configuration

8.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority	
Non maskable interrupt	true	0	0	
Hard fault interrupt	true	0	0	
Memory management fault	true	0	0	
Prefetch fault, memory access fault	true	0	0	
Undefined instruction or illegal state	true	0	0	
System service call via SWI instruction	true	0	0	
Debug monitor	true	0	0	
Pendable request for system service	true	0	0	
System tick timer	true	15	0	
PVD interrupt through EXTI line 16	unused			
Flash global interrupt	unused			
RCC global interrupt	unused			

8.3.2. NVIC Code generation

Enabled interrupt Table	Select for init	Generate IRQ	Call HAL handler
	sequence ordering	handler	
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Prefetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	true	false
Debug monitor	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true

^{*} User modified value

9. System Views

9.1. Category view

9.1.1. Current

Middleware					
System Core	Analog	Timers	Connectivity	Multimedia	Computing
DMA					
GPIO ❤					
NVIC 🔗					
RCC ♥					
sys 📀					

10. Docs & Resources

Type Link

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IBIS models https://www.st.com/resource/en/ibis_model/stm32ibis.zip

System View https://www.st.com/resource/en/svd/stm32f1_svd.zip

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