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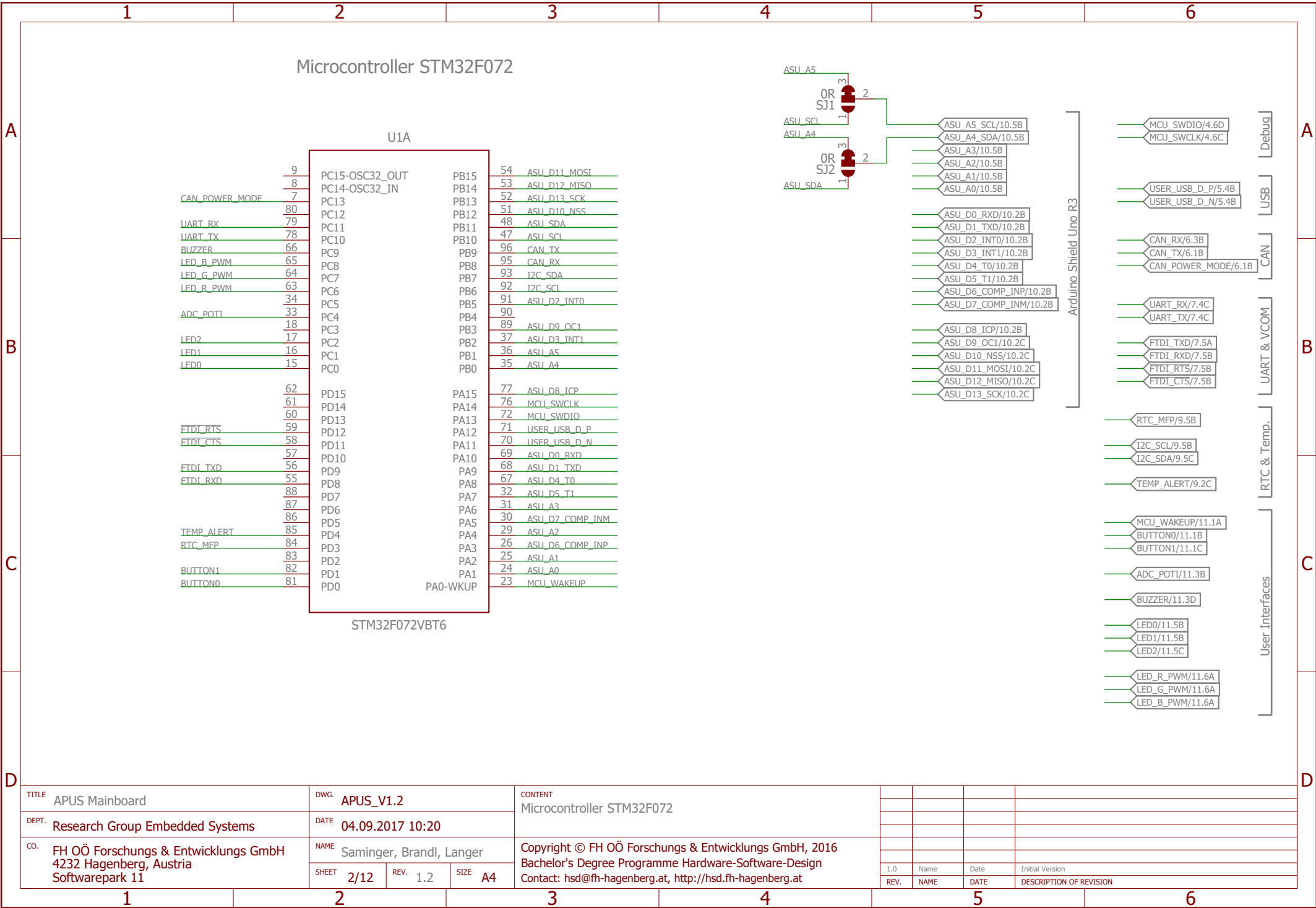
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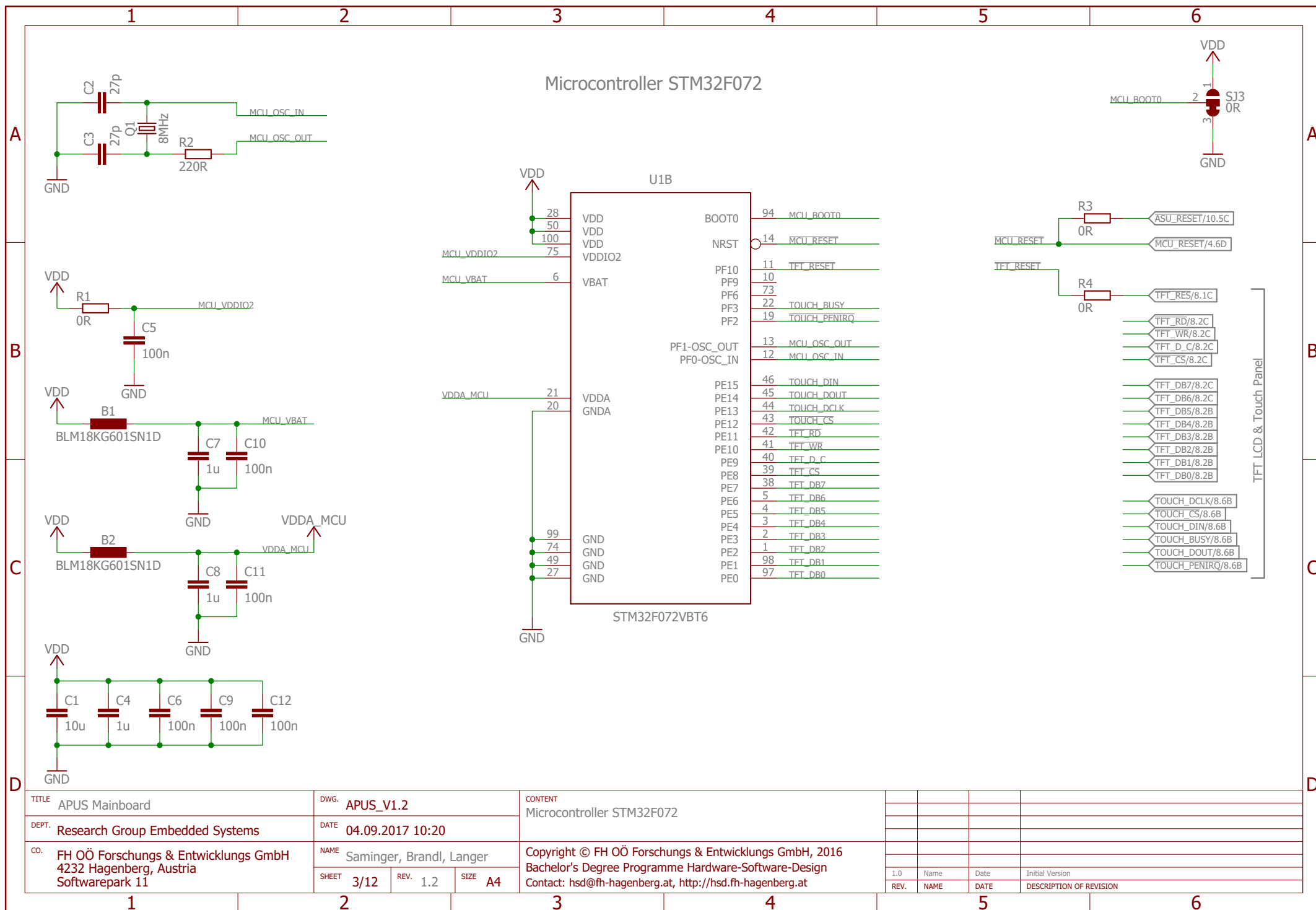
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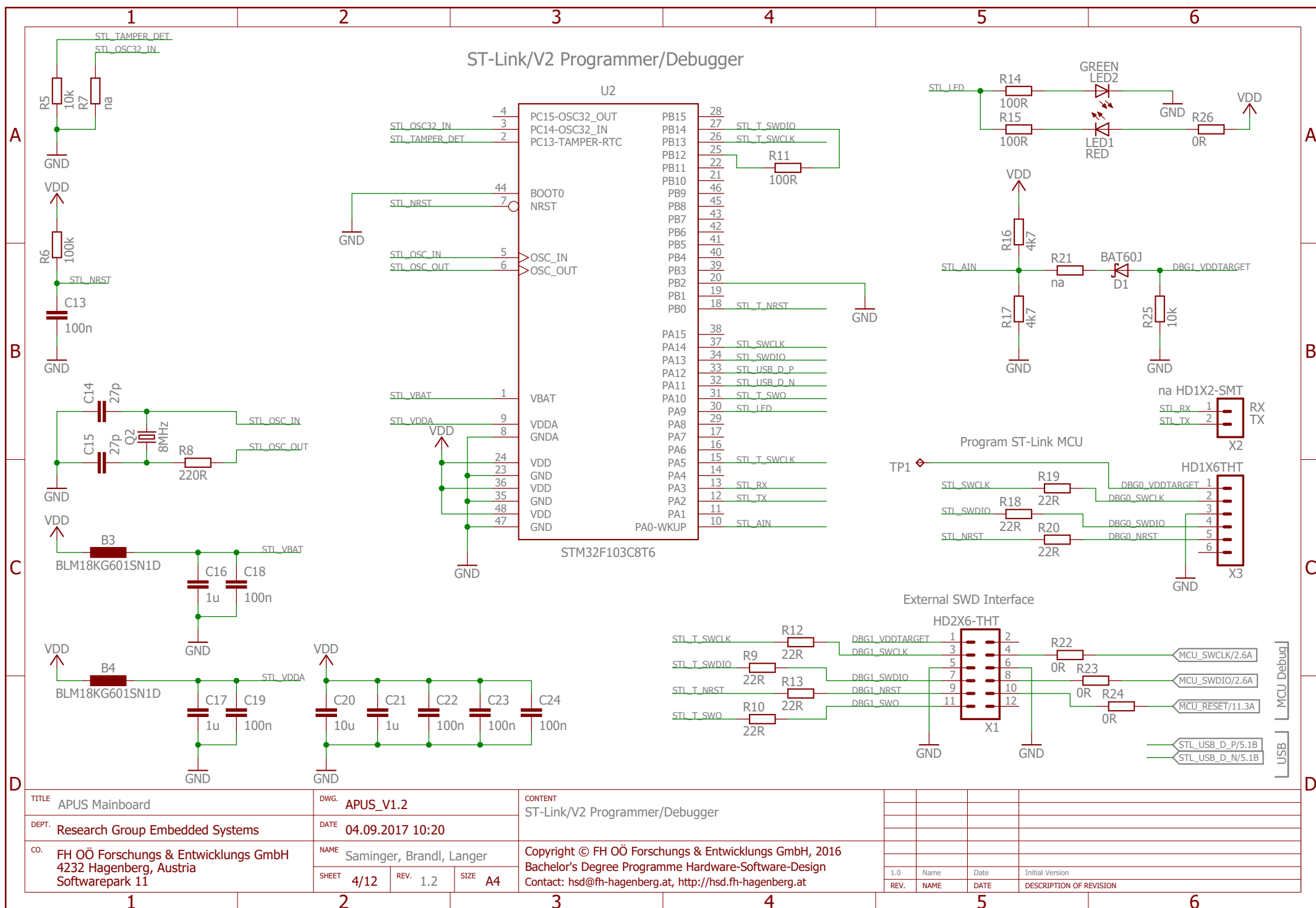
Power  
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TITLE	APUS Mainboard	DWG.	APUS_V1.2	CONTENT	Overview				
DEPT.	Research Group Embedded Systems	DATE	04.09.2017 10:20						
CO.	FH OÖ Forschungs & Entwicklungs GmbH 4232 Hagenberg, Austria Softwarepark 11	NAME	Saminger, Brandl, Langer	Copyright © FH OÖ Forschungs & Entwicklungs GmbH, 2016 Bachelor's Degree Programme Hardware-Software-Design Contact: hsd@fh-hagenberg.at, http://hsd.fh-hagenberg.at					
		SHEET	1/12	REV.	1.2	SIZE	A4		
								1.0	Name
								DATE	Initial Version
								DESCRIPTION OF REVISION	



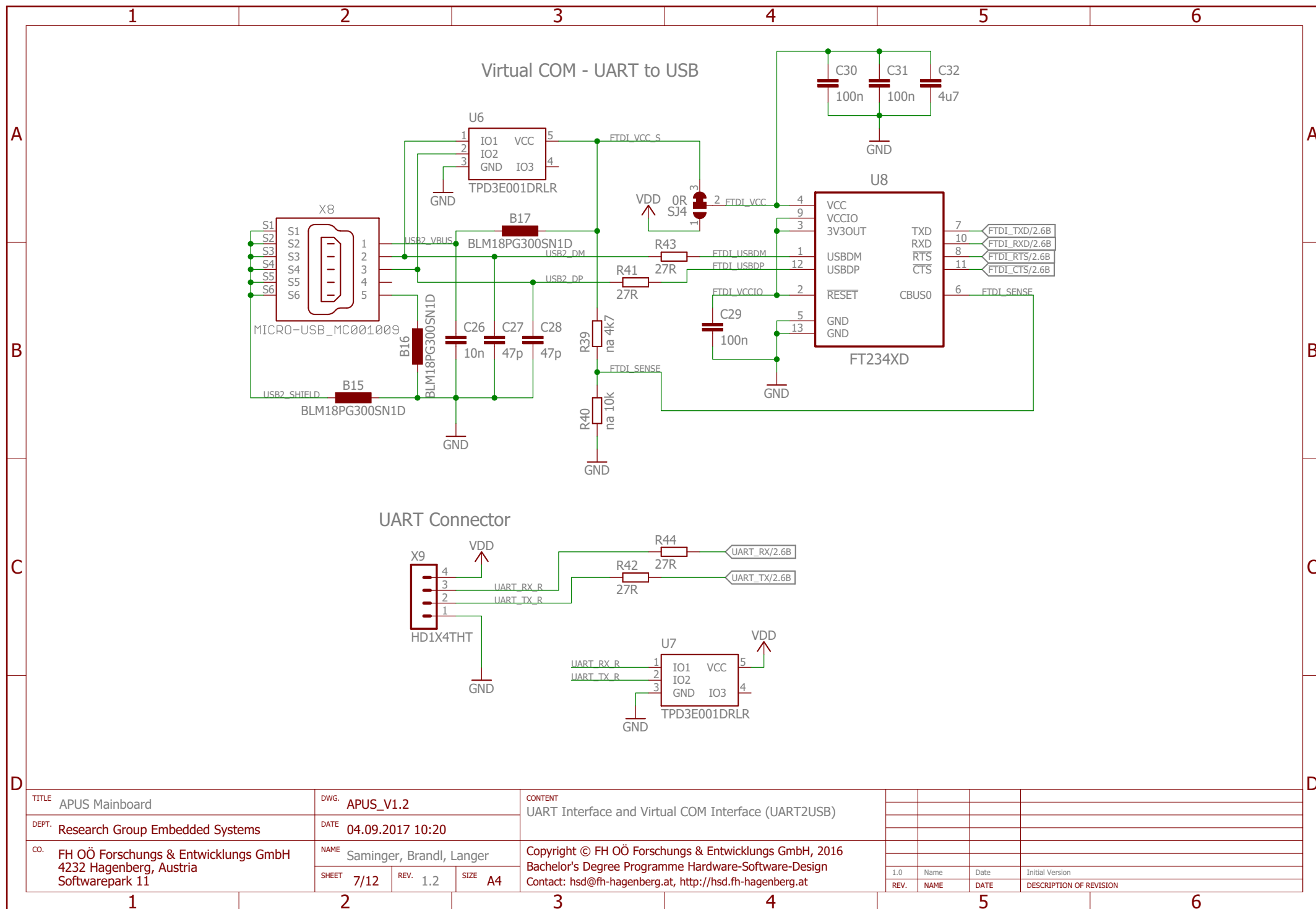


TITLE APUS Mainboard DEPT. Research Group Embedded Systems CO. FH OÖ Forschungs & Entwicklungs GmbH 4232 Hagenberg, Austria Softwarepark 11	DWG. APUS_V1.2		CONTENT Microcontroller STM32F072					
	DATE 04.09.2017 10:20							
	NAME Saminger, Brandl, Langer		Copyright © FH OÖ Forschungs & Entwicklungs GmbH, 2016 Bachelor's Degree Programme Hardware-Software-Design Contact: hsd@fh-hagenberg.at, http://hsd.fh-hagenberg.at					
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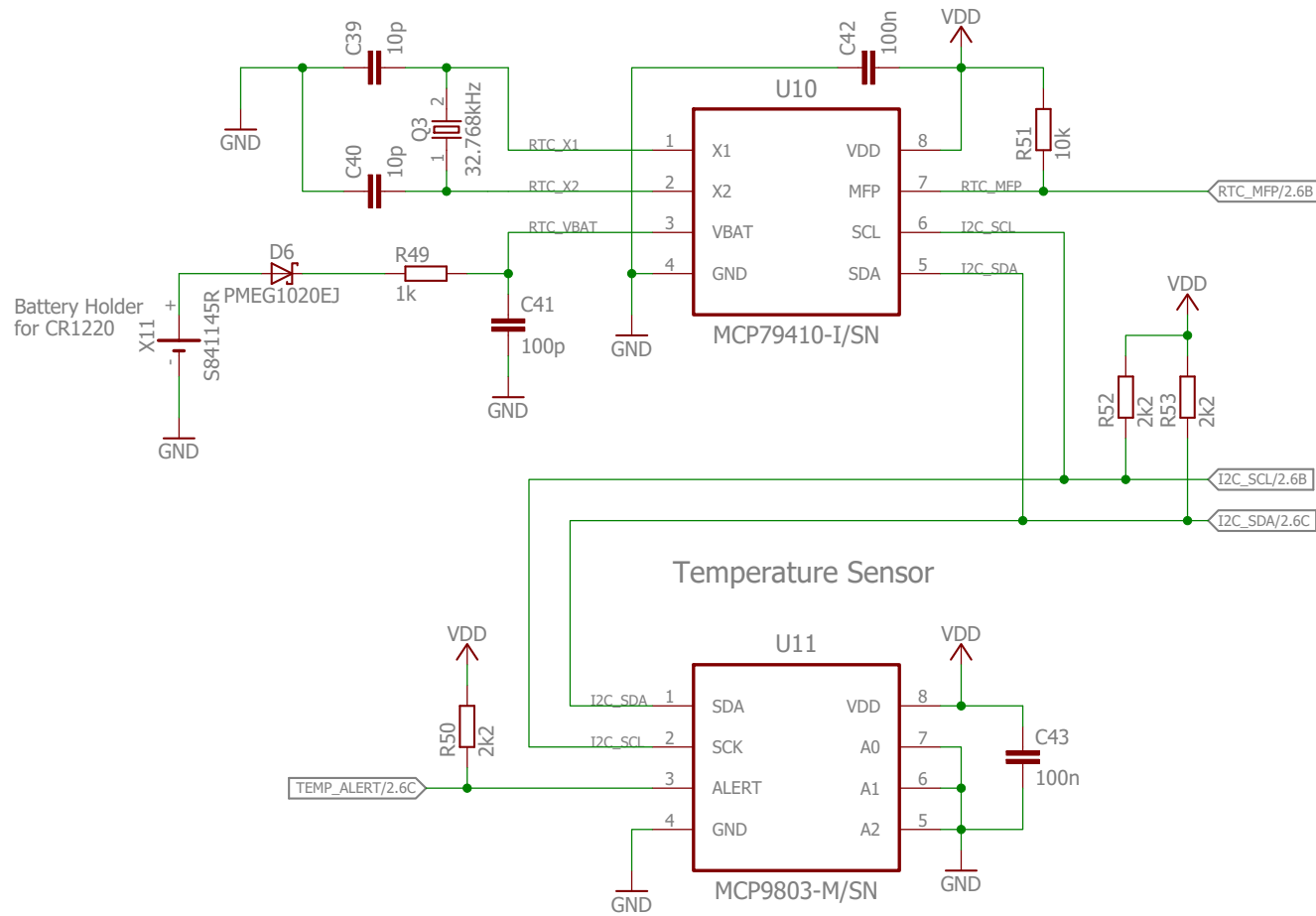








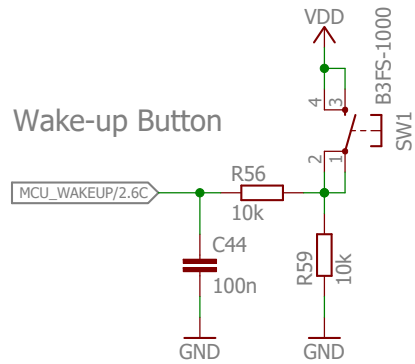
# Real Time Clock with EEPROM and SRAM



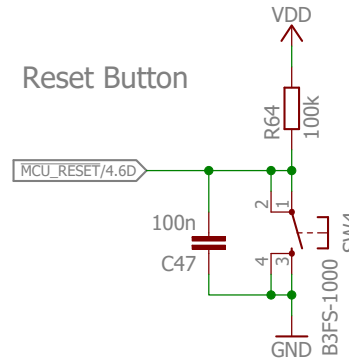
TITLE	APUS Mainboard			DWG.	APUS_V1.2			CONTENT							
DEPT.	Research Group Embedded Systems			DATE	04.09.2017 10:20			Temperature Sensor and Real Time Clock							
CO.	FH OÖ Forschungs & Entwicklungs GmbH 4232 Hagenberg, Austria Softwarepark 11			NAME			Copyright © FH OÖ Forschungs & Entwicklungs GmbH, 2016 Bachelor's Degree Programme Hardware-Software-Design Contact: hsd@fh-hagenberg.at, http://hsd.fh-hagenberg.at								
				Saminger, Brandl, Langer											
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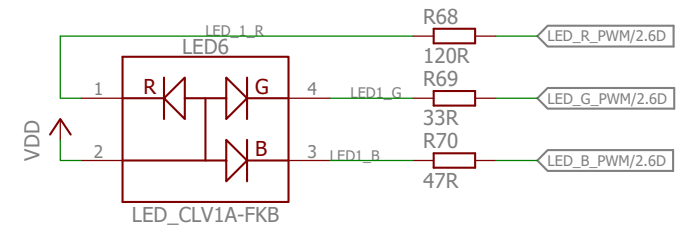
Wake-up Button



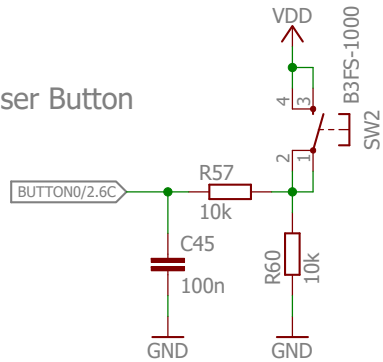
Reset Button



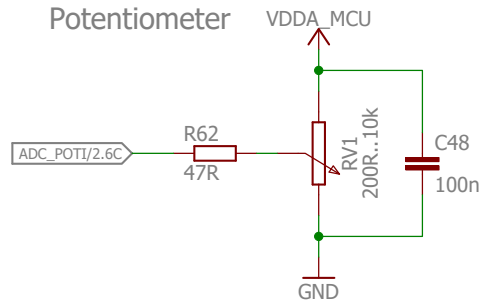
RGB LED



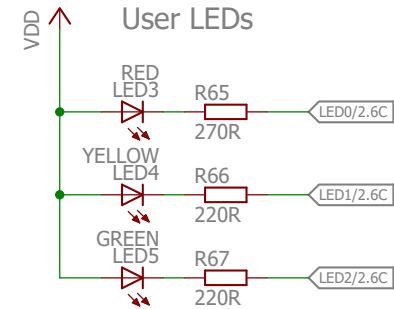
User Button



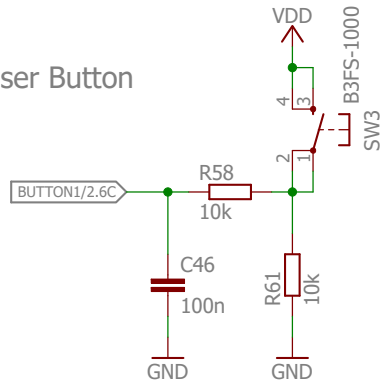
Potentiometer



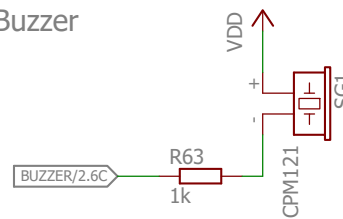
User LEDs



User Button



Buzzer



TITLE APUS Mainboard	DWG. APUS_V1.2	CONTENT LEDs, Buttons, Buzzer and Potentiometer	
DEPT. Research Group Embedded Systems	DATE 04.09.2017 10:20		
CO. FH OÖ Forschungs & Entwicklungs GmbH 4232 Hagenberg, Austria Softwarepark 11	NAME Saminger, Brandl, Langer	Copyright © FH OÖ Forschungs & Entwicklungs GmbH, 2016 Bachelor's Degree Programme Hardware-Software-Design Contact: hsd@fh-hagenberg.at, http://hsd.fh-hagenberg.at	
	SHEET 11/12		
	REV. 1.2	SIZE A4	

