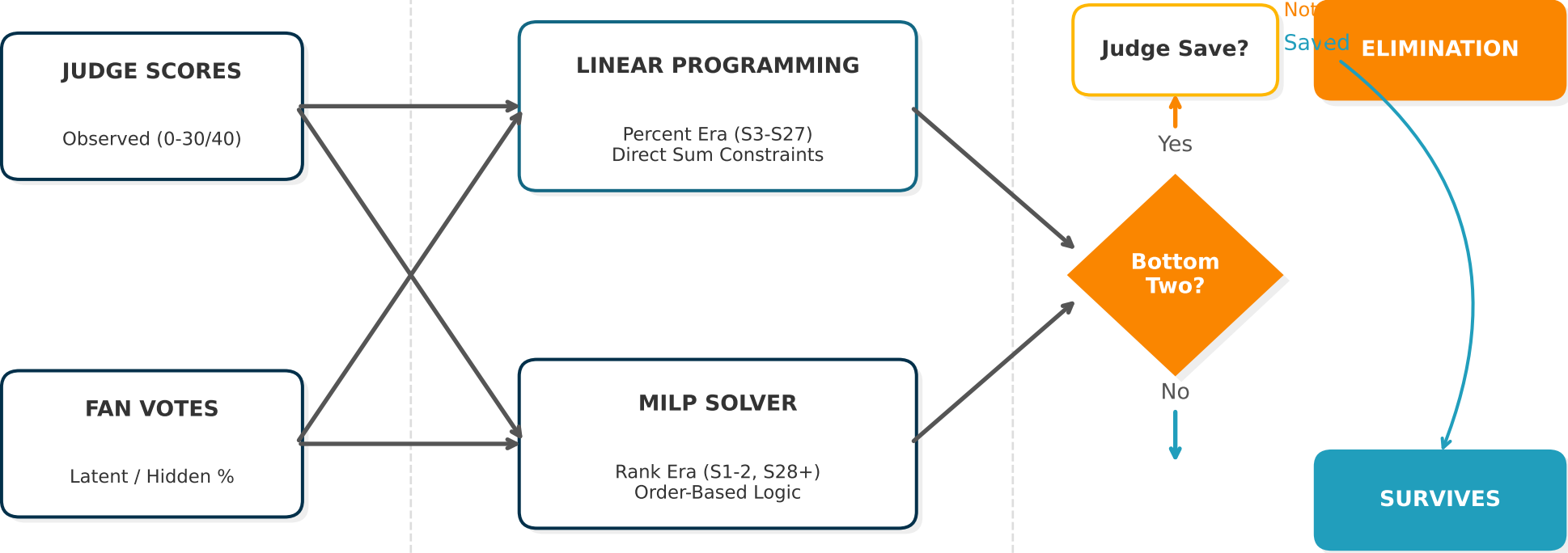


PHASE 1: DATA INPUTS

PHASE 2: DUAL-CORE ENGINE

PHASE 3: AUDIT & OUTCOME



Dual-Core Inversion Engine detects mismatches $(S^*) > 0$