

A

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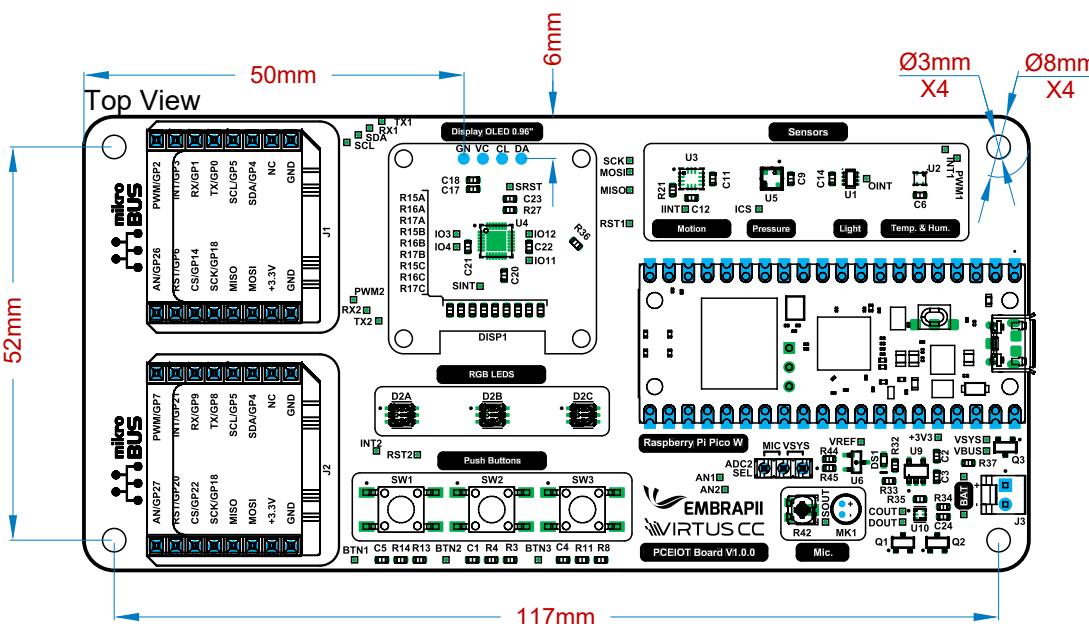
G

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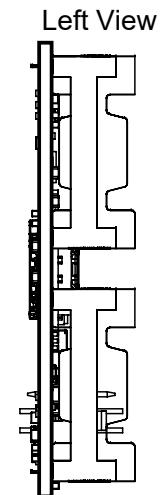
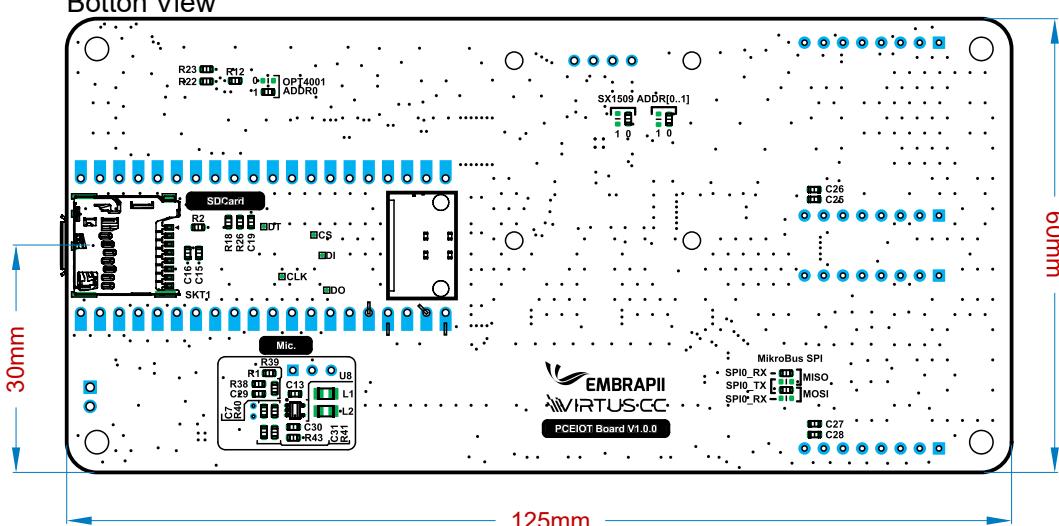
# PCEIoT V1.0.0

Variant: Fabrication

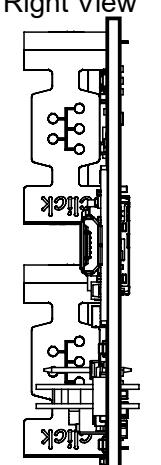
## Board view



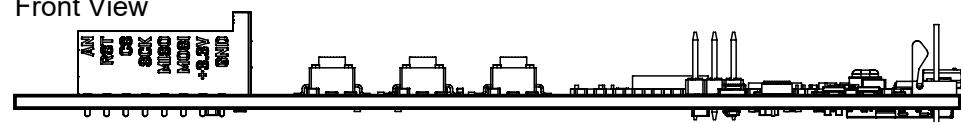
Bottom View



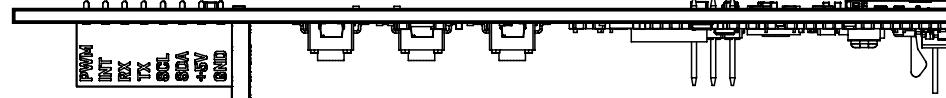
Right View



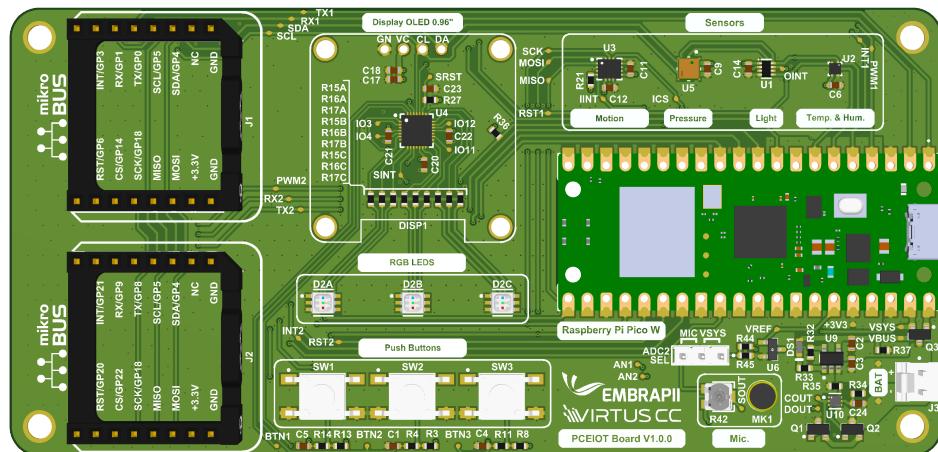
Front View



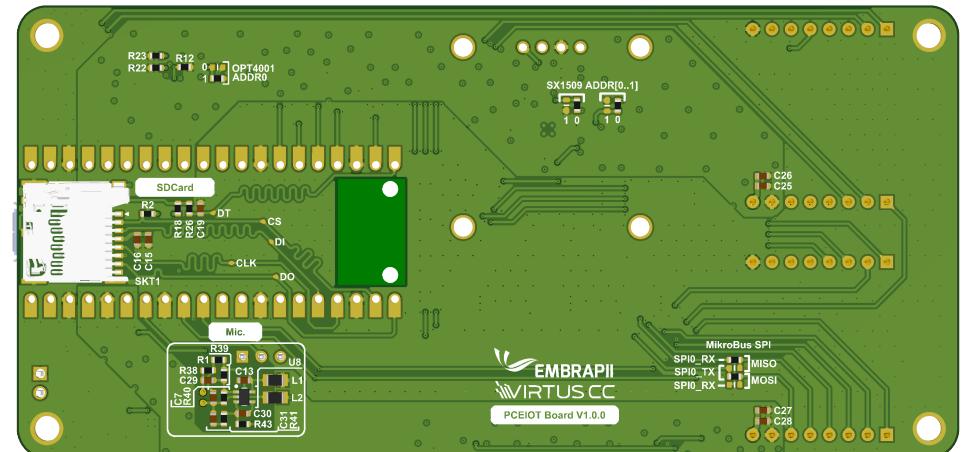
Back View



Realistic View



Realistic View



**VIRTUS CC**

ADDRESS  
R. Aprígio Veloso, 882, Universitário  
Campina Grande - PB  
58429-900

PROJECT  
PCEIoT

VERSION  
V1.0.0

TITLE  
DemetrioGurgel - PCEIoT.PCDBwf

SIZE  
A3

SCALE  
1:1

SHEET:  
1 OF 3

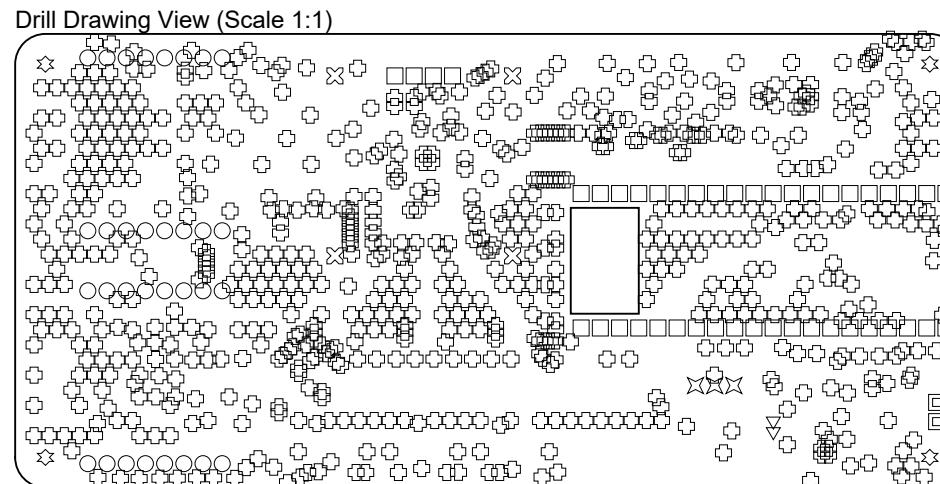
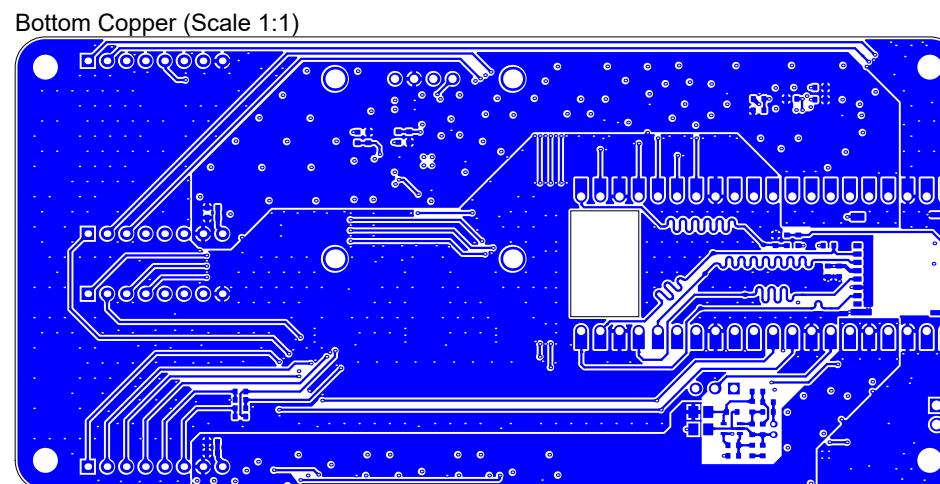
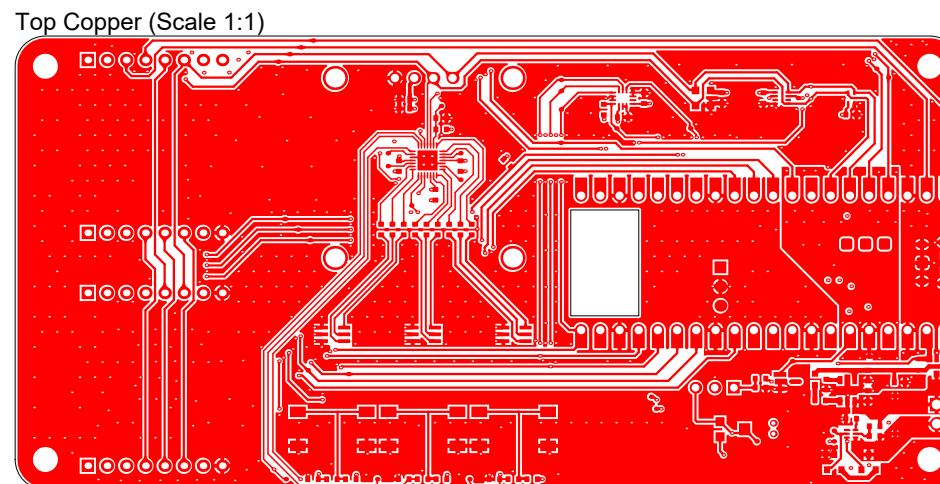
DOCUMENT NAME: DemetrioGurgel - PCEIoT.PCDBwf

DOCUMENT REVISION: Not in version control

PROJECT REVISION: Not in version control

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						REVISION	DESCRIPTION	DATE	APPROVED

# Layers, Stack and Drill Drawings



Layer Stack Legend

Material	Layer	Thickness	Dielectric Material	Type	Gerber
Top Overlay				Legend	GTO
Surface Material	Top Mask	0.030mm	Solder Resist	Solder Mask	GTS
Copper	Top Copper	<b>0.035mm</b>		Signal	GTL
Prepreg		0.210mm	7628	Dielectric	
Copper	Top Ref	<b>0.015mm</b>		Internal Plane	GP1
Core		1.065mm	FR-4	Dielectric	
Copper	Bottom Ref	<b>0.015mm</b>		Internal Plane	GP2
Prepreg		0.210mm	7628	Dielectric	
Copper	Bottom Copper	<b>0.035mm</b>		Signal	GBL
Surface Material	Bottom Mask	0.030mm	Solder Resist	Solder Mask	GBS
	Bottom Overlay			Legend	GBO

Total thickness: 1.647mm

Drill Table

Symbol	Count	Hole Size	Via / Pad	Drill Layer Pair
+	728	0.200mm	Via	Top Copper - Bottom Copper
▽	2	0.550mm	Pad	Top Copper - Bottom Copper
○	32	0.889mm	Pad	Top Copper - Bottom Copper
✗	3	1.020mm	Pad	Top Copper - Bottom Copper
□	44	1.100mm	Pad	Top Copper - Bottom Copper
□	2	1.300mm	Pad	Top Copper - Bottom Copper
✗	4	2.500mm	Via	Top Copper - Bottom Copper
✗	4	3.300mm	Via	Top Copper - Bottom Copper
819 Total				

	ADDRESS R. Aprígio Veloso, 882, Universitário Campina Grande - PB 58429-900	VERSION	
		V1.0.0	1:1
PROJECT PCEIoT	TITLE DemetrioGurgel - PCEIoT.PCBDwf	SIZE A3	SCALE 1:1
FILE NAME: DemetrioGurgel - PCEIoT.PCBDwf	SHEET: 3 OF 3		
DOCUMENT REVISION: Not in version control	PROJECT REVISION: Not in version control		

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						REVISION	DESCRIPTION	DATE	APPROVED

# Bill of materials

1 Bill Of Materials

Designator	Name	Quantity
C1, C4, C5, C19	VJ0603Y103KXAAAC	4
C2, C3	Capacitor 4.7µF +/-20% 10V 0603	2
C6, C7, C9, C11, C12, C13, C14, C15, C16, C17, C18, C20, C21, C22, C23, C24, C25, C26, C27, C28, C29	VJ0603Y104MXJCW1BC	21
C30	C0603C101K5GACTU	1
C31	GRM188R60J106ME47D	1
D2A, D2B, D2C	ASMT-YTB7-0AA02	3
DS1	19-217/R6C-AL1M2VY/3T	1
J3	640456-2	1
L1, L2	BLM21PG331SN1D	2
MK1	CMEJ-0415-42-LP	1
P1	22-28-4033	1
Q1, Q2, Q3	NTRV4101PT1G	3
R1, R38, R41, R45	Resistor 1k +/-1% 0603 100 mW	4
R2, R3, R4, R8, R11, R12, R13, R14, R18, R21, R22, R23, R26, R27, R36, R37, R44	Resistor 10k +/-1% 0603 100 mW	17
R5, R9, R10, R30, R31	Resistor 0R +/- 5% 0603 100 mW	5
R15A, R15B, R15C	CRCW0603150RFKEA	3
R16A, R16B, R16C, R17A, R17B, R17C	CRCW060350R0FKEA	6
R32	ERJ-3EKF2001V	1
R33	CRCW0603470RJNEA	1
R34	ERJ-3EKF3300V	1
R35, R43	CRCW06032K20FKEA	2
R39, R40	Resistor 100k +/-1% 0603 100 mW	2
R42	TC33X-2-104E	1
SKT1	5033981892	1
SW1, SW2, SW3	FSM4JSMA	3
U1	OPT4001DTSR	1
U2	SHT40I-AD1B-R3	1
U3	ICM-42670-P	1
U4	SX1509BIULTRT	1
U5	MS563702BA03-50	1
U6	LM4040DYM3-2.5-TR	1
U7	SC0918	1
U8	MAX4466EXK-T	1
U9	MCP73831T-2ACI/OT	1
U10	BQ29700DSER	1

2 Bill Of Materials (NF)

Designator	Name	Quantity
DISP1	0.96" Display Module	0
J4, J5	61302011821	0
J6, J7, J8, J9	61300811821	0
M1, M2, M7, M8	709670110	0
M3, M4, M9, M10	970080354	0
M5, M6, M11, M12	MPMS 003 0005 PH	0
R6, R7, R19, R20, R24	Resistor 0R +/- 5% 0603 100 mW	0

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PROJECT	PCEIoT	VERSION	V1.0.0
TITLE	DemetrioGurgel - PCEIoT.PCBDwf	SIZE	A3
FILE NAME:	DemetrioGurgel - PCEIoT.PCBDwf	SCALE	1:1
DOCUMENT REVISION:	Not in version control	SHEET:	3 OF 3
PROJECT REVISION:	Not in version control		

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