PRODUCT SPECIFICATION FOR LCD MODULE

Revision: <u>00</u>

Model No: ATK43138

Module Type: COG+FPC+B/L+ST+TP

APPROVED SIGNATURE	

- □ Approved Product Specification only
- Approved Product Specification and Samples

Prepared By	Checked By	Approved By

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1. General Description

ATK43138 is a transmissive type a-Si TFT-LCD (amorphous silicon thin film transistor liquid crystal display) module, which is composed of a TFT-LCD panel, a driver circuit and a backlight unit. The panel size is 4.3 inch and the resolution is 800(RGB)*480, the panel can

display up to 16M colors. The LCM can be easily accessed by RGB interface.

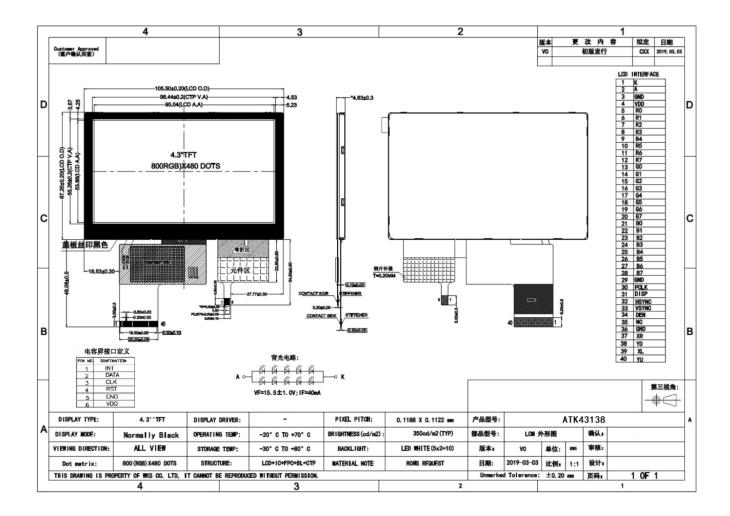
2. Physical Features

Diambay Mada	TFT-LCD Module
Display Mode	Active matrix TFT, Transmissive type
Display Format	Graphic 800×RGB×480 Dot-matrix
Input Data	24 bit RGB interface
Viewing Direction	6 O'clock

3. Mechanical Specification

Item	Contents	Unit
Module size (W×H×T)	105.40 × 67.10× 2.90	mm
Number of dots	800(RGB) × 480	
Active area (W×H)	95.04×53.86	mm

4. Outline Dimension



5. Absolute Maximum Ratings

Item	Symbol	Min	Max	Unit	Remark
Power Voltage	VCC	0.3	5.0	V	
Input Voltage	VIN	-0.3	5.0	V	Note1
Operating temperature	TOPR	-20	70	°C	Note2
Storage temperature	TSTR	-30	80	°C	
Humidity			90	%RH	

Remark:

Note 1) The driver IC may be permanently damaged if it is used under the condition exceeding the above absolute maximum values. It is also recommended to use the driver IC within the limit of its electric characteristics during normal operation. Exceeding the conditions may lead to malfunction of it and affect its credibility.

Note 2) The voltage from VSS.

6. Electrical Characteristics

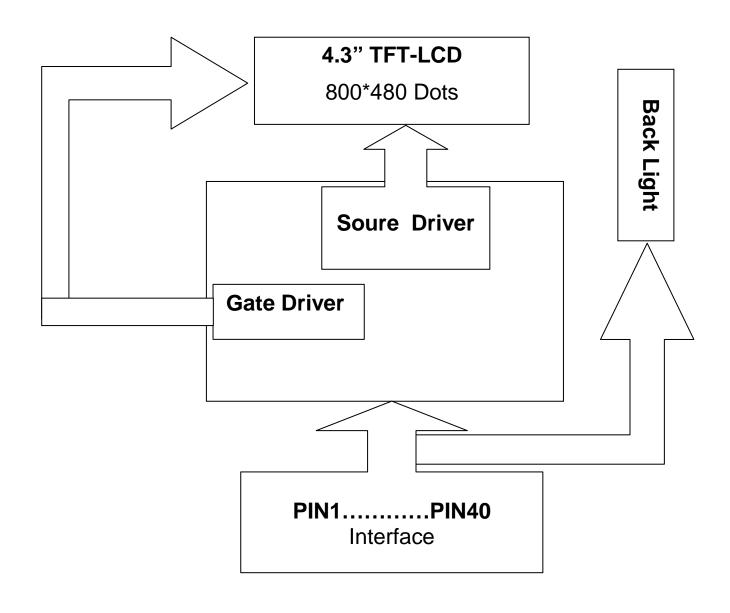
Item		Symbol	Symbol				Remark
item		Symbol	Min	Тур	Max	Unit	Remark
Power Voltage	Logic	VCC	3.0	3.3	3.6	V	Note1
Input Voltage	L level	VIL	GND		0.3*VCC	V	VCC=3.0
input voltage	H level	VIH	0.7* VCC		VCC	V	~ 3.6V
LCD Drive P		ILCD			24	mA	VCC=3.3V

Remark:

Note1:Vcom must be adjusted to optimize display quality: Cross-talk, Contrast Ratio and etc.

7. Module Function Description

7-1. Block Diagram Of LCM



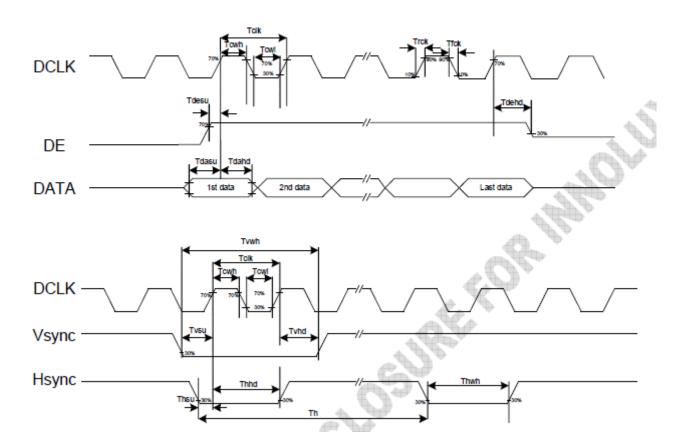
7-2. Pin Description

PIN NO.	Symbol	I/O	Description
1	К	Р	Power for LED backlight cathode
2	А	Р	Power for LED backlight anode

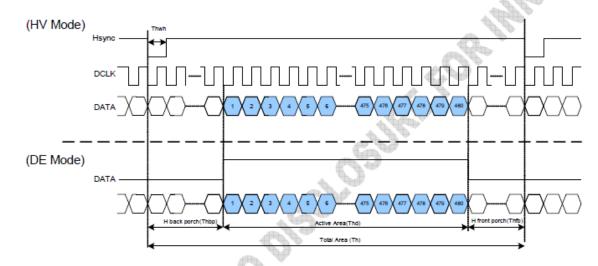
3	GND	Р	Power ground
4	DVDD	Р	Power voltage
5~12	R0~R7	I	Red data
13~20	G0~G7	I	Green data
21~28	B0~B7	I	Blue data
29	GND	Р	Power ground
30	DCLK	I	Pixel clock
31	DISP	I	Display on/off
32	HSYNC	I	Horizontal sync signal
33	VSYNC	I	Vertical sync signal
34	DE	I	Data enable
35	NC		No connect
36	GND	Р	Power ground
37	XR		TP PIN
38	YD		TP PIN
39	XL		TP PIN
40	YU		TP PIN

7-3. Timing Characteristics

Clock and Data Input Waveforms

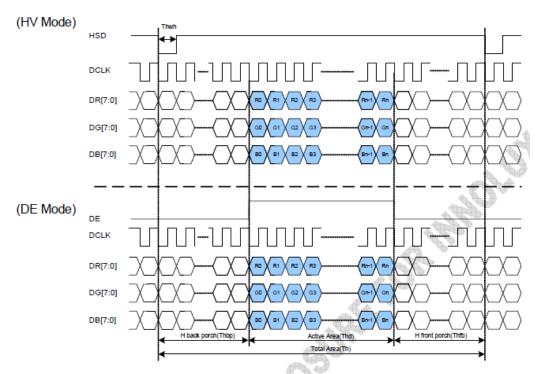


Serial 8-bit RGB Mode Data format



Parameters	Symbol	Min.	Тур.	Max.	Unit	Conditions
DCLK frequency	Fclk	24	27	30	MHz	
DCLK cycle time	Tclk	83	110	200	ns	
DCLK pulse duty	Tcwh	40	50	60	%	
Time from HSD to source output	Thso	-	13	-	DCLK	
Time from HSD to gate output	Thgo	-	27	-	DCLK	
Time from HSD to gate output off	Thgz	-	3	-	DCLK	
Time from HSD to VCOM	Thvc	-	12	-	DCLK	

Parallel RGB Mode Data format



Parallel RGB input timign table

Parameter	Symbol		Unit		
raiametei	Symbol	Min.	Тур.	Max.	Onit
DCLK frequency	fclk	5	9	12	MHz
VSD period time	Tv	277	288	400	Н
VSD display area	Tvd	272			Н
VSD back porch	Tvb	3	8	31	Н
VSD front porch	Tvfp	2	8	97	Н
HSD period time	Th	520	525	800	DCLK
HSD display area	Thd	480			DCLK
HSD back porch	Thbp	36	40	255	DCLK
HSD front porch	Thfp	4	5	65	DCLK

Serial RGB input timign table

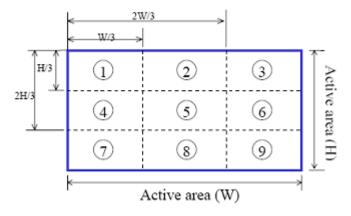
Parameter	Symbol		Unit		
r arameter	Symbol	Min.	Тур.	Max.	Onit
DCLK frequency	fclk	-	27	-	MHz
VSD period time	Tv	277	288	400	Н
VSD display area	Tvd		Н		
VSD back porch	Tvb	3	8	31	Н
VSD front porch	Tvfp	2	8	97	Н
HSD period time	Th	-	1728	-	DCLK
HSD display area	Thd		DCLK		

8. Backlight Characteristics

Item	Symbol	Min	Тур	Max	Unit	Condition	Remark
Forward voltage	VBL	20.3	21.7	23.1	V		-
Current	I _{BL}	-	20	-	mA	IF=40mA	-
ICE	Х	0.26	-	0.36	-	(恒定电流测	
ICE	Y	0.28	-	0.38	-	试)	-
Brightness of LCM	-	170	200		cd/m²		
Uniformity	-	80	80 -		%		

★1 Uniform measure condition:

- (1)Measure 9 point. Measure location is show below:
- (2)Uniform = (Min. brightness / Max. brightness)×100%
- (3)Best Contrast.



9. Electro-Optical Characteristics

Item	Symbol Condition -		Values			Unit	Remark
item	Symbol	Condition	Min.	Тур.	Max.	Offic	Kelliark
	θι	Φ=180°(9 o'clock)	60	70	-		
Viewing angle	θ _R	Ф=0°(3 o'clock)	60	70	-	dograa	Note 1
(CR≥ 10)	θτ	Φ=90°(12 o'clock)	40	50	-	degree	
	θв	Φ=270°(6 o'clock)	60	70	-		
Response time	T _{ON}		-	10	20	msec	Note 3
Tresponse time	T _{OFF}		-	15	30	msec	Note 3
Contrast ratio	CR	Normal	400	500	-	-	Note 4
	W _X	θ=Φ=0°	0.26	0.31	0.36	-	Note 2
Color chromaticity	W _Y		0.28	0.33	0.38	-	Note 5
Transmittance	Tr		-	6.26	-	%	

Test Conditions:

- V_{DD}=3.3V, I_L=20mA (Backlight current), the ambient temperature is 25°C.
 The test systems refer to Note 2.

Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view: 1° /Height: 500mm.)

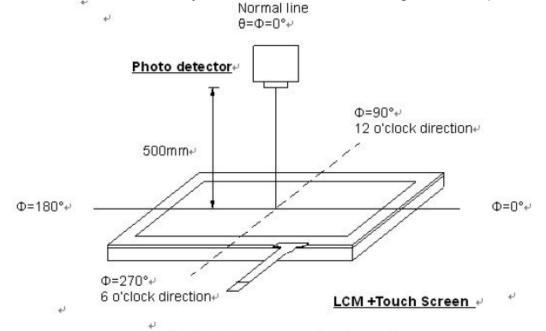


Fig. 4-2 Optical measurement system setup₽

Fig. Optical measurement system setup

Note 2: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 3: All input terminals LCD panel must be ground while measuring the center area of the panel. The LED driving condition is V_{LED} =5.0V.

10. Reliability

10.1. MTBF

The LCD module shall be designed to meet a minimum MTBF value of 50000 hours with normal. (25°C in the room without sunlight)

10. 2. Test condition

ITEM	CONDITIONS	CRITERION
OPERATING	HIGH TEMPERTURE +70°C 48HRS	NO DEFECT IN DISPLAYING AND
TEMPERATURE	LOW TEMPERTURE -20℃ 48HRS	OPERATIONAL FUNCTION
STORAGE	HIGH TEMPERTURE +80°C 48HRS	NO DEFECT IN DISPLAYING AND
TEMPERATURE	LOW TEMPERTURE -30℃ 48HRS	OPERATIONAL FUNCTION
HUMIDITY	40°C 90%RH 48HRS	NO DEFECT IN DISPLAYING AND OPERATIONAL FUNCTION

Note: The need to restore at room temperature for 2 hours after the test.

11. Inspection Standards

AQL(Acceptable Quality Level)
 AQL of major and minor defect

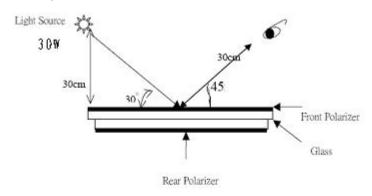
According to GB/T 2828-2003; , normal inspection, Class II

MAJOR DEFECT	MINORDEFECT
0.65	1.5

2. Basic conditions for inspection

The LCM face to us, in normal environment, About an angle of incidence 30, a distance of 30cm with normal eye, with an angle of 45 degree to check the products without uncovering the film!

(As shown below)



- 3. Inspection item and criteria
- 3.1 Visual inspection criterion in immobility

3.1.1 Glass defect

No	Defect item	Criteria	Remark
1	Dimension Unconformity	By Engineering Drawing	
	(Major defect)		

No	Defect item	Criteria	Remark
2	Cracks (Major defect)	1.Linear cracks on panel [Reject] 2. Nonlinear crack contrast by limited sample	
3	Glass extrude the conductive area (minor defect)	a: disregards and no influence assemblage 1) b≤1/3Pin width(non bonding area)	a:Length, b:Width
4	Pin-side , conductive area damaged (minor defect)	(a $c: disregards$) $b \le 1/3$ of effective length for bonding electrode [Accept]	a: Length, b: Width, c: Thickness
5	Pin-side , non-conductive area damaged (minor defect)	1) Damage area don't touch the ITO (Inclueling contraposition mark,except scribing mark)	a:Length, b:Width, c:Thickness

No	Defect item	Criteria	Remark	
	Non-pin-side damage	c <t< td=""><td></td><td>c : Thickness b: width of</td></t<>		c : Thickness b: width of
		1) b exceeds 1/3 BM		damage
	(minor defect)			
6			[Reject]	BM内级
		c=T		
		b not touch the seal glue		→ ← ·
			[Reject]	

3.1.2 LCD appearance defect (View area)

J. 1	3.1.2 LCD appearance defect (View area)						
No	Defect item	Criteria		Remark			
	Fiber · glass	Specification	Allowable	note1: L:Length,W:Width			
1	cratch · polarizer	0.05mm <w≦0.1mm;< td=""><td></td><td>note2: disregard if out of AA</td></w≦0.1mm;<>		note2: disregard if out of AA			
'	scratch/folded	L≦3.0mm	1	L →			
	(minor defect)	W>0.1mm ; L>3.0mm	0				
	Polarizer bubble \	ψ≦0.2mm	disregard	note 1:ψ=(L+W)/2 [*] ; Length , W:			
2	concave and convex (minor defect)	0.2mm<ψ ≦ 0.3mm	2	Width note2: disregard if out of AA			
-	(minor delect)	0.3mm<ψ ≦ 0.5mm	1				
		0.5mm<ψ	0				
	Dipole data distributata	ψ≦0.15mm	disregard	note2: disregard if out of AA			
3	Black dots · dirty dots · impurities · eyewinker	0.15mm<ψ ≦ 0.25mm	2	$\bigcirc \qquad \downarrow _{\phi}$			
		0.25mm<ψ ≦ 0.3mm	1	←→			
	(Major defect)	0.3mm<ψ	0	ψ			
	Polarizer prick	ψ≦0.1mm	disregard	note1:ψ=(L+W)/2 ; L= Length ,			
4	(Major defect)	0.1mm<ψ≦0.25mm	3	W=Width note2: the distance between two			
		ψ>0.25mm	0	dots >5mm			

3.1.3 .FPC

No	Defect item	Criteria		Remark
1	Copper screen peel (Major defect)	Copper screen peel	[Reject]	
2	No release tape or peel (Major defect)	No release tape or peel	[Reject]	
	Dirty dot and impurity of	Specification	Allowable	note1: Cannot have stride ITO
	FPC for customer using	ψ≦0.25mm	2	impurities
	side (minor defect)	ψ>0.25	0	

3.1.4 Black tape & Mara tape

<u>3. I</u>	.4 Black tape & Mara tape			
	FPC or H/S black tape	1.shift spec:		
	shift	1)glue to the polarize		
			[Reject]	
1		2) IC bare	[Reject]	
'	(minor defect)	2. left-and-right spec:		
		1) exceed of FPC edge	or H-S	i .
		edge	[Reject]	
		2)IC bare	[Reject]	
2	No black tape	No black tape		
	(Major defect)		[Reject]	
3	Tape position mistake	Not by engineering draw	/ing	
3	(minor defect)		[Reject]	
4	Mara tape defect	Peel before pulling the	protecting	
		film.		
	(minor defect)		[Reject]	

3.1.5 Silicon and Tuffy glue

No	Defect item	Criteria Remark				
	Quantity of silicon	Uncover the ITO and circuit area.	note: co	ompared	by	engineering
	(minor defect)	[Reject]	drawing.			
1						

No	Defect item	Criteria	Remark
2	Tuffy glue (minor defect)	 Uncover the reveal copper area [Reject] Cover layer 0.3mm(Min) ~ 3.0mm(Max) [accept] 	requirement , refer to the
3	Depth of glue covering	Depth of glue covering overtop front Polarizer	Except of the special requirement
	(minor defect)	[Reject]	

3.2 Electrical criteria

NI ₂	Defeat items Criteria		Damark
No		Criteria	Remark
1	No display	No display	
	(Major defect)	[Reject]	
2	Missing line	Missing line	
	(Major defect)	[Reject]	
3	Seg-com light and dark	Seg-com light and dark	ND filter 2% test
	(Major defect)	[Reject]	
4	No display in immobility	No display in immobility	
	(Major defect)	[Reject]	
5	Flicker of Pattern	Flicker of Pattern	
	(Major defect)	[Reject]	
		ND filter 2% test	
6	(Major defect)	IND litter 270 test	
7	Over current	Over current	
'	(Major defect)	[Reject]	
	Voltage out of specification	Voltage out of specification	
8		[Reject]	
	(Major defect)		
	Pattern blur ,error code	Pattern blur ,error code	
9	,	[Reject]	
	(Major defect)		
	Dark light, Flicker	Dark light, Flicker	
10	(Major defect)	[Reject]	
	(iviajoi delect)	[Nojeot]	

No	Defect item	Criteria		Remark
	Black/White dots Dirty dots eyewinker	Specification	Allowable	Note1: disregard if out of
		ψ≦0.15mm	disregard	AA
11		$0.15 mm <\!$	2	$\downarrow \phi$
		$0.25 \text{mm} {<} \psi \leqq 0.3 \text{mm}$	1	ψ
		0.3mm<ψ	0	
	Fiber · glass cratch · polarizer scratch/folded (minor defect)	W≦0.03mm	disregard	note1: L : Length · W : Width
		0.03mm <w≦0.05mm; L≦3.0mm</w≦0.05mm; 	2	note2: disregard if out of AA
12		0.05mm <w≦0.1mm; L≦3.0mm</w≦0.1mm; 	1	V W
		W>0.1mm ; L>3.0mm	0	

12. Precautions For Using LCD Modules

Please pay attentions to the followings as using the LCD module.

12.1 Handling

- (a) Do not apply strong mechanical stress like drop, shock or any force to LCD module. It may cause improper operation, even damage.
- (b) Because the ITO film very fragile and easy to be damaged, do not hit, press or rub the display surface with hard materials.
- (c) Do not put heavy or hard material on the display surface, and do not stack LCD modules.
- (d) If the display surface is dirty, please wipe the surface softly with cotton swab or clean cloth.
- (e) Wipe off water droplets or oil immediately.
- (f) Protect the LCD module from ESD. It will damage the LSI and the electronic circuit.
- (g) Do not touch the output pins directly with bare hands.
- (h) Do not disassemble the LCD module.

12.2 Storage

- (a) Do not leave the LCD modules in high temperature, especially in high humidity for a long time.
- (b) Do not expose the LCD modules to sunlight directly.
- (c) The liquid crystal is deteriorated by ultraviolet. Do not leave it in strong ultraviolet ray for a long time.
- (d) Avoid condensation of water. It may cause improper operation.
- (e) Please stack only up to the number stated on carton box for storage and transportation. Excessive weight will cause deformation and damage of carton box.

12.3 Operation

- (a) When mounting or dismounting the LCD modules, turn the power off.
- (b) Protect the LCD modules from electric shock.
- (c) The Driver IC control algorithms stated above should always obeyed to avoid damaging the LSI and electronic circuit.
- (d) Be careful to avoid mixing up the polarity of power supply for backlight.

- (e) Absolute maximum rating specified above has to be always kept in any case. Exceeding it may cause non-recoverable damage of electronic components or, nevertheless, burning.
- (f) When a static image is displayed for a long time, remnant image is likely to occur.
- (g) Be sure to avoid bending the FPC to an acute shape, it might break FPC.

12.4 Others

- (a) If the liquid crystal leaks from the panel, it should be kept away from the eyes or mouth.
- (b) It is recommended to peel off the protection film on the ITO film slowly so that the electrostatic charge can be minimized.
- (c) It is recommended to peel off the protection film on the polarizer slowly so that the electrostatic charge can be minimized.

13. Records Of Version

Version	Revise Date	Page	Content
v1.5	2020-4-13	All	New released