

	MemtoReg	RegDst	IorD	PCSrc(2)	ALUSrcB(2)	ALUSrcA	IRWrite	MemWrite	PCWrite	Branch	RegWrite	ALUOp(2)	
LW	0	0	0	00	10	1	0	0	0	0	0	00	0280
	0	0	1	00	00	0	0	0	0	0	0	00	1000
	1	0	0	00	00	0	0	0	0	0	1	00	4004
SW	0	0	0	00	10	1	0	0	0	0	0	00	0280
	0	0	1	00	00	0	0	1	0	0	0	00	1020
	0	0	0	00	00	0	0	0	0	0	0	00	0000
R-TYPE	0	0	0	00	00	1	0	0	0	0	0	10	2082
	0	1	0	00	00	0	0	0	0	0	1	00	2004
	0	0	0	00	00	0	0	0	0	0	0	00	0000
BEQ	0	0	0	01	00	1	0	0	0	1	0	01	0489
	0	0	0	00	00	0	0	0	0	0	0	00	0000
	0	0	0	00	00	0	0	0	0	0	0	00	0000
ADDI	0	0	0	00	10	1	0	0	0	0	0	00	0280
	0	0	0	00	00	0	0	0	0	0	1	00	0004
	0	0	0	00	00	0	0	0	0	0	0	00	0000
JUMP	0	0	0	10	00	0	0	0	1	0	0	00	0810
	0	0	0	00	00	0	0	0	0	0	0	00	0000
	0	0	0	00	00	0	0	0	0	0	0	00	0000