# 6 More clock algorithms, processes versus threads (week 6)

#### 6.1 vector clocks

let s,v be the vector clock with state/event s, s.p the process on which s is an event/state. last week we looked at a vector clock algorithm that satisfied:

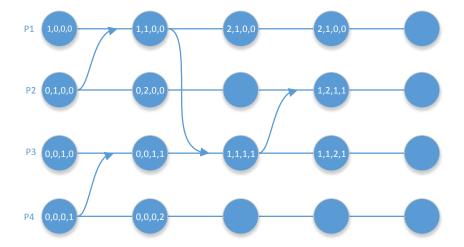
$$\forall s, t \quad s.v < t.v \iff s \rightarrow t$$

the vector clock algorithm VC2 preserves this property when s.t are in different processes.

$$\forall s.p \neq t.p \quad s \rightarrow t \iff s.t < t.v$$

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\begin{aligned} &P_j:: \\ &\text{var} \\ &v: \mathbf{array}[1\dots \mathbf{N}] \text{ of integer} \\ &&\text{initially } (\forall i, i \neq j: v[i] = 0) \land (v[j] = 1); \\ &\not \parallel initial(s) \Rightarrow (\forall i: i \neq s.p: s.v[i] = 0) \land (s.v[s.p] = 1) \end{aligned} \begin{aligned} &\mathbf{send \ event} \ (\mathbf{s}, \, \mathbf{send}, \, \mathbf{t}); \\ &t.v[t.p] := t.v[t.p] + 1; \\ &\not \parallel \ (\forall i: i \neq t.p \ t.v[i] = s.v[i]) \land (t.v[t.p] > s.v[t.p]) \end{aligned} \begin{aligned} &\mathbf{receive \ event} \ (\mathbf{s}, \, \mathbf{receive}(\mathbf{u}), \, \mathbf{t}); \\ &\mathbf{for} \ i := \mathbf{1to} \ N \ \mathbf{do} \\ &t.v[i] := \max(s.v[i], u.v[i]); \end{aligned} \mathbf{internal \ event} \ (\mathbf{s}, \, \mathbf{internal}, \, \mathbf{t}); \\ &t.v := s.v; \end{aligned}
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Figure 8: Vector clock algorithm VC2



## 6.2 comparison of vector clocks x,y

$$\begin{array}{ll} x < y & \quad (\forall k: 1 \leq k \leq \mathbb{N}: x[k] \leq y[k]) \wedge (\exists j: 1 \leq j \leq \mathbb{N}: x[j] < y[j]) \\ x \leq y & \quad (x < y) \vee (x = y) \end{array}$$

#### 6.3 clock synchronization algorithm

System model: we assume each machine has a timer that causes H interrupts per second.

- clock c add ticks(interrupts)
- at UTC time t (universal coordinated time)  $C_p(t)$  is clock on machine p at t
- perfect clock:  $C_p(t) = t$   $\forall p, t \Leftrightarrow C'_p(t) = \frac{dC_p(t)}{dt} = 1$  frequency of the perfect clock p at time t  $C'_p(t) 1 \triangleq \underline{\text{skew}}$  of P's clock, difference of the perfect clock  $C_p(t) t \triangleq \text{offset}$
- Real timers do not interrupt # times per second, maximum drift rate y such that  $1-p \le \frac{dC_p/t}{dt} \le 1+p$  at time  $\Delta t$  two clocks that are drifting apart can be at  $|C_2^{(\Delta t)} C_1^{(\Delta t)}| = 2p\Delta t$
- if the difference  $\alpha$  should never exceed  $\delta$  then synchronization every  $\frac{\delta}{2p}$  seconds is needed.
- it is important to make sure that time always moves forward

Principle of the network time protocol(NTP)

A estimates³ offset to B as  $\theta = T_1 - \frac{(T_1 - T_2) + (T_4 - T_3)}{2} - \frac{(T_2 - T_1) + (T_3 - T_4)}{2}$  (assumes communication time request both ways) and delay  $\delta = \frac{(T_4 - T_1) - (T_3 - T_2)}{2}$ 

<sup>&</sup>lt;sup>3</sup>see in Tannenbaum

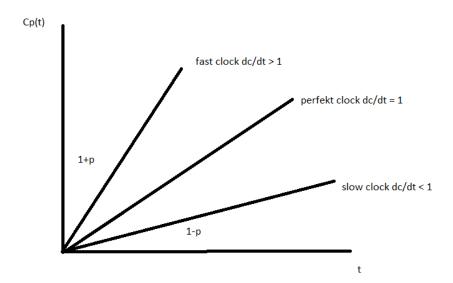


Figure 9: Speed of fast, perfect and slow clocks

- A probes B and B probes A
- NTP stores 8 pairs  $(\theta, \delta)$  per pair using min  $\delta$  for smallest delay
- Either A or B can be more stable, less stable clocks adjusts
- reference clock has stratum 1
- lower stratum level is better, will be used

### 7 Processes and Threads

#### process

- execution of a program
- processor creates virtual processor for each program and stores it in a "process table"
- $\bullet\,$  each virtual process has its own independent address space
- transparent sharing of resources(processor, memory)-separation
- process switch is expensive(save CPU context, registers, pointers, modify memory management unit(MMU),translation lookaside buffer(TLB))
- perhaps even swap to disk

#### threads

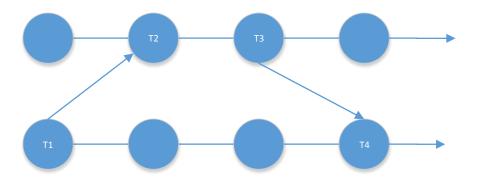


Figure 10: Time stamps used by NTP  $\,$ 

- $\bullet\,$  several threads sharing CPU
- $\bullet\,$  thread context has little memory information
- $\bullet\,$  threads avoid blocking application
- $\bullet$  thread switch is fast
- ullet user level threads allows parallel computation
- $\bullet\,$  I/O or other blocking system calls block all threads
- $\longrightarrow$  lightweight processes aim at combining the advantages of both.