startup				send			send				send					send	
write cycle	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
time stamp	0.0	0.1	0.2	0.3	0.4	0.5	0.6	0.7	0.8	0.9	0.10	0.11	0.12	0.13	0.14	0.15	0.16
write time to index	0	1	2	3	4	5	6	7	0	1	2	3	4	5	6	7	0
write ref_X to index	0	1	2	3	4	5	6	7	0	1	2	3	4	5	6	7	0
write sig_I to index	0	0	1	2	3	4	5	6	7	0	1	2	3	4	5	6	7
send buffer #					Α				В				Α				В
	Thick lines indicate buffer sends of each 4 samples																
	Employing double buffer [A; B]																