## g G IC122 +5 V MRIO9 4.7K x 8 Clock CI66 33P R199 W XIOI R198 mı STABY P20 P21 P23 P23 CI65 33P AMHZ CPU IC 150 6 AS R/W PO/AO DO/AO 3 29 W €201 MR108 4.7 K x 8 #5 R223 4.7Kx2 R220 AO , AI2 ~ AI5 A8 ~ A15 AS AO ~ A7 V W V 13 I IC 153 IC 142 O SPX90 SCHEMATIC DIAGRAM (DIGITAL Latch RI ₹I mI CI97 47 22 K AO ~ A7 DO ~ D7 Reset DO ~ D7 Ē, R/W IC153 $AO \sim AI5$ \sqrt{\sqrt{0}} A A A13 A15 TP136 R231 ១ ខាក្ន IC 146 ទីវីងជីស្ដីស្ន 12844 173 18 0 RESET ŌĒ 8000 ~ FFFF IC 151 **ROM** R2357X 8-2357X 15K XLOK (500KHz) 2000~3FFF Modulaticn Data Generator 8010 R236 NW R237 NO BIOI 3V R236 $\sqrt{\frac{1}{1-}}$ ICI53 0.1 Memory Back up -O35 R/W $AO \sim AI2$ N N ŌĒ 4000 ~ 5FFF RAM IC 152 ç 10/16 # 6000 ~ 7FFF AO