Celsee, Inc. Genesis V2 Daughter PCB – PN 10000387 Rev. 1 10/31/2018 **Fabrication Specification Brian Boniface, Software and Electrical Engineer**

The following table identifies the fabrication specification for the Genesis V2 Daughter PCB:

Attribute	Specification
Board Size	10.24" x 5.67" (includes rails)
Layers	4
Stack Order	Top Signal (Component Side), Ground, Power, Bottom Signal
Material Type	FR4
Finished	0.62"
Thickness	
Rout Type	Individually routed
Array/Panel	The PCB already includes array rails. No additional arrays required.
Finished Plating	Lead-free solder
Copper Weight	1-oz
Drill	Specified in PTH and NPTH drill files.
Slots	No
Soldermask	Specified in top and bottom mask gerbers.
Soldermask	Blue
Color	
Silkscreen	Specified in top and bottom silk gerbers. No silk on pads.
Silkscreen Color	White
Gold Fingers	No
Dielectric	Use of standard dielectric stack as determined by design.
Thickness	
Impedance	No
RoHS	Yes
ITAR	No
Board Thickness	+/- 10%
Tolerance	
Dimensional	+/- 10 mils
Tolerance	
Inspection	IPC Class II
Criteria	