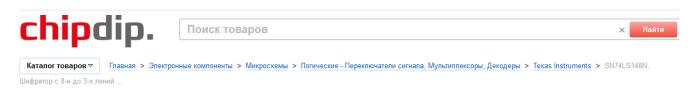
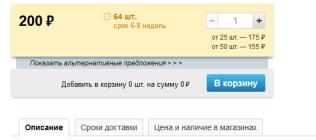
03апр2020

https://www.chipdip.ru/product/sn74ls148n



SN74LS148N, Шифратор с 8-и до 3-х линий передачи данных





The SN74LS148N is a 8-to-3 Priority Encoder features priority decoding of the inputs to ensure that only the highest-order data line is encoded. The LS147 encode nine data lines to four-line (8-4-2-1) BCD. The implied decimal zero condition requires no input condition as zero is encoded when all nine data lines are at a high logic level. Cascading circuitry (enable input E1 and enable output E0) has been provided to allow octal expansion without the need for external circuitry. For all types, data inputs and outputs are active at the low logic level.

Технические параметры

Серия	74LS
Тип	priority encoder
Схема	1x8:3
Кол-во каналов	<u>1</u>
Максимальный выходной ток: High/Low, мА	-0.4/8
Напряжение питания, В	4.755.25
Рабочая температура, °C	0+70
Корпус	dip-16(7.62мм)