_				
Reg. HEX Chann	Purpose	Comment 1	Comment 2	Action
1	1 FreqLSW	CH1 Freq =100 000*FreqMSW + FreqLSW μHz	FreqMSW = ENT(Freq/100 000)	rectori
2	1 FreqMSW	CH1 Freq =100 000*FreqMSW + FreqLSW µHz	FreqLSW = Freq - 100 000*FreqMSW	
3	2 FreqLSW	CH2 Freq =100 000*FreqMSW + FreqLSW µHz	FreqMSW = ENT(Freq/100 000)	
4	2 FreqMSW	CH2 Freq =100 000*FreqMSW + FreqLSW µHz	FreqLSW = Freq - 100 000*FreqMSW	
5	1,2 Channel state	bit2 = CH2=ON/OFF (0/1) bit3 = ?	Reg 0x05 = 8 at some point. To be checked	
		bit5 = CH1 ON/OFF (0/1)		
		313 317 317 (472)		
6	1,2 Relays Control + Config bits	Bit 0 = CH2 JD3 Relay ON/OFF (0/1)	Bit5 & Bit6 seem to be linked strangely: To be checked	
		Bit 1 = CH2 JD1 Relay ON/OFF (0/1)		
		Bit 2 = CH1 JD4 Relay ON/OFF (0/1)		
		Bit 3 = CH1 JD2 Relay ON/OFF (0/1)		
		Bit 4 = AC/DC Input Coupled Mode (0/1)		
		Bit 5 = Uplink ON/OFF (0/1)		
		Bit 6 = Uplink Mode Master/Slave (0/1)		
8	1 PhaseDW	Phase = 360*(1-PhaseDW/1048575)	PhaseDW=(1-Phase/360)*1048575	
9	2 PhaseDW	Phase = 360*(1-PhaseDW/1048575)	PhaseDW=(1-Phase/360)*1048575	
Α				
В				
C Input	Input_measure_Request value	01 = ?	Work in progress	
		03 = Request Frequency 07 = Request counter		
		0B = Request + WIDE		
		0F = ?		
		1B = Request -WIDE		
		1A = ?		
		1E = ?		
		1F = ?		
D Input		Read value requested by Reg 0x0C	Address has to be suitten in and 121 f	
E	1,2 Flash to FP Read_Value_Read	Return one single value read from flash (one waveform sample but not only)	Address has to be written in reg0x12 before and	
F	1,2 Flash to FP Read_Trigger	Write 1 to trigger Read then write 0 (Reset Trigger)	triggered with Reg 0x15	
10	TIT HOUSE WENT THE SECTION OF THE SE	Request with value from 0 to F written then read reg 0x1A: Read 16 parameters	Seen only in startup sequence	To be identified. Low priority
	1,2 Flash to FPGA Read_Channel Number	CH1=1, CH2=2, 0=?	Set which channel to load the Waveform for	To the state of the priority
	1,2 Memory Address	Waveform Sample Memory address	Set value in register and read reg 0x0E	
13	1,2 Flash to FPGA Read_Trigger	Write 1 to trigger Read then write 0 (Reset Trigger)		
14	FP to Flash Write_Trigger	write 1 to trigger , 0 to reset (rearm trigger)	Written with 0 before flash read operation	
15	FP to Flash Write_Value	Value to write to Flash		
16	FP to Flash Write_Page_Erase_Trigger	Write 1 to trigger , 0 to reset (rearm trigger)	Reg0x12 contains address of page to be erased. 1	
17	ED to Elech Welter Chart Address Triange	Weite day being 0 to seek (seems beings)	waveform = 4 pages	
18	FP to Flash Write_Start_Address_Trigger Flash operation Status Request	Write 1 to trigger , 0 to reset (rearm trigger) Ask for cyclic flash operation status update	Trigger Start of block address to FPGA FPGA returns 1/0 for pending/no pending operation.	
10	riasii operation status nequest	Ask for cyclic hash operation status update	Cyclic	
19		Trigger reg 0x1B value	Seen only in startup sequence	To be identified. Low priority
1A		? Answer of request from reg 0x10	Seen only in startup sequence	To be identified. Low priority
1B		0 to F? : Write 16 parameters. Parameters Initialization ?	Seen only in startup sequence	To be identified. Low priority
1C	1 Manual Source for FSK/ASK/PSK/BURST	0= Start, 1=Stop	For burst :Write 0 followed by 1 immediately	
1D	1 Modulation Source & Burst Number	Reg 0x1D = 00AB BBBB	Set FSK/ASK/PSK /Burst Source + Burst number	
		A = FSK / ASK / PSK / BURST Source		
		0 = FSK / ASK / PSK / BURST Not Selected 1 = CH2		
		2 = Ext.(AC)		
		3 = MANU		
		4 = Ext.(DC)		
		B BBBB= Burst Numbers = 1 to F4240 (1 to 1 000 000)		
1E Input	Measure status Request	Read 1 for Frequency available		To be checked
		Read 2 for not data available		
1F		Read 3 for min/max available		
20				
21	1 Pulse Width	10ns = 2, 4 000 000 000 ns = 3B9A CA00 -> 1 step = 4 ns	CH1 Only	
22	1 FSK HOP/ FM BIAS FreqLSW	Freq =100000*FreqMSW + FreqLSW μHz	FreaMSW = ENT(Frea/100 000)	
23	1 FSK HOP/ FM BIAS FreqMSW			
24		Freq =100000*FreqMSW + FreqLSW μHz	FreqLSW = Freq - 100 000*FreqMSW	
	1 Modulation Mode	No Modulation = 0	FreqLSW = Freq - 100 000*FreqMSW 2 identical writes looks useless for some modes	
1	1 Modulation Mode	No Modulation = 0 PSK = 1 two times		
	1 Modulation Mode	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82		
	1   Modulation Mode	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times		
	1 Modulation Mode	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8		
	1 Modulation Mode	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 (CO-IN) source = 18, Mod Rate(Reg 0x36) then 18		
	1 Modulation Mode	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8		
	1 Modulation Mode	No Modulation = 0 PSK = 1 two times FSK = 2, tho prequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with EXT. (VCO-IN) source = 18, Mod Rate(Reg 0x36) then 18 BURST = 10, Burst Number (Reg 0x10) then 0		
	1 Modulation Mode	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2, I/CVC-IN) source = 18, Mod Rate(Reg 0x36) then 18 BURST = 10, Burst Number (Reg 0x1D) then 0 FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then A0 two times FM with EXT. (VCO-IN) source = 30 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times PM with CH2 source = 40, PM BIAS (Reg 0x3) then 40		
	1 Modulation Mode	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with EXT. (VCO-IN) source = 18, Mod Rate(Reg 0x36) then 18 BURST = 10, Burst Number (Reg 0x10) then 0 FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then A0 two times FM with EXT. (VCO-IN) source = 30 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with EXT. (VCO-IN) source = 50, PM BIAS (Reg 0x3A) then 50	2 identical writes looks useless for some modes	
25	1 Modulation Mode	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with EXT. (VCO-IN) source = 18, Mod Rate(Reg 0x36) then 18 BURST = 10, Burst Number (Reg 0x1D) then 0 FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then A0 two times FM with EXT. (VCO-IN) source = 30 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 50 Written with 0x02FA F080 (= 50 000 000)	2 identical writes looks useless for some modes  Seen only in startup sequence	To be identified. Low priority
26	1 Modulation Mode	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 BURST = 10, Burst Number (Reg 0x10) then 0 FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then A0 two times FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times PM with CH2 source = 40, PM BIAS (Reg 0x34) then 40 PM with CH2 source = 40, PM BIAS (Reg 0x34) then 40 PM with CH2 source = 40, PM BIAS (Reg 0x34) then 40 PM with CH2 source = 50, PM BIAS (Reg 0x34) then 50 Written with 0x02FA F080 (= 50 000 000) Written with 0x1DCD 6500 (= 500 000 000) Written with 0x1DCD 6500 (= 500 000 000)	2 identical writes looks useless for some modes  Seen only in startup sequence Seen only in startup sequence	To be identified. Low priority
26 27	1 Modulation Mode	No Modulation = 0 PSK = 1 two times FSK = 2, tho prequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 18, Mod Rate(Reg 0x36) then 18 BURST = 10, Burst Number (Reg 0x10) then 0 FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then A0 two times FM with CH2 source = 40, PM BIAS (Reg 0x23 & 0x22) then B0 two times FM with EXT. (VCO-IN) source = 30 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times PM with CH2 source = 40, PM BIAS (Reg 0x34) then 40 PM with EXT. (VCO-IN) source = 50, PM BIAS (Reg 0x3A) then 50 Written with 0x02FA F080 (= 50 000 000) Written with 0x10FD 6500 (= 500 000 000) Written with 0x10FD 6500 (= 500 000 000) Written with 0x01	2 identical writes looks useless for some modes  Seen only in startup sequence Seen only in startup sequence Seen only in startup sequence	To be identified. Low priority To be identified. Low priority
26	1 Modulation Mode	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 BURST = 10, Burst Number (Reg 0x10) then 0 FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then A0 two times FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times PM with CH2 source = 40, PM BIAS (Reg 0x34) then 40 PM with CH2 source = 40, PM BIAS (Reg 0x34) then 40 PM with CH2 source = 40, PM BIAS (Reg 0x34) then 40 PM with CH2 source = 50, PM BIAS (Reg 0x34) then 50 Written with 0x02FA F080 (= 50 000 000) Written with 0x1DCD 6500 (= 500 000 000) Written with 0x1DCD 6500 (= 500 000 000)	2 identical writes looks useless for some modes  Seen only in startup sequence Seen only in startup sequence	To be identified. Low priority
26 27 28 29 2A		No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with EXT. (VCO-IN) source = 18, Mod Rate(Reg 0x36) then 18 BURST = 10, Burst Number (Reg 0x1D) then 0 FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then A0 two times FM with EXT. (VCO-IN) source = 30 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with EXT. (VCO-IN) source = 50, PM BIAS (Reg 0x3A) then 50 Written with 0x1DC0 6500 (= 500 000 000) Written with 0x1DC0 6500 (= 500 000 000) Written with 0x01 Written with 0x2A05 F200	Seen only in startup sequence	To be identified. Low priority
26 27 28 29	1 Modulation Mode  1 OffsetDW	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 18, Mod Rate(Reg 0x36) then 18 BURST = 10, Burst Number (Reg 0x1D) then 0 FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then A0 two times FM with EXT, (VCO-IN) source = 30 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with EXT, (VCO-IN) source = 50, PM BIAS (Reg 0x3A) then 50 Written with 0x02FA F080 (= 50 000 000) Written with 0x1DCD 6500 (= 50 00 000 000) Written with 0x2A05 F200 Written with 0x2A05 F200 Written with 0x2A05 F200 Written with 0x2A05 F200	Seen only in startup sequence [-1-1-1-1]	To be identified. Low priority 4094 is a best match but not logical
26 27 28 29 2A		No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 18, Mod Rate(Reg 0x36) then 18 BURST = 10, Burst Number (Reg 0x1D) then 0 FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then A0 two times FM with EXT, (VCO-IN) source = 30 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with EXT, (VCO-IN) source = 50, PM BIAS (Reg 0x3A) then 50 Written with 0x02FA F080 (= 50 000 000) Written with 0x1DCD 6500 (= 50 00 000 000) Written with 0x2A05 F200 Written with 0x2A05 F200 Written with 0x2A05 F200 Written with 0x2A05 F200	Seen only in startup sequence	To be identified. Low priority
26 27 28 29 2A 2B	1 OffsetDW	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 18, Mod Rate(Reg 0x36) then 18 BURST = 10, Burst Number (Reg 0x1D) then 0 FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then A0 two times FM with EXT, (VCO-IN) source = 30 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with EXT, (VCO-IN) source = 50, PM BIAS (Reg 0x3A) then 50 Written with 0x02FA F080 (= 50 000 000) Written with 0x1DCD 6500 (= 50 00 000 000) Written with 0x2A05 F200 Written with 0x2A05 F200 Written with 0x2A05 F200 Written with 0x2A05 F200	Seen only in startup sequence [-1-1-1-1]	To be identified. Low priority 4094 is a best match but not logical
26 27 28 29 2A 2B	1 OffsetDW 2 OffsetDW	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 BURST = 10, Burst Number (Reg 0x10) then 0 FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then A0 two times FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with EXT. (VCO-IN) source = 50, PM BIAS (Reg 0x3A) then 50 Written with 0x02FA F080 (= 50 000 000) Written with 0x1DC0 6500 (= 500 000 000) Written with 0x2A05 F200 Written with 0x2A05 F200 Written with 0x2A05 F200 Written with 0xA931 A000	Seen only in startup sequence [-1v]-1v]: OffsetDW-2047+(4094/6,21)*Offset outside ]-1;1v[: OffsetDW-2047+(4094/24)*Offset	To be identified. Low priority 4094 is a best match but not logical (DAC has 4096 steps)
26 27 28 29 2A 2B	1 OffsetDW	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 8, Mod Rate(Reg 0x36) then 18 BURST = 10, Burst Number (Reg 0x1D) then 0 FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then A0 two times FM with CH2, T(VCO-IN) source = 30 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with EXT. (VCO-IN) source = 50, PM BIAS (Reg 0x3A) then 50 Written with 0x02FA F080 (= 50 000 000) Written with 0x1DCD 6500 (= 500 0000) Written with 0x1DCD 6500 (= 500 0000) Written with 0x2DA F080 F200 Written with 0x2DA F080 F200 Written with 0x2DA F080 F200 Written with 0x4031 A000	Seen only in startup sequence [-10/1/10]: OffestDW-2047+(4094/6,21)*Offset outside ]-1;1V[: OffsetDW=2047+(4094/24)*Offset [00/;0.5000V]: AmpDW=Amp*3685/0,5	To be identified. Low priority 4094 is a best match but not logical (DAC has 4096 steps)  Just for offset = 0. Ranges depend on
26 27 28 29 2A 2B	1 OffsetDW 2 OffsetDW	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with EXT. (VCO-IN) source = 18, Mod Rate(Reg 0x36) then 18 BURST = 10, Burst Number (Reg 0x10) then 0 FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then A0 two times FM with CH2 source = 30 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times PM with CH2 source = 40, PM BIAS (Reg 0x34) then 40 PM with EXT. (VCO-IN) source = 50, PM BIAS (Reg 0x3A) then 50 Written with 0x02FA F080 (= 50 000 000) Written with 0x10C 6500 (= 500 000 000) Written with 0x10C 6500 (= 500 000 000) Written with 0x2A05 F200 Written with 0x2A05 F200 Written with 0x4931 A000  [0V;0.5000V]: Amp=0.5xAmpDW/3685 [0.5001V;5.0000V]: Amp=5xAmpDW/3685	Seen only in startup sequence [-1v]-1v]: OffsetDW-2047+(4094/6,21)*Offset outside ]-1;1v[: OffsetDW-2047+(4094/24)*Offset	To be identified. Low priority 4094 is a best match but not logical (DAC has 4096 steps)
26 27 28 29 2A 2B	1 OffsetDW 2 OffsetDW	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 8, Mod Rate(Reg 0x36) then 18 BURST = 10, Burst Number (Reg 0x1D) then 0 FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then A0 two times FM with CH2, T(VCO-IN) source = 30 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with EXT. (VCO-IN) source = 50, PM BIAS (Reg 0x3A) then 50 Written with 0x02FA F080 (= 50 000 000) Written with 0x1DCD 6500 (= 500 0000) Written with 0x1DCD 6500 (= 500 0000) Written with 0x2DA F080 F200 Written with 0x2DA F080 F200 Written with 0x2DA F080 F200 Written with 0x4031 A000	Seen only in startup sequence [-1v;1v]: OffestDW=2047+(4094/6;21)*Offset outside ]-1;1v[: OffsetDW=2047+(4094/24)*Offset [Ov;0.5000V]: AmpDW=Amp*3685/0,5 [0.5001v;5.0000V]: AmpDW=Amp*3685/5	To be identified. Low priority 4094 is a best match but not logical (DAC has 4096 steps)  Just for offset = 0. Ranges depend on
26 27 28 29 2A 2B 2C 2D	1 OffsetDW 2 OffsetDW 1 AmpDW	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 BURST = 10, Burst Number (Reg 0x10) then 0 FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then A0 two times FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times FM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with EXT. (VCO-IN) source = 30 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times PM with EXT. (VCO-IN) source = 50, PM BIAS (Reg 0x3A) then 50 Written with 0x02FA F080 (= 50 000 000) Written with 0x1DC 05500 (= 500 000 000) Written with 0x1DC 05500 (= 500 000 000) Written with 0x2A05 F200 Written with 0x4D3FA F080 (= 50 000 000 000) Written with 0x4D3FA F080 (= 50 000 000 000) Written with 0x4D3FA F080 (= 50 000 000 000) Written with 0x4D3FA F080 (= 50 000 000 000) Written with 0x4D3FA F080 (= 50 000 000 000) Written with 0x4D3FA F080 (= 50 000 000 000) Written with 0x4D3FA F080 (= 50 000 000 000) Written with 0x4D3FA F080 (= 50 000 000 000) Written with 0x5D5F Written with 0x4D3FA F080 (= 50 000 000 000) Written with 0x4D3FA F080 (= 50 000 000 000) Written with 0x5D5F Written with 0x4D3FA F080 (= 50 000 000 000) Written with 0x4D3FA F080 (= 50 000 000 000) Written with 0x5D5F Written with 0x4D3FA F080 (= 50 000 000 000 000) Written with 0x4D3FA F080 (= 50 000 000 000 000) Written with 0x5D5F Written with 0x4D3FA F080 (= 50 000 000 000 000 000 000 000 000 000	Seen only in startup sequence [-1V;1V]: OffestDW=2047+(4094/6,21)*Offset outside ]-1;1V[: OffsetDW=2047+(4094/6,21)*Offset  [0V;0.5000V]: AmpDW=Amp*3685/0,5 [0.5001V;5.0000V]: AmpDW=Amp*3685/0 [0.5001V;5.0000V]: AmpDW=Amp*3685/0,5 [0.5001V;5.0000V]: AmpDW=Amp*3685/0,5	To be identified. Low priority 4094 is a best match but not logical (DAC has 4096 steps)  Just for offset = 0. Ranges depend on offset value too
26 27 28 29 2A 2B 2C 2D	1 OffsetDW 2 OffsetDW 1 AmpDW 2 AmpDW	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 BURST = 10, Burst Number (Reg 0x10) then 0 FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then A0 two times FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with EXT. (VCO-IN) source = 50, PM BIAS (Reg 0x3A) then 50 Written with 0x02FA F080 (= 50 000 000) Written with 0x02FA F080 (= 50 000 000) Written with 0x02FA F080 (= 500 000 000) Written with 0x2A05 F200 Written with 0x2A05 F200 Written with 0x4D05 F200 Written with 0x4D31 A000  [0V;0.5000V]: Amp=0.5xAmpDW/3685 [0.5001V;2.0000V]: Amp=20.5xAmpDW/3685 [0.5001V;2.0000V]: Amp=20.5xAmpDW/3685 [0.5001V;2.0000V]: Amp=0.5xAmpDW/3685 [0.5001V;2.0000V]: Amp=0.5xAmpDW/3685 [0.5001V;2.0000V]: Amp=20.5xAmpDW/3685 [0.5001V;2.0000V]: Amp=20.5xAmpDW/3685	Seen only in startup sequence [-1v1:V1]: OffestDW=2047+(4094/6,21)*Offset outside ]-1;1V[: OffsetDW=2047+(4094/24)*Offset [0V;0.5000V]: AmpDW=Amp*3685/0,5 [0.5001V;2.0.0000V]: AmpDW=Amp*3685/5 [5.0001V;2.0.0000V]: AmpDW=Amp*3685/0,5	To be identified. Low priority 4094 is a best match but not logical (DAC has 4096 steps)  Just for offset = 0. Ranges depend on offset value too  Just for offset = 0. Ranges depend on
26 27 28 29 2A 2B 2C 2D 2E	1 OffsetDW 2 OffsetDW 1 AmpDW 2 AmpDW	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 8, Mod Rate(Reg 0x36) then 18 BURST = 10, Burst Number (Reg 0x10) then 0 FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then A0 two times FM with CH2, T(VCO-IN) source = 30 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with EXT, (VCO-IN) source = 50, PM BIAS (Reg 0x3A) then 50 Written with 0x2FA F080 (= 50 000 000) Written with 0x1DCD 6500 (= 500 0000) Written with 0x1DCD 6500 (= 500 0000) Written with 0x1DCD 6500 (= 500 0000 000) Written with 0x403 F200 Written with	Seen only in startup sequence [-1V;1V]: OffestDW=2047+(4094/6,21)*Offset outside ]-1;1V[: OffsetDW=2047+(4094/6,21)*Offset  [0V;0.5000V]: AmpDW=Amp*3685/0,5 [0.5001V;5.0000V]: AmpDW=Amp*3685/0 [0.5001V;5.0000V]: AmpDW=Amp*3685/0,5 [0.5001V;5.0000V]: AmpDW=Amp*3685/0,5	To be identified. Low priority 4094 is a best match but not logical (DAC has 4096 steps)  Just for offset = 0. Ranges depend on offset value too  Just for offset = 0. Ranges depend on
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26 27 28 29 2A 2B 2C 2D 2C 2D 2E 2F 30 31	1 OffsetDW 2 OffsetDW 1 AmpDW 2 AmpDW	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 8, Mod Rate(Reg 0x36) then 18 BURST = 10, Burst Number (Reg 0x10) then 0 FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then A0 two times FM with CH2, T(VCO-IN) source = 30 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with EXT, (VCO-IN) source = 50, PM BIAS (Reg 0x3A) then 50 Written with 0x2FA F080 (= 50 000 000) Written with 0x1DCD 6500 (= 500 0000) Written with 0x1DCD 6500 (= 500 0000) Written with 0x1DCD 6500 (= 500 0000 000) Written with 0x403 F200 Written with	Seen only in startup sequence [-1V;1V]: OffestDW=2047+(4094/6,21)*Offset outside ]-1;1V[: OffsetDW=2047+(4094/6,21)*Offset  [0V;0.5000V]: AmpDW=Amp*3685/0,5 [0.5001V;5.0000V]: AmpDW=Amp*3685/0 [0.5001V;5.0000V]: AmpDW=Amp*3685/0,5 [0.5001V;5.0000V]: AmpDW=Amp*3685/0,5	To be identified. Low priority 4094 is a best match but not logical (DAC has 4096 steps)  Just for offset = 0. Ranges depend on offset value too  Just for offset = 0. Ranges depend on
26 27 28 29 2A 2B 2C 2C 2D 2E 2F 30 31 31	1 OffsetDW 2 OffsetDW 1 AmpDW 2 AmpDW	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 8, Mod Rate(Reg 0x36) then 18 BURST = 10, Burst Number (Reg 0x10) then 0 FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then A0 two times FM with CH2, T(VCO-IN) source = 30 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with EXT, (VCO-IN) source = 50, PM BIAS (Reg 0x3A) then 50 Written with 0x2FA F080 (= 50 000 000) Written with 0x1DCD 6500 (= 500 0000) Written with 0x1DCD 6500 (= 500 0000) Written with 0x1DCD 6500 (= 500 0000 000) Written with 0x403 F200 Written with	Seen only in startup sequence [-1V;1V]: OffestDW=2047+(4094/6,21)*Offset outside ]-1;1V[: OffsetDW=2047+(4094/6,21)*Offset  [0V;0.5000V]: AmpDW=Amp*3685/0,5 [0.5001V;5.0000V]: AmpDW=Amp*3685/0 [0.5001V;5.0000V]: AmpDW=Amp*3685/0,5 [0.5001V;5.0000V]: AmpDW=Amp*3685/0,5	To be identified. Low priority 4094 is a best match but not logical (DAC has 4096 steps)  Just for offset = 0. Ranges depend or offset value too  Just for offset = 0. Ranges depend or
26 27 28 29 2A 2B 2C 2D 2E 2E 2F 30 31 32 33	1 OffsetDW 2 OffsetDW 1 AmpDW 2 AmpDW	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 8, Mod Rate(Reg 0x36) then 18 BURST = 10, Burst Number (Reg 0x10) then 0 FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then A0 two times FM with CH2, T(VCO-IN) source = 30 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with EXT, (VCO-IN) source = 50, PM BIAS (Reg 0x3A) then 50 Written with 0x2FA F080 (= 50 000 000) Written with 0x1DCD 6500 (= 500 0000) Written with 0x1DCD 6500 (= 500 0000) Written with 0x1DCD 6500 (= 500 0000 000) Written with 0x403 F200 Written with	Seen only in startup sequence [-1V;1V]: OffestDW=2047+(4094/6,21)*Offset outside ]-1;1V[: OffsetDW=2047+(4094/6,21)*Offset  [0V;0.5000V]: AmpDW=Amp*3685/0,5 [0.5001V;5.0000V]: AmpDW=Amp*3685/0 [0.5001V;5.0000V]: AmpDW=Amp*3685/0,5 [0.5001V;5.0000V]: AmpDW=Amp*3685/0,5	To be identified. Low priority 4094 is a best match but not logical (DAC has 4096 steps)  Just for offset = 0. Ranges depend or offset value too  Just for offset = 0. Ranges depend or
26 27 28 29 2A 2B 2C 2D 2E 2E 2F 30 31 31	1 OffsetDW 2 OffsetDW 1 AmpDW 2 AmpDW	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 8, Mod Rate(Reg 0x36) then 18 BURST = 10, Burst Number (Reg 0x10) then 0 FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then A0 two times FM with CH2, T(VCO-IN) source = 30 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with EXT, (VCO-IN) source = 50, PM BIAS (Reg 0x3A) then 50 Written with 0x2FA F080 (= 50 000 000) Written with 0x1DCD 6500 (= 500 0000) Written with 0x1DCD 6500 (= 500 0000) Written with 0x1DCD 6500 (= 500 0000 000) Written with 0x403 F200 Written with	Seen only in startup sequence [-1V;1V]: OffestDW=2047+(4094/6,21)*Offset outside ]-1;1V[: OffsetDW=2047+(4094/6,21)*Offset  [0V;0.5000V]: AmpDW=Amp*3685/0,5 [0.5001V;5.0000V]: AmpDW=Amp*3685/0 [0.5001V;5.0000V]: AmpDW=Amp*3685/0,5 [0.5001V;5.0000V]: AmpDW=Amp*3685/0,5	To be identified. Low priority 4094 is a best match but not logical (DAC has 4096 steps)  Just for offset = 0. Ranges depend on offset value too  Just for offset = 0. Ranges depend on
26 27 28 29 2A 2B 2C 2D 2E 2F 30 31 32 33 34 4 35 36 36	1 OffsetDW 2 OffsetDW 1 AmpDW 2 AmpDW	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 8, Mod Rate(Reg 0x36) then 18 BURST = 10, Burst Number (Reg 0x10) then 0 FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then A0 two times FM with CH2, T(VCO-IN) source = 30 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with EXT, (VCO-IN) source = 50, PM BIAS (Reg 0x3A) then 50 Written with 0x2FA F080 (= 50 000 000) Written with 0x1DCD 6500 (= 500 0000) Written with 0x1DCD 6500 (= 500 0000) Written with 0x1DCD 6500 (= 500 0000 000) Written with 0x403 F200 Written with	Seen only in startup sequence [-1V;1V]: OffestDW=2047+(4094/6,21)*Offset outside ]-1;1V[: OffsetDW=2047+(4094/6,21)*Offset  [0V;0.5000V]: AmpDW=Amp*3685/0,5 [0.5001V;5.0000V]: AmpDW=Amp*3685/0 [0.5001V;5.0000V]: AmpDW=Amp*3685/0,5 [0.5001V;5.0000V]: AmpDW=Amp*3685/0,5	To be identified. Low priority 4094 is a best match but not logical (DAC has 4096 steps)  Just for offset = 0. Ranges depend on offset value too  Just for offset = 0. Ranges depend on
26 27 28 29 2A 2B 2C 2D 2E 2F 30 31 32 33 34 35 36 37 37	1 OffsetDW 2 OffsetDW 1 AmpDW 2 AmpDW 2 DutyDW 2 DutyDW	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 8, Mod Rate(Reg 0x36) then 18 BURST = 10, Burst Number (Reg 0x10) then 0 FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then A0 two times FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with EXT. (VCO-IN) source = 50, PM BIAS (Reg 0x3A) then 50 Written with 0x02FA F080 (= 50 000 000) Written with 0x02FA F080 (= 500 000 000) Written with 0x02FA F080 (= 500 000 000) Written with 0x2A05 F200 Written with 0x2A05 F200 Written with 0x4B05 F200 Written with 0x4B31 A000  [0V;0.5000V]: Amp=0.5xAmpDW/3685 [0.5001V;5.0000V]: Amp=20xAmpDW/3685 [0.5001V;5.0000V]: Amp=20xAmpDW/3685 [0.5001V;5.0000V]: Amp=20xAmpDW/3685 [0.5001V;5.0000V]: Amp=20xAmpDW/3685 [0.000%=0, 100.000% = 1FFFF 0.000%=0, 100.000% = 1FFFF	Seen only in startup sequence [-1v;1v]: OffsetDW=2047+(4094/6,21)*Offset outside ]-1;1v[: OffsetDW=2047+(4094/24)*Offset [0v;0.5000v]: AmpDW=Amp*3685/0,5 [0.5001v;5.0000v]: AmpDW=Amp*3685/0,5 [0.5001v;5.0000v]: AmpDW=Amp*3685/0,5 [0.5001v;5.0000v]: AmpDW=Amp*3685/5 [5.0001v;20.0000v]: AmpDW=Amp*3685/5	To be identified. Low priority 4094 is a best match but not logical (DAC has 4096 steps)  Just for offset = 0. Ranges depend on offset value too  Just for offset = 0. Ranges depend on offset value too
26 27 28 29 2A 2B 2C 2D 2E 2E 2F 30 31 32 33 34 35 36 36 37 38	1 OffsetDW 2 OffsetDW 1 AmpDW 2 AmpDW 2 DutyDW 2 DutyDW	No Modulation = 0 PSK = 1 two times FSK = 2, thop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 8, Mod Rate(Reg 0x36) then 18 BURST = 10, Burst Number (Reg 0x10) then 0 FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then A0 two times FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times FM with CH2 source = 40, PM BIAS (Reg 0x34) then 40 PM with CH2. (VCO-IN) source = 50, PM BIAS (Reg 0x3A) then 50 Written with 0x02FA F080 (= 50 000 000) Written with 0x10C 0500 (= 500 000 000) Written with 0x10C 0500 (= 500 000 000) Written with 0x2A0F F200 Written with 0x2A0F F200 Written with 0x420F F200 Written with 0x4931 A000  [0V;0.5000V]: Amp=0.5xAmpDW/3685 [0.5001V;2.0000V]: Amp=20xAmpDW/3685 [0.5001V;2.0000V]: Amp=20xAmpDW/3685 [0.5001V;2.0000V]: Amp=20xAmpDW/3685 [0.5001V;2.0000V]: Amp=20xAmpDW/3685 [0.5001V;2.0000V]: Amp=20xAmpDW/3685 [0.5001V;2.0000V]: Amp=20xAmpDW/3685 [0.000%=0, 100.000% = 1FFFF  0.00%=0, 100.00% = 3FFF  Written with 7FF DFFF	Seen only in startup sequence [-1y.1V]: OffestDW=2047+(4094/6,21)*Offset outside ]-1;1V[: OffsetDW=2047+(4094/6,21)*Offset [OV;0.5000V]: AmpDW=Amp*3685/0,5 [0.5001V;5.0000V]: AmpDW=Amp*3685/5 [5.0001V;20.0000V]: AmpDW=Amp*3685/5 [5.0001V;20.0000V]: AmpDW=Amp*3685/20  written after Reg 0x05 Channel state	To be identified. Low priority Under the construction of the
26 27 28 29 24 26 27 28 29 20 20 20 21 30 31 32 33 34 35 36 37	1 OffsetDW 2 OffsetDW 1 AmpDW 2 AmpDW 2 DutyDW 2 DutyDW	No Modulation = 0 PSK = 1 two times FSK = 2, Hop frequency(Reg 0x23 & 0x22) then 82 ASK = 4 two times AM with CH2 source = 8, Mod Rate(Reg 0x36) then 8 AM with CH2 source = 8, Mod Rate(Reg 0x36) then 18 BURST = 10, Burst Number (Reg 0x10) then 0 FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then A0 two times FM with CH2 source = 20 two times, FM BIAS (Reg 0x23 & 0x22) then B0 two times PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with CH2 source = 40, PM BIAS (Reg 0x3A) then 40 PM with EXT. (VCO-IN) source = 50, PM BIAS (Reg 0x3A) then 50 Written with 0x02FA F080 (= 50 000 000) Written with 0x02FA F080 (= 500 000 000) Written with 0x02FA F080 (= 500 000 000) Written with 0x2A05 F200 Written with 0x2A05 F200 Written with 0x4B05 F200 Written with 0x4B31 A000  [0V;0.5000V]: Amp=0.5xAmpDW/3685 [0.5001V;5.0000V]: Amp=20xAmpDW/3685 [0.5001V;5.0000V]: Amp=20xAmpDW/3685 [0.5001V;5.0000V]: Amp=20xAmpDW/3685 [0.5001V;5.0000V]: Amp=20xAmpDW/3685 [0.000%=0, 100.000% = 1FFFF 0.000%=0, 100.000% = 1FFFF	Seen only in startup sequence [-1v;1v]: OffsetDW=2047+(4094/6,21)*Offset outside ]-1;1v[: OffsetDW=2047+(4094/24)*Offset [0v;0.5000v]: AmpDW=Amp*3685/0,5 [0.5001v;5.0000v]: AmpDW=Amp*3685/0,5 [0.5001v;5.0000v]: AmpDW=Amp*3685/0,5 [0.5001v;5.0000v]: AmpDW=Amp*3685/5 [5.0001v;20.0000v]: AmpDW=Amp*3685/5	To be identified. Low priority 4094 is a best match but not logical (DAC has 4096 steps)  Just for offset = 0. Ranges depend on offset value too  Just for offset = 0. Ranges depend on offset value too