



2.4 Operands and Effective Address



- Add R1, R2, R3 ; $R1 \leftarrow R2 + R3$
 - **Operands:** contents of R2 and R3; Add; Result in R1
- Store R2, SUM ; $[SUM] \leftarrow R2$
 - **Operand:** content of R2; Store content of R2 in memory address SUM
- Load R1, NUM ; $R1 \leftarrow [NUM]$
 - **Operand:** content of [NUM]; Load R1 with content of [NUM]
- **Effective Address EA:** where the operand is

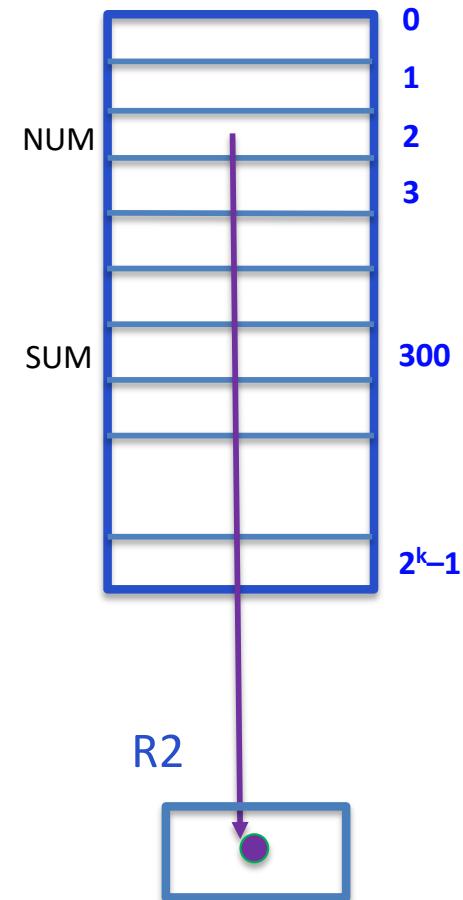
[x]:=M[x]
**=content of
memory location x**



2.4.1 Direct or Absolute Naming



- Declaration in high-level language:
 - Integer NUM, SUM (user-defined symbols)
- Assembly level:
 - Specify locations by programmer or compiler
 - Load R2, NUM; (*Direct or Absolute*)
 - Constant or Variable is [NUM]



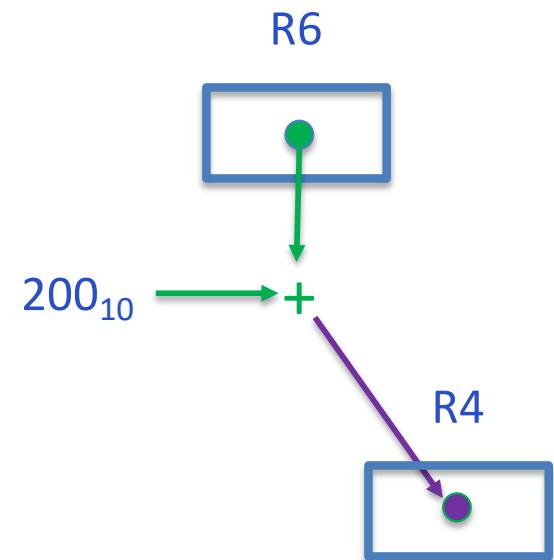


Register and Immediate



- Adding constant 200_{10} to a variable
 - Example:
 - Use Immediate with # sign
 - Add R4, R6, #200; (Register: R6)

Variable





2.4.2 Indirection and Pointers



- Load R2, (R5)

- ***Register Indirect***: EA=[R5];
- R5 contains the address of the operand and is a pointer
- Example: Used as an index (pointer) to a 1-D array (a list)

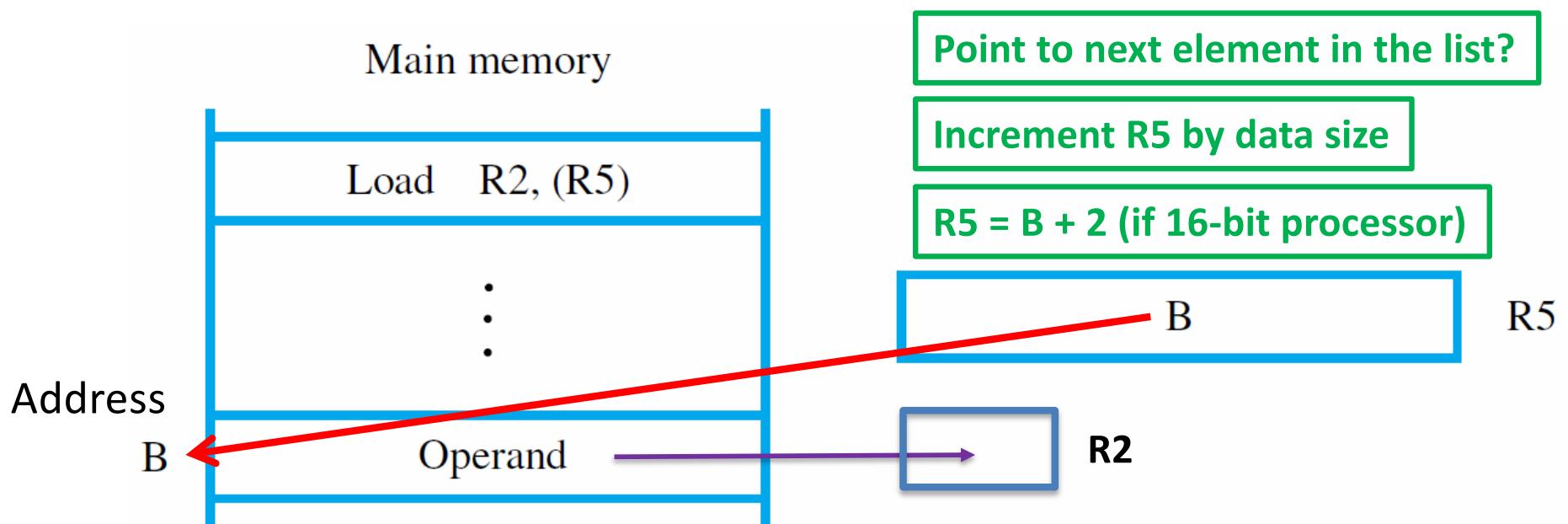




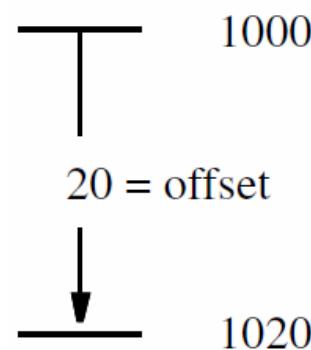
Fig. 2.9(b) Offset and Index



Load $R_i, X(R_j)$

$R_i \leftarrow [X + R_j]$

$EA = R_j + X$



Offset from a reference point or base 1000_{10}

Load $R2, 1000(R5)$

⋮
⋮
 $X=1000$

⋮
⋮
Operand

$R5 = \text{index register}$

20

Increment $R5$ by data size for next element in the list/array

$R5 = R5 + 2$ (if 16-bit processor)

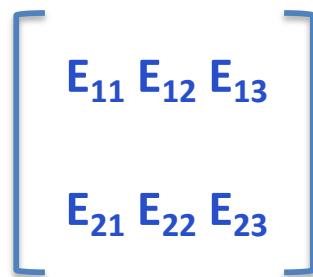
(b) Offset is in the index register



2.4.3 Indexing and Arrays



- Load R2, X(R5); e.g., Load R2, 1000(R5)
 - Index: EA=[R5]+X ; base X, index R5
- Load R2, (R5,R6)
 - Base with Index: EA=[R5]+[R6] ; base R5 index R6, or, index R6 base R5
- Assume: A 2 X 3 Matrix is in the Memory



E₁₁ Address A
E₁₂ Address A+4
E₁₃ Address A+8
E₂₁ Address A+12
E₂₂ Address A+16
E₂₃ Address A+20

```
.align 4:  
address  
-100  
-104  
-108
```



Indexing Examples/Exercise



- 1: Use “X(R5)” to access matrix elements

Load first row into R2, R3, R4

E ₁₁	Address A
E ₁₂	Address A+4
E ₁₃	Address A+8
E ₂₁	Address A+12
E ₂₂	Address A+16
E ₂₃	Address A+20

- 2: Use “(R5,R6)” to access matrix elements

Load first row into R2, R3, R4