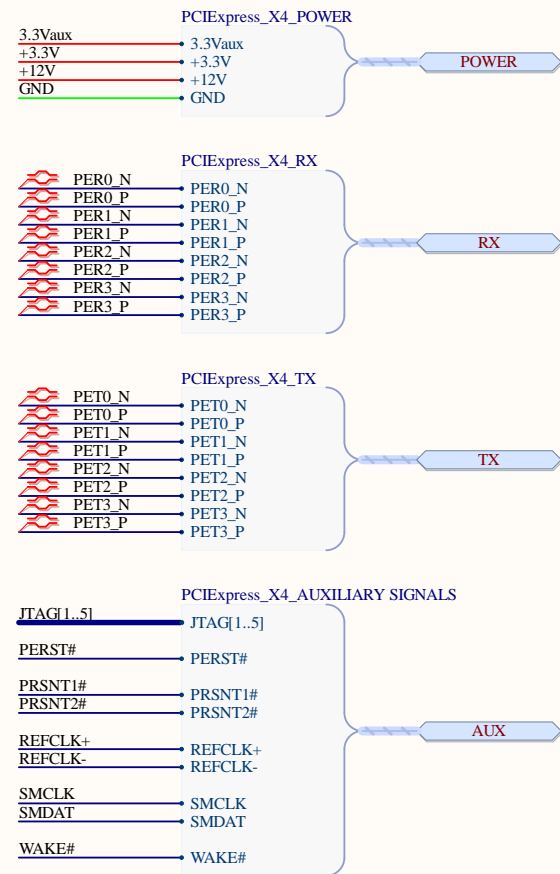
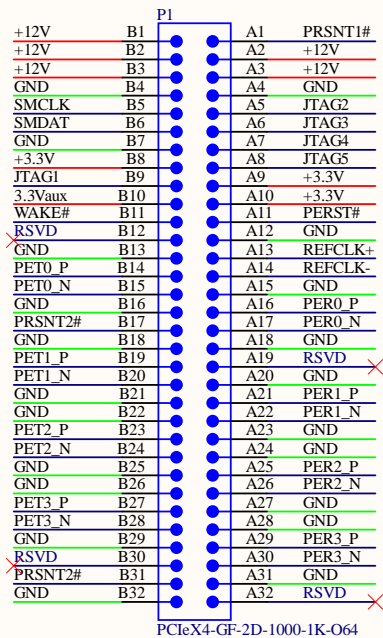

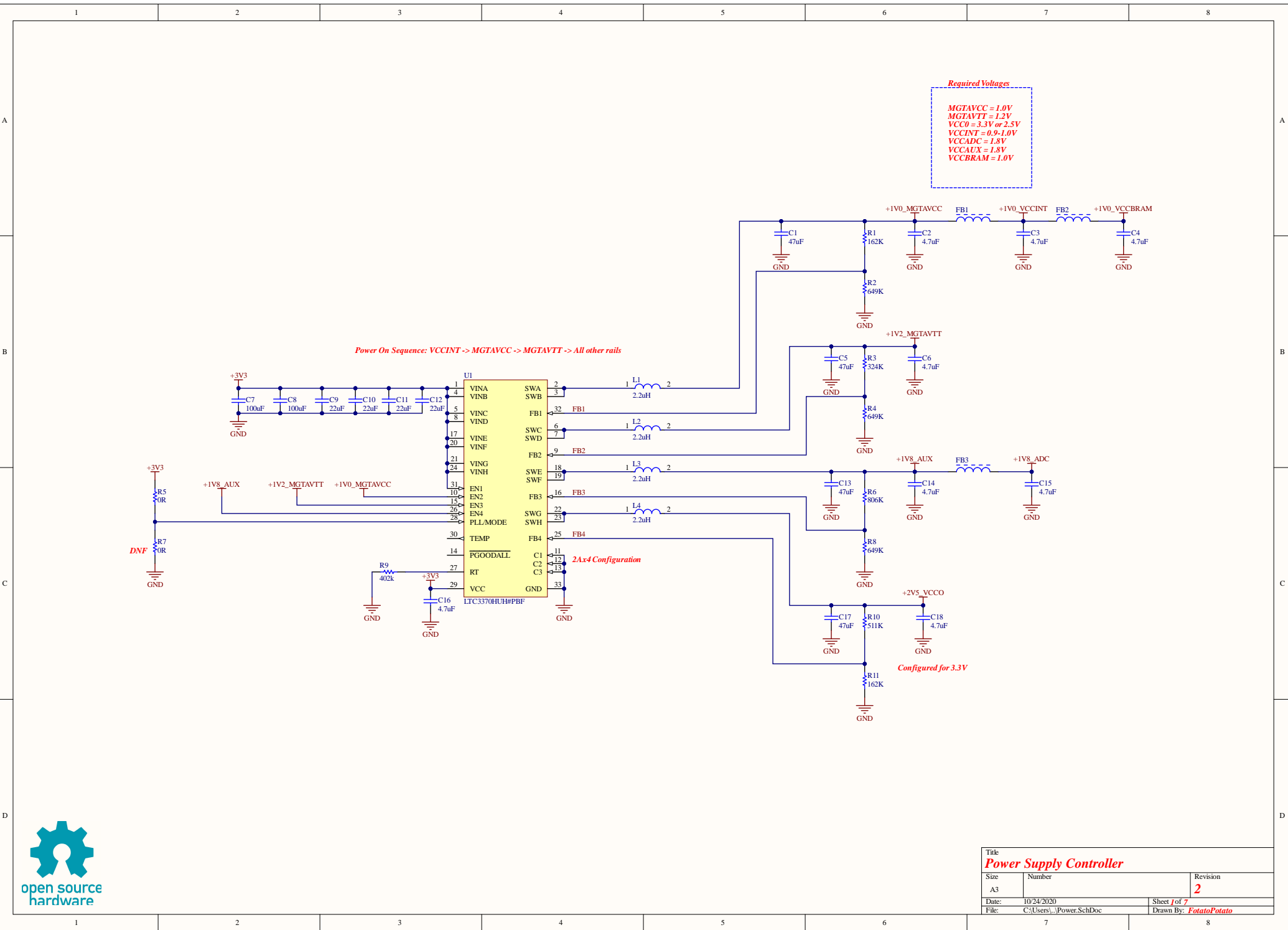


- Nominal values used, dimensions in mm
 - The mounting holes and keep-out areas around them are only required when the I/O bracket is mounted on the card directly
 - Component height rule and clearance rule derived from PCI_Express_CEM_r2.0.pdf, Page 84.
 - Stackup is not specified in PCI_Express_CEM_r2.0.pdf, nor implemented in this template.

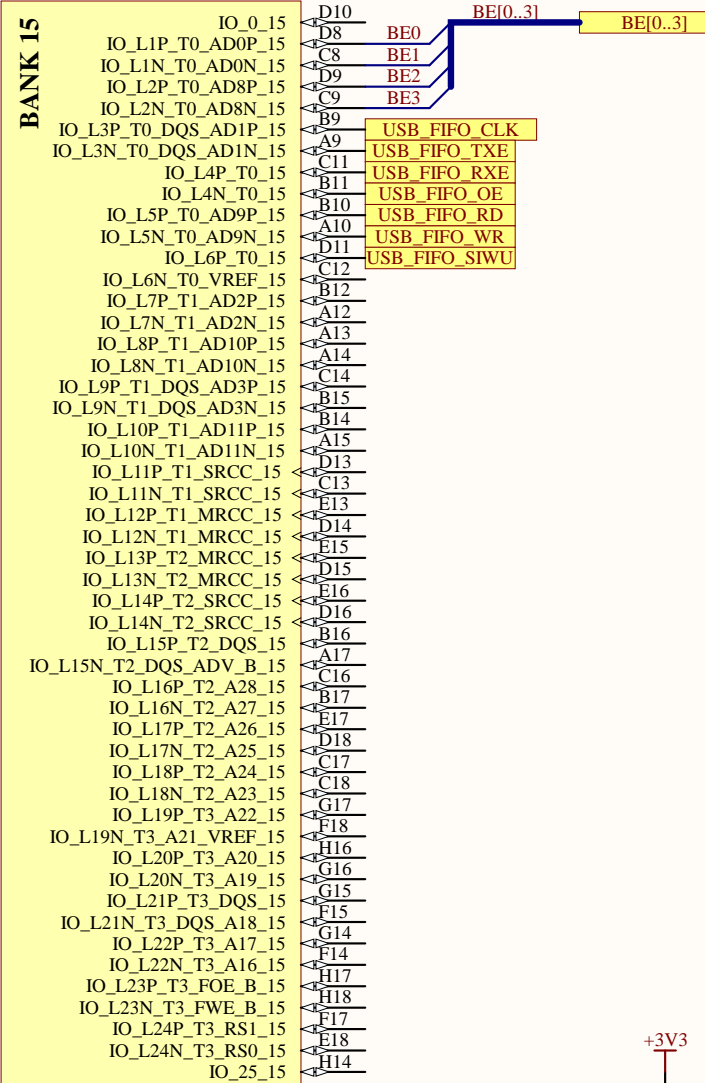


Sheet Title <i>PCI Express x4 64Pin</i>			<i>Altium</i> IBP Shanghai Level 3 - Building 3 No.168 Linhong Road Shanghai 200335, China	
Project Title <i>PCI Express x4 Low profile card</i>				
Size: <i>A4</i>	Item:	Revision:		
Date: <i>10/24/2020</i>	Time: <i>1:21:08 AM</i>	Sheet * of *		
File: <i>CON PCI Express X4 64PIN.SchDoc</i>				

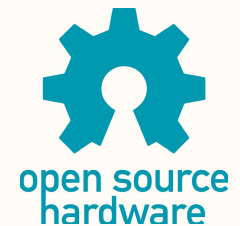
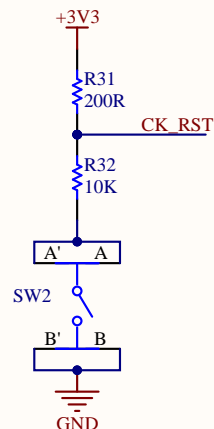
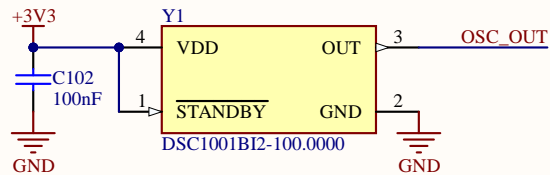
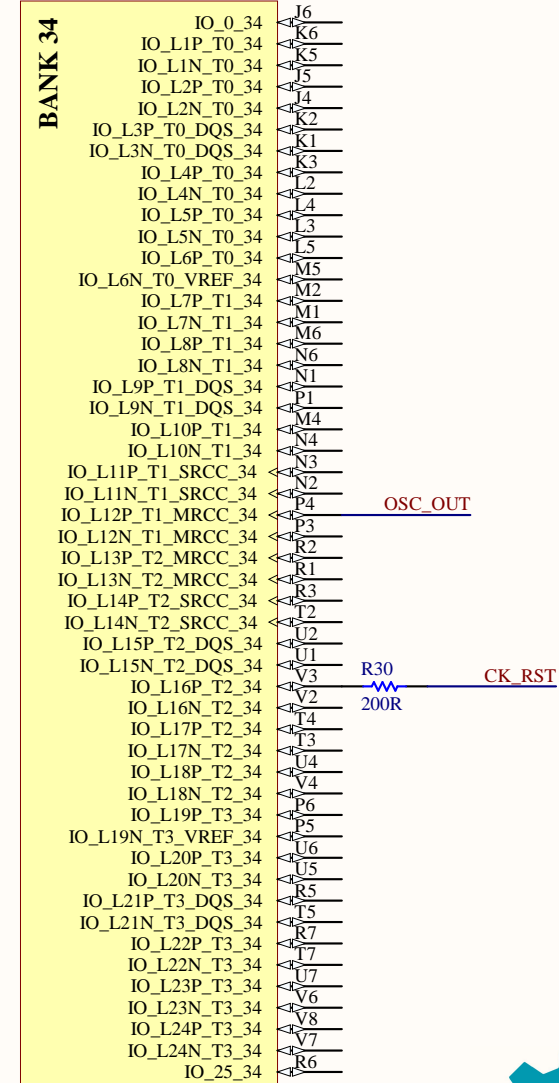


Title		
Power Supply Controller		
Size	Number	Revision
A3		2
Date:	10/24/2020	Sheet 1 of 7
File:	C:\Users\...Power.SchDoc	Drawn By: FotatoPotato

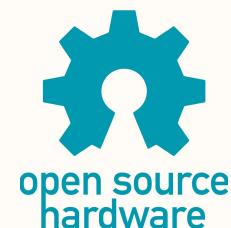
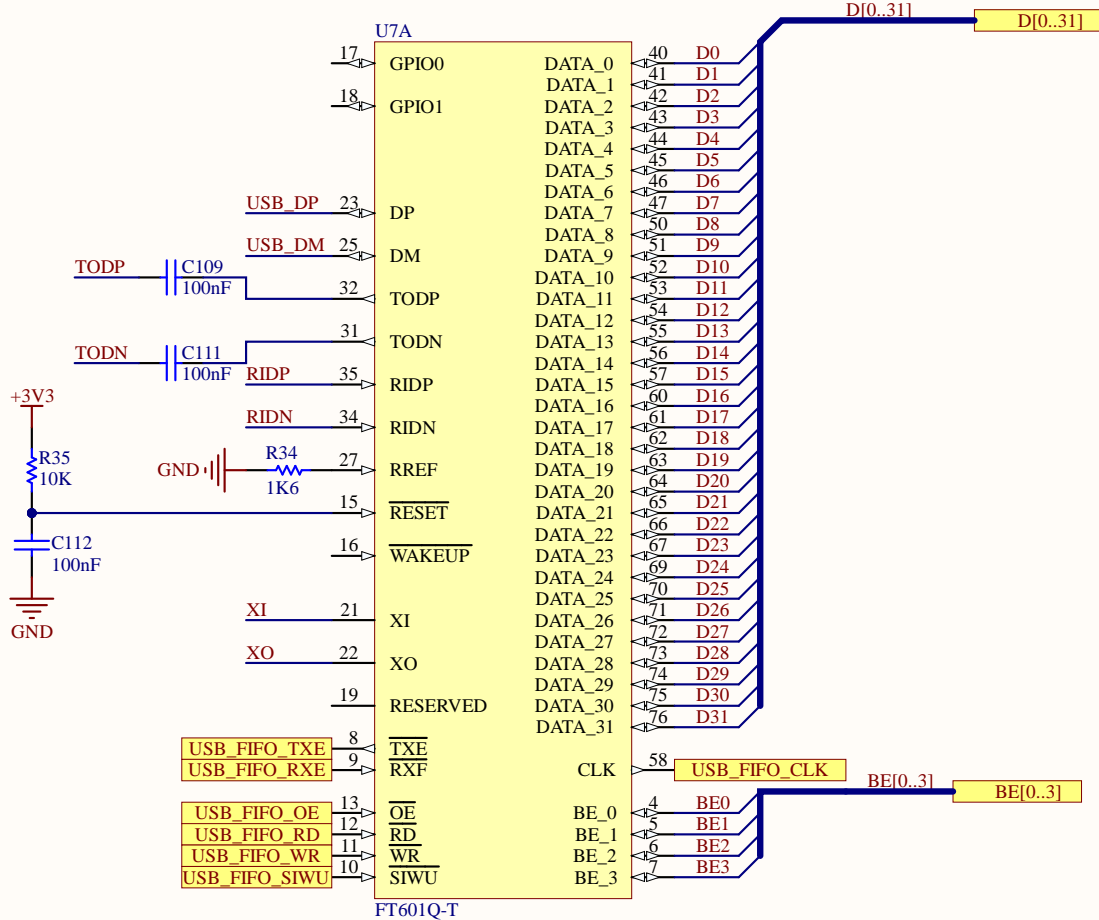
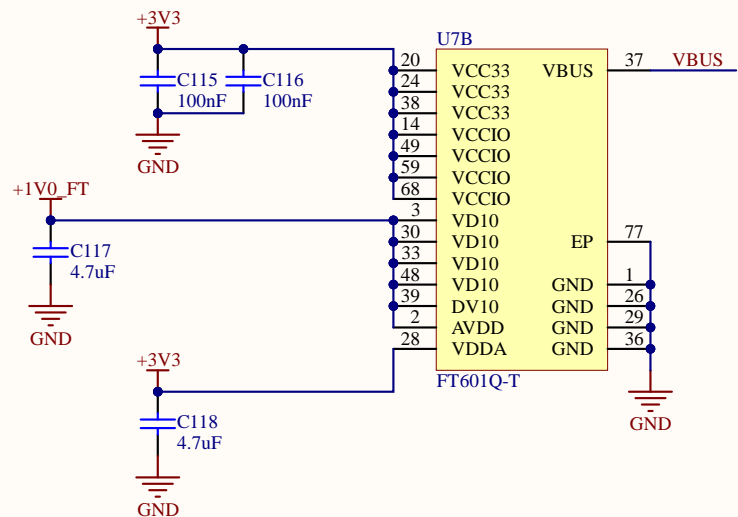
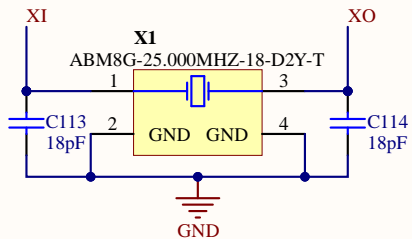
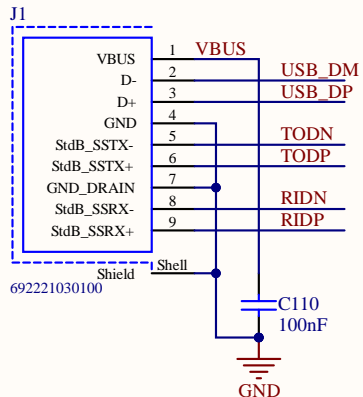
U2B



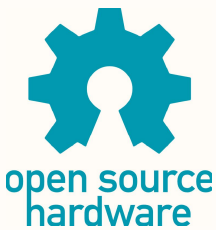
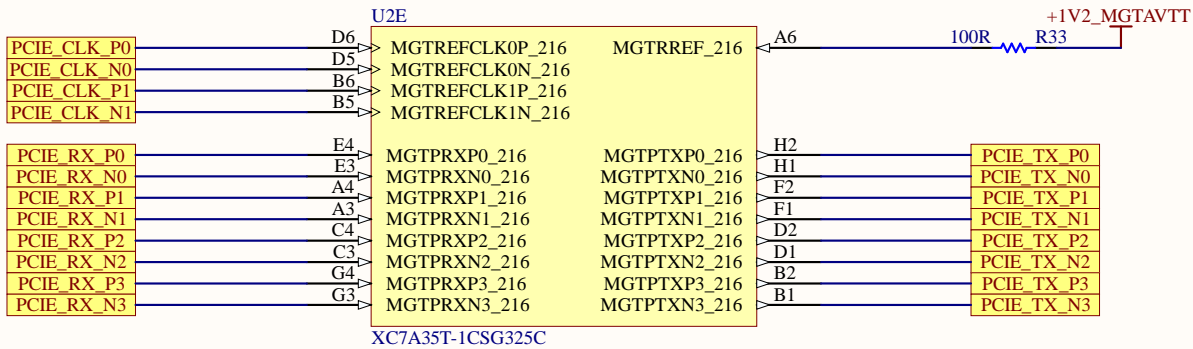
U2C



Title FPGA GPIO/CLK		
Size A	Number	Revision 2
Date: 10/24/2020	Sheet 4 of 7	
File: C:\Users\...\FPGA_IO.SchDoc	Drawn By: FotatoPotato	



Title FT601 USB Controller		
Size A	Number	Revision 2
Date:	10/24/2020	Sheet 5 of 7
File:	C:\Users\...\USB_Controller.SchDoc	Drawn By: FotatoPotato



Title		
FPGA PCIE		
Size	Number	Revision
A		2
Date:	10/24/2020	Sheet 6 of 7
File:	C:\Users\...\FPGA_PCIE.SchDoc	Drawn By: FotatoPotato