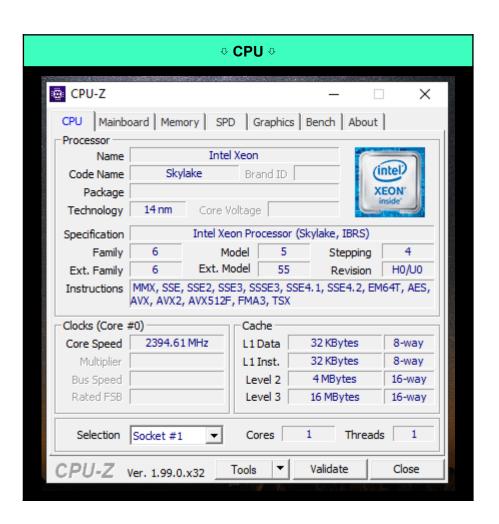
## **RESPOSTAS**

## Aplicativo CPU-Z no computador virtual disponibilizado pela Descomplica:



	⊅ MAI						
CPU-Z					X		
CPU Mainboard Memory SPD Graphics Bench About							
- Motherboard							
Manufacturer							
	OpenStack Nova						
Bus Specs,							
Chipset				Rev.			
Southbridge				Rev.			
LPCIO							
BIOS							
Brand	SeaBIOS						
	1.11.0-2.el7						
Date	04/01/2014						
-Graphic Interf	ace						
	Bus						
Transfe		Max, Sup	oported [				
Side Band Add	ressing	Max, Sup	oported				
CPU-Z	/er. 1.99.0.x32	Tools ▼	Validate		lose		
CPU-Z	/er. 1.99.0.x32	Tools 🔻	Validate	C	lose		
CPU-Z		Tools ▼	Validate	C	lose		
			Validate	C	lose		
			Validate —	C	lose		
CPU-Z		EMORY &	_				
CPU   Mainbo	<b>⋄ M</b> I	EMORY 4	Bench Ab				
CPU-Z CPU   Mainbo		EMORY &	Bench Ab				
CPU-Z  CPU   Mainbo	ard Memory SPI	EMORY &  Graphics   1  Channel DC Mod	Bench Ab				
CPU   Mainbo General Type Size	ard Memory SPI	EMORY &	Bench Ab				
CPU-Z CPU   Mainbo	oard Memory SPI	Channel DC Mod	Bench Ab				
CPU   Mainbo General Type Size	Pard Memory SPI  92 GBytes  DRAM Frequency	Channel DC Moo	Bench Ab				
CPU   Mainbo General Type Size	oard Memory SPI  92 GBytes  DRAM Frequency FSB;DRAM	Channel DC Mod	Bench Ab				
CPU-Z  CPU   Mainbo	Pard Memory SPI  92 GBytes  DRAM Frequency FSB;DRAM CAS# Latency (CL)	Channel DC Moo	Bench Ab				
CPU-Z  CPU   Mainbo General  Type Size  Timings	DRAM Frequency FSB:DRAM CAS# Latency (CL)	Channel DC Mod	Bench Ab				
CPU-Z  CPU   Mainbo General   Type   Size   Timings   RAS# to	Pard Memory SPI  92 GBytes  DRAM Frequency FSB;DRAM CAS# Latency (CL)	Channel DC Mod	Bench Ab				
CPU-Z  CPU   Mainbo General	DRAM Frequency FSB;DRAM CAS# Latency (CL) CAS# Delay (tRCD)	Channel DC Moo	Bench Ab				
CPU-Z  CPU   Mainbo General  Type   Size    Timings    RAS# to	DRAM Frequency FSB:DRAM CAS# Latency (CL) CAS# Delay (tRCD) AS# Precharge (tRP) Cycle Time (tRAS)	Channel DC Mod	Bench Ab				
CPU-Z  CPU   Mainbo General  Type   Size    Timings    RAS# to	DRAM Frequency FSB:DRAM CAS# Latency (CL) CAS# Delay (tRCD) AS# Precharge (tRP) Cycle Time (tRC)	Channel DC Mod	Bench Ab				
CPU-Z  CPU   Mainbo General   Type   Size   Timings   RAS# to	DRAM Frequency FSB;DRAM CAS# Latency (CL) CAS# Delay (tRCD) AS# Precharge (tRP) Cycle Time (tRAS) ank Cycle Time (tRC) DRAM Idle Timer otal CAS# (tRDRAM)	Channel DC Mod	Bench Ab				
CPU-Z  CPU   Mainbo General   Type   Size   Timings   RAS# to	DRAM Frequency FSB:DRAM CAS# Latency (CL) CAS# Delay (tRCD) AS# Precharge (tRP) Cycle Time (tRAS) ank Cycle Time (tRC) DRAM Idle Timer	Channel DC Mod	Bench Ab				
CPU-Z  CPU   Mainbo General Type   Size RAS# to	DRAM Frequency FSB;DRAM CAS# Latency (CL) CAS# Delay (tRCD) AS# Precharge (tRP) Cycle Time (tRAS) ank Cycle Time (tRC) DRAM Idle Timer otal CAS# (tRDRAM)	Channel DC Mod	Bench Ab				
CPU-Z CPU   Mainbo	DRAM Frequency FSB:DRAM CAS# Latency (CL) CAS# Delay (tRCD) AS# Precharge (tRP) Cycle Time (tRAS) ank Cycle Time (tRC) DRAM Idle Timer otal CAS# (tRDRAM) ow To Column (tRCD)	Channel DC Mod	Bench Ab	pout			

