

Clayton Skaggs

(512) 923-0688

clayton.skaggs89@gmail.com

<https://www.claytonskaggs.dev/>

U.S. Citizen

Work Experience

Microchip Technology

February 2018 - June 2018

Product Test Engineer – Chandler, AZ

Responsible for ensuring the testability and manufacturability of integrated circuits from the component feasibility stage through production ramp.

- Coordinate between multiple teams to insure products meeting quality, specification, and manufacturing yield requirements. .
- Analyzes and evaluates component specification versus performance to ensure optimal match of component requirements with production equipment capability with specific emphasis on yield analysis.

Intel Corp

July 2012- Aug 2017

Product Development Engineer – Test Content Development- Santa Clara, CA

Mar 2016 – Aug 2017

Responsible for architecture definitions, circuit and logic design/development recommendation for DFT implementations, verification and validation model development, functional and structural test content development.

- Ensuring the testability and manufacturability of high speed serial I/O (KTI/PCIE) content from architectural design through to production ramp for server CPUs on the latest generation process node.
- Test content generation of patterns to support HVM testers.
- Silicon debug to identify functional and DFT related bugs and silicon characterization to validate IP.
- Support platform and system level validation teams to identify and close silicon issues.
- Coordinate with internal teams to deliver high quality analog test content and support improving product health indicators.
- Utilize Agile/SCRUM methodologies to improve the quality and cadence of deliverables.

Product Development Engineer – Test Content Integration - Phoenix, AZ

Jul 2012 – Mar 2016

Responsible for ensuring the testability and manufacturability of integrated circuits from the component feasibility stage through production ramp.

- Make significant contributions to design, development and validation of circuits. Including evaluation, development and debug of complex test methods.
- Designs, validates, and debugs production test hardware.
- Analyzes and evaluates component specification versus performance to ensure optimal match of component requirements with production equipment capability with specific emphasis on yield analysis and bin split capability.
- Creates and applies concepts for optimizing component production relative to both quality and cost constraints.
- Utilize Agile/SCRUM methodologies to improve the quality and cadence of deliverables.

Certification

- AWS
 - Certified Developer - Associate (Nov 2017)
 - Certified Cloud Practitioner (Nov 2019)
- UC Irvine
 - Coding Boot Camp (Aug 22)

Education

Bachelor of Science Computer Engineering
Oklahoma State University; Stillwater, OK

May 2012
GPA: 3.02

Research Assistant

Oklahoma State University
VLSI Computer Architecture Research Group, Dr. James E. Stine

Jan-May 2012