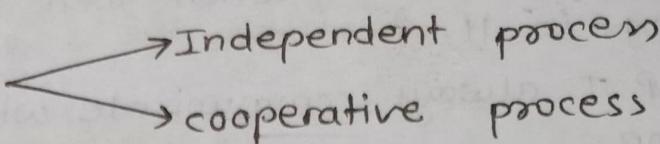


## \* CONCURRENCY CONTROL

## i) Process/thread synchronization and Mutual Exclusion:

## ii) process synchronization

Independent

- i) Execution of 1 process  
does not affect the execution  
of other process

PS problem arises in the case  
of cooperative process also  
because resource are shared  
in cooperative process.

Cooperative

- i) execution of 1 process  
affects the execution  
of other process

A process that can affect  
or be affected by other  
processes executing in the  
sys.

④ Process synchronisation is the task of coordinating the execution of processes in a way that no two processes access the same shared resources and data.

⑤ Thread synchronisation is defined as a mechanism which ensures that 2 or more concurrent processes or threads don't simultaneously execute some particular program segment known as a critical section.

## ⑥ Mutual exclusion :-

If a process is executing in its critical section, then no other process is allowed to execute in the critical section.

⑦ Semaphore :-

- signaling mechanism
- Integer value

Binary counting  
either 0 or 1  
have any value

## Concurrency :-

- Execution of multiple instruction sequences at the same time
- It happens in OS, when there are several process threads running in it.
- ~ P T always communicate with each other through SM or MP.
- Concurrency results in sharing of resources results in problem like deadlock & resource starvation.

~ - numpiny

## \* Problems in Concurrency :-

- 1) Sharing global resources → safely is difficult  
→ Then the order in which IF 2 P both make use of a global var various R & W op executed & both perform R & W on that var is critical.

## 2) Optimal allocation of resource :-

- It is difficult for the OS to manage the allocation of resource optimally.

## 3) Locating Programming errors :-

- It is very difficult to locate a programming error because reports are usually not reproducible.

## 4) Locking the channel :-

- It may be inefficient for the OS to simply lock the channel & prevent its use by other processes.

## \* Advantages of concurrency :-

SPL<sup>2</sup>

### 1) Running of multiple applications

- It enables to run multiple applications at the same time.

### 2) Better Resource Utilization :-

- It enables that the resources that are unused by one application can be used for the other application.

### 3) Better Average Response time :-

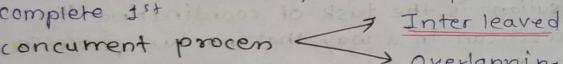
- Without CC, each appln has to be run to completion before the next one can be run.

### 4) Better performance :

- It enables the better performance by the OS.  
When 1 appln uses only the processor & another app uses only the disk drive then the time to run both concurrently to completion will be shorter than the time to run each application consecutively.

## Principle of Concurrency :-

- Multicore P & II Processing, allows multiple processes and threads to be executed simultaneously.
- M.P.s & threads can access the same M.S., D.V in code, or even R or W to the same <sup>memory storage</sup> ~~Device Var~~
- The amount of time it takes a process to execute cannot be simply estimated, and you cannot predict which process will complete 1st.

concurrent process  Interleaved Overlapping

It is impossible to predict the relative speed of execution and following factors determine it:

- 1) The way OS handles interrupts.
- 2) The other processes activities.
- 3) The OS's scheduling policies.

## Interleaved :-

Divides memory into small chunks.

[ It is used as ↑-level technique to solve memory issue for MB & chips.

overlapping :- A procedure in which a CS works on several programs, suspending work on a program & moving to another when it encounters an instruction for I/O or CPU

## Drawback of Concurrency :-

- It is required to protect multiple appn from one another.
- To coordinate multiple appn through additional mechanism.
- Add<sup>n</sup> performance overheads & complexities in OS are required for switching among appn.
- Sometimes running too many applications concurrently leads to severely degraded performance.

## Issues of Concurrency

- 1) Non-atomic : opn that are not atomic but interruptible by interrupt.
- 2) Race Condition - A RC occurs if the outcome depends on which of several processes gets to it first.
- 3) Blocking - P can be waiting for resource.
- 4) Starvation ; It occurs when a process doesn't obtain service to its needs.
- 5) Deadlock ; If 2 processes are blocked & hence neither can proceed to execute.

## Four Conditions for correct mutual Exclusion.

- 1) NO 2 processes simultaneously in critical section.
- 2) NO assumptions made out about speeds or no. of CPU's.
- 3) NO process running outside its region may block another process running in the critical region.
- 4) NO process must wait forever to enter its critical region.  
↳ waiting forever indicates deadlock condition.

## ① Requirements for Mutual Exclusion :-

- 1) Freedom from deadlocks : endless waiting due to circular wait.
- 2) Freedom from starvation : unbounded w. d. t. order of service policy.
- 3) Fairness : R. are served in order they are made.
- 4) Fault tolerance : Algo breaks if P die or messages are lost or garbled.
- 5) Safety : only 1 process/thread at a time inside CS.
- 6) Progress : if nobody has access and somebody wants in, somebody gets in.

## ② Mutual Exclusion : OS support (Semaphore & mutex)

M & S both provide synchronization service but they are not same.

- i) Mutex :-
  - is a mutual exclusion object.
  - that synchronizes access to a resource.
  - It is created with a unique name at the software level.
  - Is a locking mechanism.
  - makes sure only 1 thread can acquire the M at a time & enters CS.
  - This thread only releases the M when it exits the CS.

wait (mutex);      signal (mutex);  
↳ CS  
A binary semaphore as can be used as a mutex but a Mutex can never be used as a semaphore.

### ii) Semaphore :-

- ↳ Counting      Binary
- Is a signaling mechanism.
- A thread that is waiting on a semaphore can be signaled by another thread.
- uses 2 atomic operations
  - wait -- of S
  - IF S is 0 or -ve no operation is performed.
- wait (S)  

```
{ while(S<=0);  
    S++;  
}
```
- signal (S)  

```
{  
    S++;  
}
```

## Classical synchronization problems :-

i) Reader / Writers Problem.

ii) Producer and Consumer Problem.

iii) Inter - Process communication (Pipes, shared memory).

1) Reader / Writers Problem.

The R/W Problem related to an object such as a file that is shared between multiple process.

The R/W problem is used to manage synchronization so that there are no problem with the object data.

- There are four types of cases could happen :-

case	Process 1	Process 2	Allowed / Not Allowed
1	Writing	Writing	NA
2	Writing	Reading	NA
3	Reading	Writing	NA
4	Reading	Reading	A

Reader Process.

wait ( mutex );

rc++;

if ( rc == 1 )

wait ( wrt );

signal ( mutex );

Read db

wait ( mutex );

rc--;

if ( rc == 0 )

signal ( wrt );

signal ( mutex );

Writer Process.

wait ( wrt );

write db;

signal ( wrt );

ii) Producer - Consumer Problem

- A classic synchronization problem in the OS.

mutex = 1

empty = 5

full = 0

buffer [5], max-items = 5, input = 0

producer ()

{

while ( true )

{

down ( empty )

down ( mutex )

{

item = buffer [in] = item

in = ( in + 1 ) % N

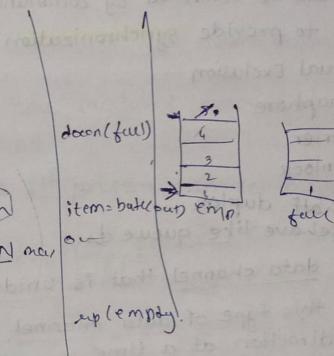
up ( mutex )

{

up ( full )

}

empty



## Inter Process Communication (Pipes, Shared Memory)

- IPC is used for exchanging useful information between numerous threads in one or more processes (or programs).

### Role of synchronization in IPC

Typically this is provided by IPC mechanism, but sometimes it can also be controlled by communication process.

### Methods to provide synchronization

- i) Mutual Exclusion
- ii) Semaphore
- iii) Barrier
- iv) Spinlock

half duplex

Pipe: Behave like queues.

Type of data channel that is unidirectional in nature

Data in this type of data channel can be moved in only a single direction at a time.

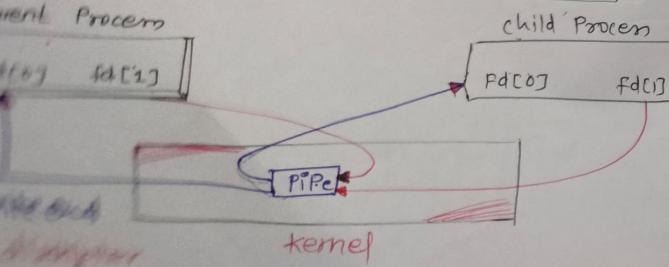
Still, one can use 2-channel of this type, so that can able to send & receive data in 2 process.

Typically it uses the standard i/p & o/p

These pipes are used in all types of Posix sys & diff ver. of glibc as well

is a mechanism by which the o/p of 1 P is directed i/p of another p

ie flow of data between 2 related P



### Why IPC

Computational

It helps to speed up modularity

Privilege Separation

Convenience

Help OS to communicate with each other & synchronize their action.

### • FIFO & I/O

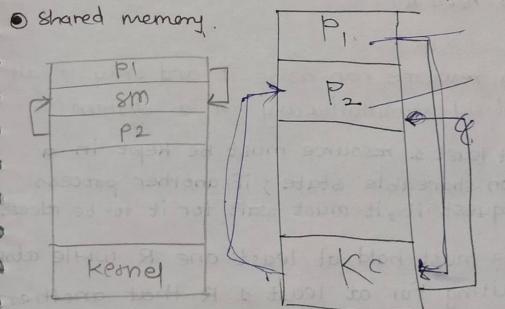
Communication between two unrelated processes.

It is full-duplex method

which means that the 1st process can communicate with the 2nd process

And the opposite can also happen.

### • Shared memory



## deadlock

### Principles of Deadlock

deadlock occurs when a process or thread enters waiting state because a requested system resource held by another waiting process, which in turn waiting for another resource held by another waiting process.

In communications sys, deadlocks occurs mainly due to lost or corrupt signals rather than resource contention.

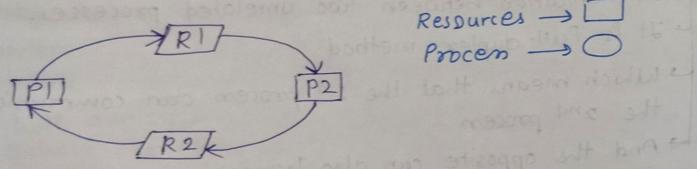


Fig: Both P need R

### Necessary conditions :-

deadlock situation on a resource can arise if and only if all following conditions hold simultaneously in a system.

**Mutual Exclusion :-** At least 1 resource must be kept in a non-shareable state; if another process request it, it must wait for it to be released.

**Hold and Wait :-** A P must hold at least one R while also waiting for at least 1 R that another P is currently holding.

**No preemption :-** Once a process holds a R (i.e. after its request is granted), that resource can't be taken away from that P until the P voluntarily release it.

**Circular Wait :-** There must be  $P_0, P_1, P_2, \dots, P_N$  such that every  $P_i$  is waiting for  $P_{(i+1)}$  Percent ( $N+1$ ).

### Deadlock modeling

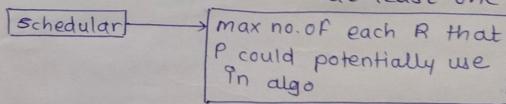
- As a collection of limited resources that can be divided into different categories and allocated to a variety of processes, each with different requirements.
- Memory, Pointers, CPUs, open files.
- The kernel keeps track of which resources are free and which are allocated, to which process they are allocated, and a queue of processes waiting for this resources to become available for all kernel managed resource.

### Methods to Handling deadlock

- 1) Deadlock Prevention # Deadlock Ignorance
- 2) Deadlock Avoidance ✓
- 3) Deadlock Detection
- 4) Deadlock Recovery

**1) Deadlock Prevention :-** we can prevent by avoiding one of 4 necessary conditions.

**2) Deadlock Avoidance :-** To avoid deadlocks by avoiding at least one of the aforementioned conditions



**3) Deadlock Detection :-** If it can't be avoided, another is to detect them & recover in

Aside from the performance hit of constantly checking for a policy/algorithm for recovering from DL must be in place & when P must be aborted or have their resources released there is the possibility of lost work.

**4) Recovery from Deadlock :-**

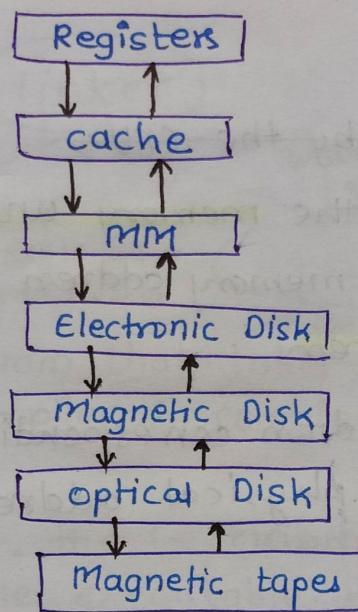
- 1) Inform the sys operator & give him/her permission to intervene
- 2) Stop 1 or more of the P. involved in the dl
- 3) Prevent the use of resources.

# Unit III : MEMORY MANAGEMENT

## \* Memory Management Requirement

### ◎ What is main memory ?

- A central to the operation of a modern computer.
- MM is a large array of words or bytes, ranging in size from 100's to billions.
- MM is a repository of rapidly available info shared by the CPU & I/O devices.
- MM is the place where programs and info are kept when the processor is effectively utilizing them.
- RAM, Volatile M  
↓  
lost data when Power interrupts occurs.



### ◎ What is Memory mgmt.

- The task of subdividing the memory among different processes is called memory management.
- Method in os to manage operations between mm & disk during Process execution.
- Aim of M.mgmt is to achieve efficient utilization of memory.

## Why M.mgmt is required?

- Allocate and de-allocate memory before and after process execution.
- To keep track of used memory space by process.
- To minimize fragmentation issues.
- To proper utilization of mm
- To maintain data integrity while executing of process

A address generated by the CPU is known as a "Logical Add"  
Also virtual add  
Logical add. space can be defined as the size of the process  
can be changed.

## Physical Address Space

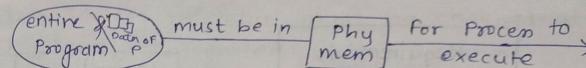
- An address generated by the CPU
- An address seen by the memory unit  
[one loaded into the memory address register of the memory]
- Known as real address.
- Set of all physical address corresponding to these logical address is known as physical address space.
- Remains constant
- PA computed by MMU
- RT mapping from V to Phy add is done by a two device MMU.

## 1) static loading -

- loading the entire program into a fixed address.
- Requires more memory space.

## 2) Dynamic loading

- To gain proper memory utilization, DE is used



- Size of a process is limited to the size of physical memory
- Routine is not loaded until it is called.
- All Routine are residing on disk in a relocatable load form
- Unused Routine never loaded.

## \* linker

### Static

- Faster Program start
- easy to debug
- Symbols are resolved at compile time

A linker is a program that takes one or more object files generated by a compiler & combines them into a single executable file.

- In static linking, the linker combines all necessary program modules into single executable program.
- so there is no runtime dependency.

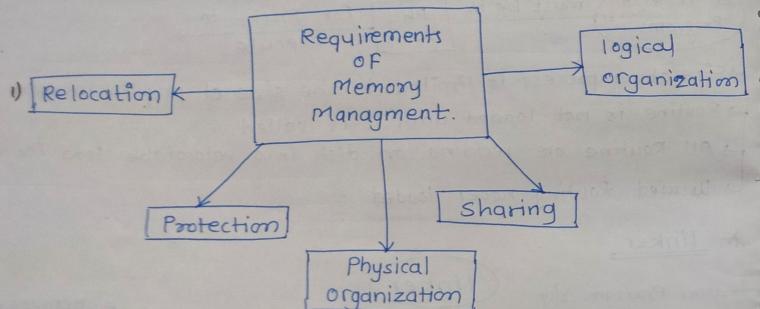
### Dynamic

when shared code found.  
PF can occur

- In Dynamic linking is similar to dynamic loading
- stub small piece of code in for each appropriate lib routine
- stub execute → checks if needed routine already in memory  
program loads the routine into memory.

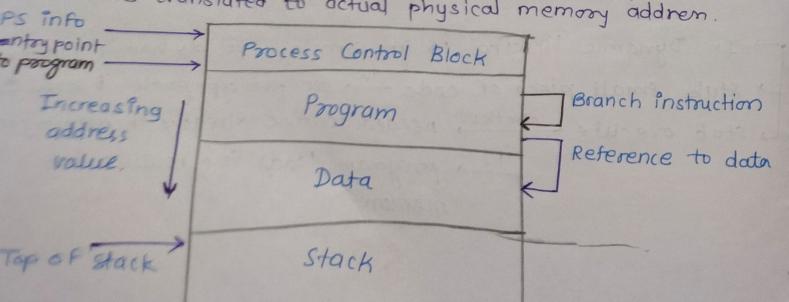
## \* Memory Mgmt Requirement 8-

- mgmt keeps track of the status of each memory location.
- It allocates the memory dynamically to the programs at their request and frees it for reuse when it is no longer needed
- Requirements



### i) Relocation

- Programmer cannot know where the program will be placed in memory when it executes.
- A process may be often relocated in main memory due to swapping.
- Swapping enables the OS to have a larger pool of ready-to-execute processes.
- Memory reference in code (for both instructions and data) must be translated to actual physical memory address.



### 2) Protection 3- Prevent P from interfering with OS or other P.

- P should be not able to reference memory locations in another P without permission.
- Impossible to check addresses at compile time in programs since the program could be relocated.
- Address reference must be checked at run time by h/w.  
↳ Often integrated with relocation.

### 3) sharing 8- Allow P to share data/ Program

- Must allow several processes to access a common portion of main memory without compromising protection.  
↳ Cooperating P's may need to share access to the same copy of the program rather than have their own separate copy.

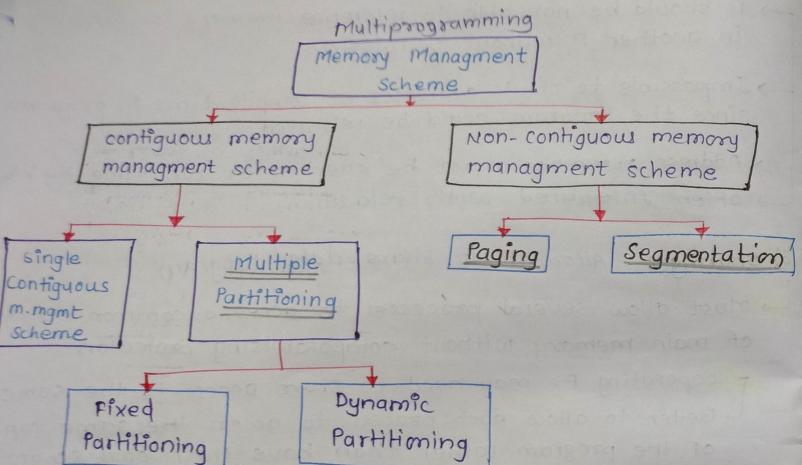
### 4) Logical Organization 8- support modules, shared subroutines.

- Users w/ programs in modules with diff. characteristics.  
↳ Instruction modules are execute-only.  
↳ Data modules are either R-only or R/W  
↳ Some modules are private others are public.
- To effectively deal with user programs, the OS & h/w should support a basic form of module to provide the required protection & sharing.

### 5) Physical Organisation 8- Manage mem ↔ Disk transfer

- SM is the long term store for programs and data while MM holds program & data currently in use.
- Moving info between these 2 levels of memory is a major concern of memory management.  
↳ It is 1<sup>st</sup> inefficient to leave this responsibility to the AP.

## \* Memory Partitioning 8-



Ways to organizing prog in mem.

Contiguous memory management scheme 8-

→ Contiguous memory allocation means assigning continuous blocks of memory to the process.  
low overhead, if no to find large enough block

Non-Contiguous memory management scheme 8-

easy to find hole in which seg fit chunks/called segment  
→ The program is divided into blocks (fixed size or variable) and loaded at different portions of memory.  
each seg can be placed in diff part of mem

Multiple Partitioning 8-

mem is divided in a no.of fixed-sized partitions where each partition should contain only 1 process.

Partitions are of equal size & all processes have same size.

Programs are assigned to partitions based on their size & memory required by them.

Available memory is 'hole'

i) Fixed Partitioning 8- OS maintain a map that indicates which part of memory are available & wa occupied by Process.

Partition main memory into a set of non overlapping regions called partitions.

Pros:- simple to implement  
Littl os overhead

Partitions can be of equal or unequal sizes.

- Any process whose size is less than partition size or = PS can be loaded into the partition

- If all partitions are occupied, the OS can swap a p out of a par

- A program may be too large to fit in a partition. Then programmer must then design the program with overlays.

(P arrived need mem) search hole large enough to store P allocate a memory to P

otherwise keep rest available to future request

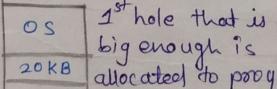
→ WASM Dy-storage allo. problem occurs. which concerns how to satisfy a request of size n from a list of free holes.

ii) Dynamic Partitioning

Partitions are of variable length & number

Each P is allocated exactly as much memory as it requires

Placement Algo 8- memory utilization should be maximum



1st hole that is big enough is allocated to pro

smallest hole that is bge is allo to pro

OS 20 KB

15 KB

40 KB

60 KB

25 KB

Hole = 40-25 = 15 KB

PA = 25 KB

Hole = 25-25 = 0 KB

OS 20 KB

15 KB

40 KB

60 KB

25 KB

Hole = 60-25 = 35 KB

OS 8m

2m

4m

6m

8m

8m

8m

12m

16m

Equal size Partition

Unequal size Partition

cons- Inefficient Partition.

Max no of active process

Largest hole that is big is allocated to pro

Process A = 25 KB

15 KB

40 KB

60 KB

25 KB

Hole = 60-25 = 35 KB

## Dynamic Partitioning :-

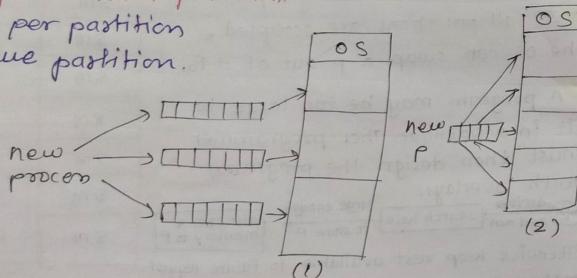
- Partitions are created dynamically.
- Each process is loaded into a partition of exactly the same size of that process.
- M.M is divided into no. of static partitions of sys generation time.

### Placement Algo for fixed partition.

- 1 P queue per partition
- 2 Single queue partition.

e.g:-

m.m
OS
8M
8M
8M
8M



## Buddy system :-

$$\frac{256}{512}$$

- Is a simple dynamic storage allocation method.
- BS is a mem allocn & mgmt algo that manages mem in power of 2 inc.
- Linux OS manages mem using buddy Algo

1 MB	
512 KB	512 KB
256 KB	256 KB
128 KB	512 KB
0 KB	300 KB
128 KB	256 KB
512 KB	300 KB

100 KB Request  
300 KB Request  
100 KB Returned

- Single allocation block to be split to form two blocks half the size of the parent block.
- These two blocks are known as 'buddies'
- An algo in which larger memory block is divided into small parts to satisfy the request
- An algo used to give best fit.

## \* Paging 8-

→ Paging is a memory management scheme that eliminates the need of contiguous allocation of physical memory.

→ Paging plays important role in implementing virtual memory.

→ The PAS is conceptually divided into several fixed-size blocks, frames.

→ Paging is a memory management technique in which process address space is broken into blocks of same size called page (size is power of 2, between 512 bytes & 8192 bytes).

→ The size of the process is measured in the no. of pages.

→ MM is divided into small fixed-sized blocks of memory called Frames.

Process P

1st 100 bytes  
2nd 100 bytes  
3rd 100 bytes,  
4th 100 bytes  
5th 100 bytes  
6th 100 bytes  
7th 100 bytes  
8th 100 bytes  
8 & so on....

Page0  
Page1  
Page2  
Page3  
Page4  
Pages  
Page5  
Page6  
Page7  
Page14

Main Memory operating System		Secondary Memory
Process PA - Page4	F0	
	F1	
Process P - Page0	F2	
Process P - Page2	F3	
Process P - Page1	F4	
Process P - Page7	F5	
Process P - PageN		
Page for other Processes	..	
Pages for other Processes	..	
Pages for other Processes	FN	

Fig : Paging.

- Logical Address or Virtual Address : An generated by the CPU

- LA space or VA space : The set of all LA generated by a program

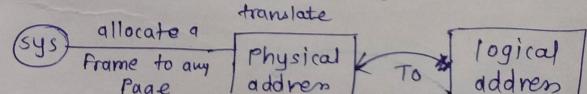
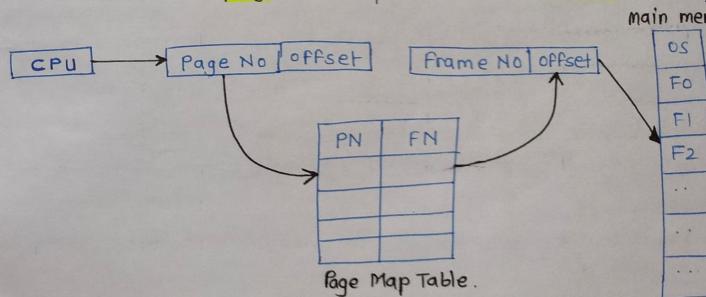
- Physical Address or: An address actually available on a memory unit (represented in bits)

- Physical Address space: The set of all physical addresses (words / bytes) corresponding to the logical addresses

- Page address is called logical address and represented by Page number and the offset.

- Frame address is called physical address and represented by a Frame number and the offset.

- A DS called page map table is used to keep track of the relation between a page of a process to a frame in phy



& create entry  $\rightarrow$  into the page table to be used through execution of the program.

## Advantages of paging

- Reduce external fragmentation
- Simple to implement
- efficient m.mgmt tech
- Swapping becomes very easy

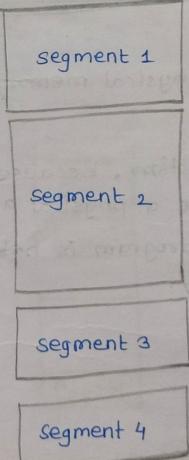
## Disadvantages.

- Suffer from internal fragmentation
- Requires extra memory space
- need high specified sys

## \* segmentation 8-

- It is memory mgmt technique.
- Divided into several segments of different sizes
- one for each module that contains pieces that perform related functions.
- Each segment is actually a different LAS of the program
- When a P is to be executed
- Its corresponding segmentation are loaded into NCM through every segment is loaded into cm block of available memory.

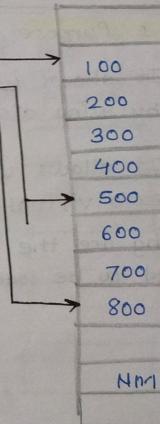
Process P



segment Map Table

SN	Size	Memory Address
1	400	100
2	200	500
3	100	800
N	X	NM

Main Memor



## \* Virtual Memory :-

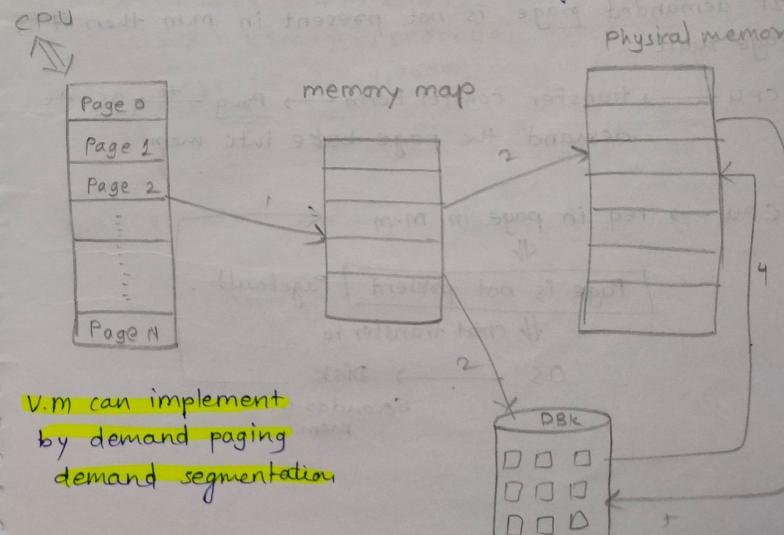
- A common technique in computer's operating system.
- Storage allocation scheme.
- Secondary memory can be addressed as though it were part of the main memory.
- The size of virtual storage is limited.
- It is a technique that is implemented using both h/w & s/w.
- It maps memory addresses used by a program, called V.add. into physical add's in computer.
-  can address more  than the amount physically installed on system. This extra memory called as virtual memory.

### VM & Purpose

- i] It allows us to extend the use of physical memory by using disk.
- ii] It allows us to have memory protection, because each virtual address is translated to a physical add.

Following are the situations, when entire program is not required to be loaded fully in main memory.

- softg which appear to be present but actually it is 16 GB  $\iff$  4 GB  $\Rightarrow$  It just gives illusion to user who use V.M. Real mem of comp.
- \* A programmer can write a prog which requires more memory than the capacity of M.M such a prog is executed by Virtual mem tech
- only part of program needs to be in memory of exec
- logical add space can therefore be much larger than phys AS.
- Allows AS to be shared by several P
- more Program running concurrently
- less I/O needed to load or swap process.



V.M can implement  
by demand paging  
demand segmentation

# Performance of Demand Paging.

Page fault rate  $0 \leq P \leq 1.0$

if  $P = 0$  no page fault.

if  $P = 1$  every reference is a fault.

Effective Access time

$$EAT = (1-P) \times \text{Memory Access} + P (\text{Page Fault overhead} + \text{swap page out} + \text{swap page in} + \text{restart overhead})$$

Eg:- MAT = 200 ns

APF = 8 ms

Achieving Page fault  
Service time

$$EAT = (1-P) \times 200 + P(8 \text{ ms})$$

$$= P - P \times 200 + P \times (8,000,000)$$

$$EAT = 200 + P \times 7,999,800$$

## \* Page fault

The demanded page is not present in M.M then its PF.

\* Page fault can be handled by OS.

1) OS looks at PCB to decide :

- invalid reference  $\Rightarrow$  abort

- just not in memory  $\Rightarrow$  (load the page)

2) Get empty Frame.

3) Swap page into frame via disk operation

4) Reset page table to indicate to page.

now in memory

set validation bit = V

5) Restart the instruction that cause the PF

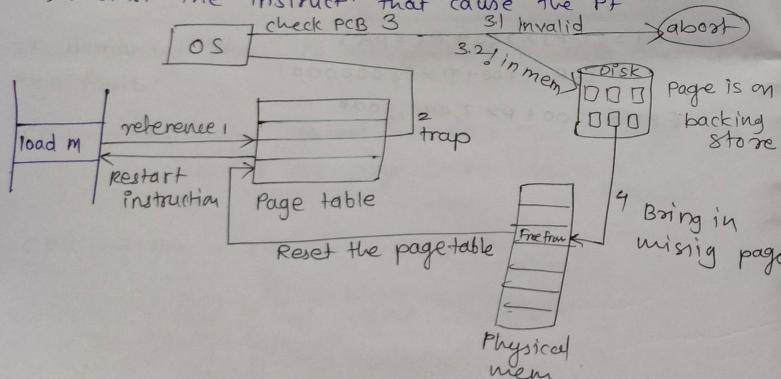


Fig: PF handling

What happens if there is no free frame?

1) Page Replacement : find some page in memory, but not really in use, swap it out  
same page may be brought into memory several times

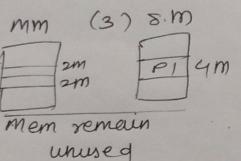
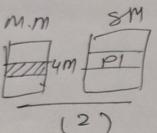
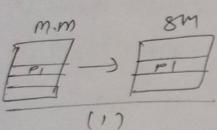
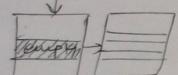
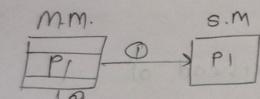
## ② Page Replacement :-

To prevent over-allocation of memory by modifying page-fault service routine to include page replacement.

use modify (dirty) bit to reduce overhead of Page transfer.

## \* Fragmentation

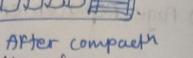
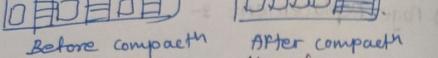
A process are loaded & removed from memory the free memory space is broken into little pieces.



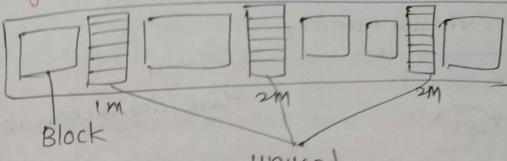
Fragmentation

External fragmentation

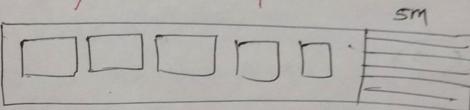
Internal fragmentation



fragmented memory before compaction

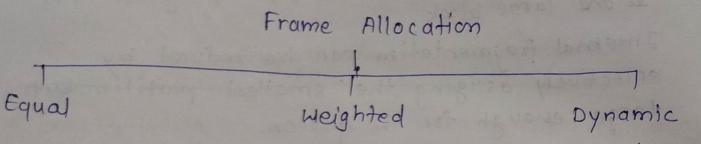
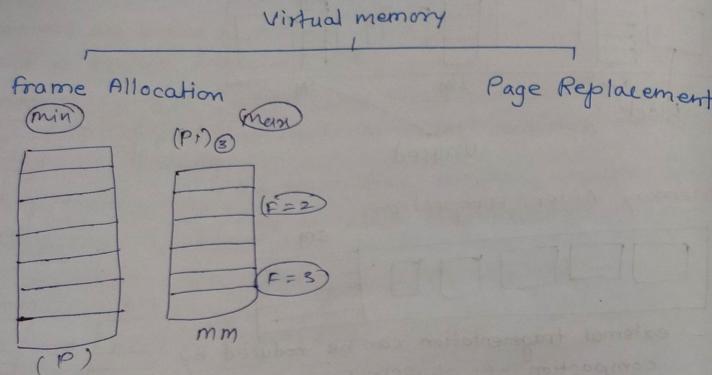


Memory After compaction.



- external fragmentation can be reduced by compaction of shuffle free memory together in one large block.
- Internal fragmentation can be reduced by effectively assigning the smallest partition but large enough for the process.

## Allocation of Frames.



$$\begin{array}{|c|} \hline \text{60 Frame} \\ \hline \end{array}$$

$$60/3 = 20$$

$$\begin{aligned} P_1 &= 30 \\ P_2 &= 30 \\ P_3 &= \frac{40}{100} \\ F &\rightarrow 10 \text{ frames} \end{aligned}$$

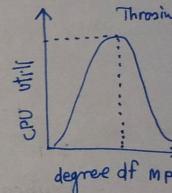
$$P_1 = \left(\frac{30}{100}\right) \times 10 = 3 \text{ f}$$

$$P_2 = \left(\frac{30}{100}\right) \times 10 = 3 \text{ f}$$

$$P_3 = \left(\frac{40}{100}\right) \times 10 = 4 \text{ f}$$

## Thrashing 8-

- swapping out a piece of a process just before that piece is needed.
- The processor spends most of its time swapping pieces rather than executing user instruction.
- If a process does not have enough pages pagefault rate ↑ This leads to
  - ↓ CPU utilization
  - os think that is need to ↑ the ° of MP
  - Another P added to the system.
- A process is busy swapping pages in & out.
- This ↑ paging activity is called thrashing
- ↑ paging ↓ CPU utilization

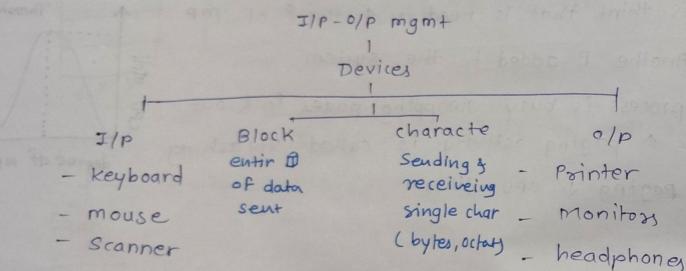


UNIT V

## \* I/O/P &amp; File mgmt

## I/O Mgmt &amp; Disk scheduling :-

- IO Mgmt controls the data exchange between external devices and mm.
- IO sys transfer info between comp mm & the outside world



## - Organisation of I/O Functions :-

1) Programmed I/O :-

- ① Under direct control of CPU  
 ② CPU issue a command on behalf of a P to an I/O module.

CPU then waits for the operatn to be completed before proceeding.

2) Interrupt driven I/O :-

- ②, if I/O instrucn is nonblocking, CPU continues to execute  $\rightarrow$  instrucn from the same process.

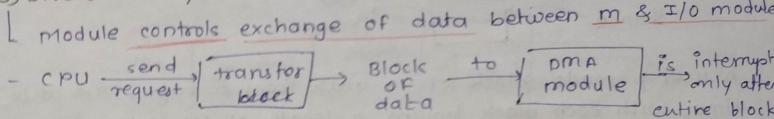
I/O instruction  
 block  
 $\downarrow$   
 OS takes over  
 suspend current P,  
 Assign CPU to  
 another P

unblock  
 CPU continues

to execute  
 next instrucn  
 From same P.

## I/O Buffering :-

## 3) Direct memory access (DMA)



- DMA module take over control of sys bus to perform the transfer
- CPU initiates the I/O by sending the following information to DMA module.
  - + Request type - read or write; using the read or write control line between the CPU and DMA module.
  - Address of the I/O devices; on the data line.
  - Starting location in memory for read / write; communicated on data lines and stored by DMA module in its address register.
  - No. of words to read / write; communicated on data line and stored in the data count register.

Enlist the character of block & character device

## 2 types of I/O devices

## Blocked

Driver communicate  
 by sending entire  
 block of data

HD, USB camera,

Disk On Key

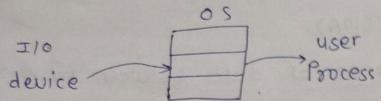
## Character

A character device  
 which the driver  
 communication by sending  
 and receiving  
 single character

Serial Port, Sound  
 Parallel Port card

## I/O Buffering

[match speed gap of I/O devices & CPU]



- A user p reads block of a data from a tape, one at a time, with each block having a length of 100 bytes.
- data should be read into user space area
- One simplest way would be to execute an I/O command to the tape unit and then wait for the data to become available
- to overcome inefficiency & inconvenience
- improves throughput of I/O operation
- In Buffering, OS stores its own copy data in memory while  $\xrightarrow{\text{trans}} \text{to or from dev}$  to cope with device speed mismatch  
transfer size mismatch
- to maintain copy semantics

## Buffering

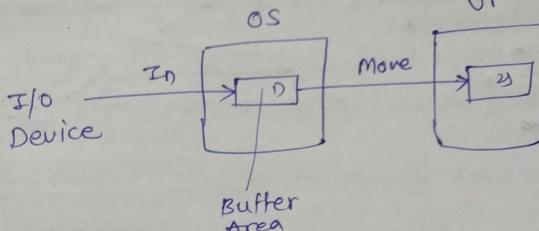
single

Double

Circular

Block oriented (T+C)  
string oriented

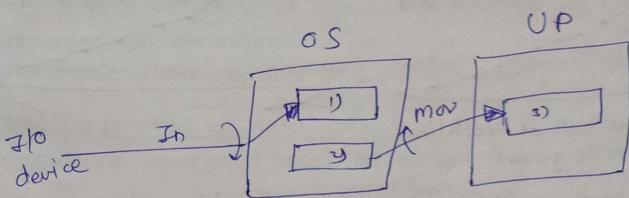
single Buffering.



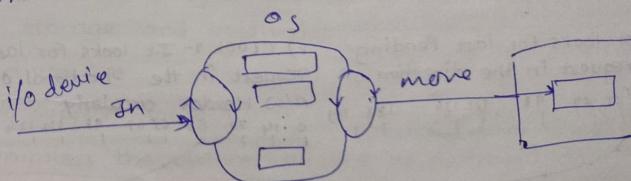
$CC = T, C$

Double Buffering.

BO  
SO  
For line  
For byte



Circular Buffer

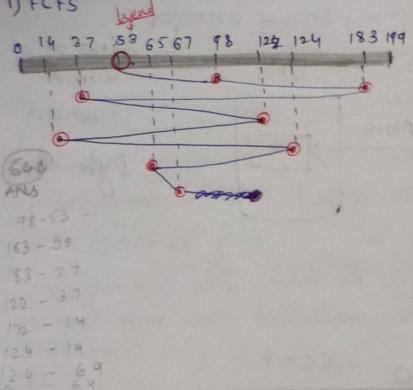


line at a  
time  
fashion

Byte at a

## Disk Scheduling. 98, 182, 37, 14, 124, 65, 67, 121

1) FCFS

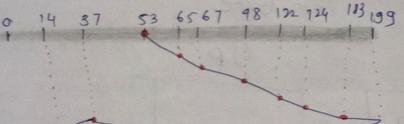


Head

ANS

98 - 53  
163 - 98  
83 - 37  
122 - 37  
172 - 14  
124 - 19  
124 - 69

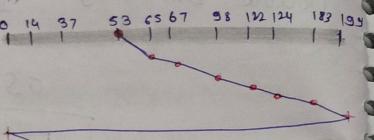
3) SCAN (ELEVATOR) (Highorder)



Scan goes to the end of track in which direction is given.

(381)

4) CSCAN :- Goes end of both track



(382)

5) CLOOK :- It looks for last pending request in the direction of R/W Header circularly

R/W Header circularly

6) CLook :- It looks for last pending request in the direction of R/W Header circularly

R/W Header circularly

5) Look - It looks for last pending request in the direction of R-W header

R-W header

• 14 37 53 65 67 98 122 124 183 199

(299)

(320)

## File Management

Managing the file system to enable users to keep their data safely and correctly is an important function of OS.

- Managing file systems by OS is called File mgmt

file mgmt provides tools for these file related activities.

- Creating new files for storing data

- Updating

- Securing data through passwords & encryption.

- Recovery in case of sys failure.

- A file is a collection of correlated information which is recorded on secondary or non-volatile storage like magnetic disks, optical disks, and tapes.

- It is a [method of data collection] that is used as a medium for giving input and receiving output from that program.

- In general, a file is a sequence of bits, bytes, or records whose meaning is defined by the file creator & user.

- Every file has a logical location where they are located for storage and retrieval.

### Objectives

- I/O support for a variety of storage device types.

- Minimizes the chances of lost or destroyed data.

- Helps OS to standardize I/O interface routine for user processes.

- It provides I/O support for multiple user in a multiuser systems environment.

F-Files

□ - Storage

○ - Disk

File is on ○ or other □ & do not disappear when user log off

Properties

f have names & are associated with access permission that permits controlled sharing

F could be R/W or more complex structures to reflect the relationships between them

### File structure

A FS needs to be predefined format in such a way that an OS understands.

It has an exclusively defined structure, which is based on its types.

F → by owner → per  
per → file



It is a series of characters that is organised in line

Name  
String used to refer file

Type  
Identifier

Identifier  
refer other attribute of f  
Location  
exact loc of f  
Size  
Time, date, Security

Type - sys supports different need to know T/F

Name - string used to refer file  
size - size of file in bytes

location - exact loc of f

Identifier - uniquely identify file by OS

Protection - DIFF T of permission for R/W & EXE

Time, date, security

File Type :-

Character Special File :- h/w file that R/W data character by character  
ex, mouse, KB, printer

Ordinary Files :-

store user info

may be T, exe programs, DB

Allow user to perform op +, del & modi

Directory Files :-

contains files & other info about

A folder to hold & organize multiple files.

Special files :-

Reposition  
move R/I/O Position

Create File

Find space  
on disk

Make an entry  
in the directory

Common terms associated with the files.

Field → Record → File → Database

This operations are available in terms of system call & can be used in user program for handling the file related operation/function of file

- 1) Create file
  - 2) Write in file
  - 3) Read from file
  - 4) File deletion
- (s) Truncate in file - erase content
- (e) File seek operation - to set the pointer at specified location for R or W  
Lumose from its current - file - P

## File Access Methods

- Process that determines the way that files are accessed and read into memory.

Generally, a single access method is always supported by operating systems.

- Some OS which also supports multiple access method.

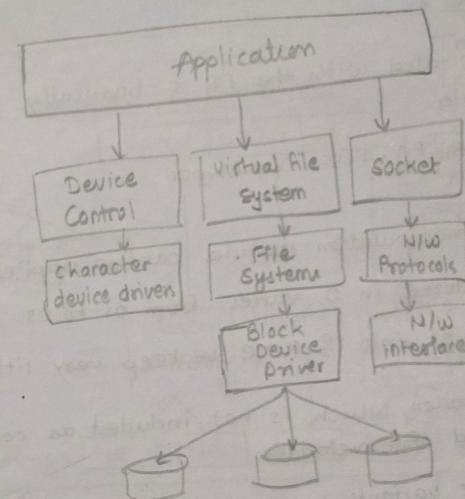
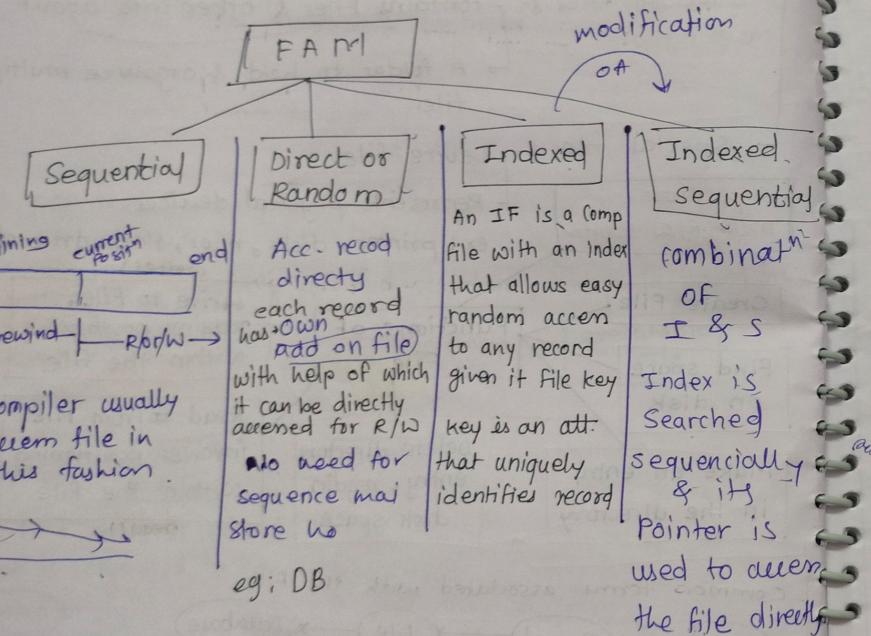


Fig: Linux File & I/o system

## File Directories :-

- Directory is associated with the FS & basically collection of files.
- Info including FA, FLOC, ownership of.
  - Special kind
  - Various file manipulation routines can be applied.
  - Other info stored in D varies from OS to OS
  - Some keep lot of info & some keep very little info.
- These other information which is not included as content of the file is called file metadata.

- Systems like UNIX variants use an external table called as inode in which such info are stored.

### Basic Information

- File name
- Type of the file
- File Organization

### File access related information

Various categories of users are able to access a file with specific permission.

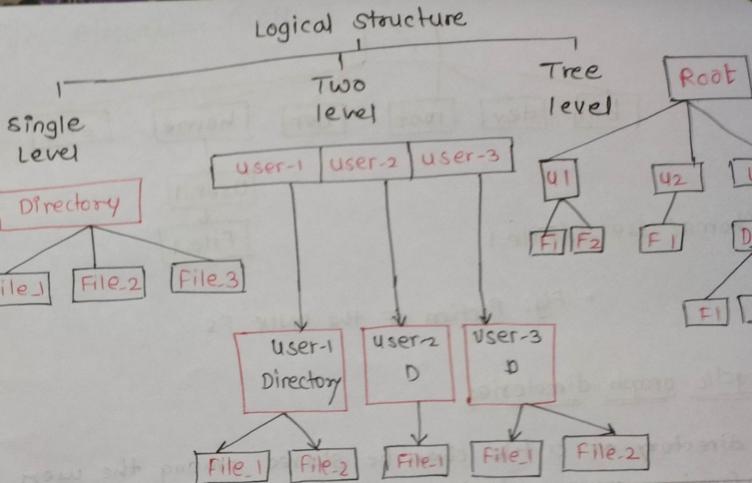
### Location of a file in secondary memory related info

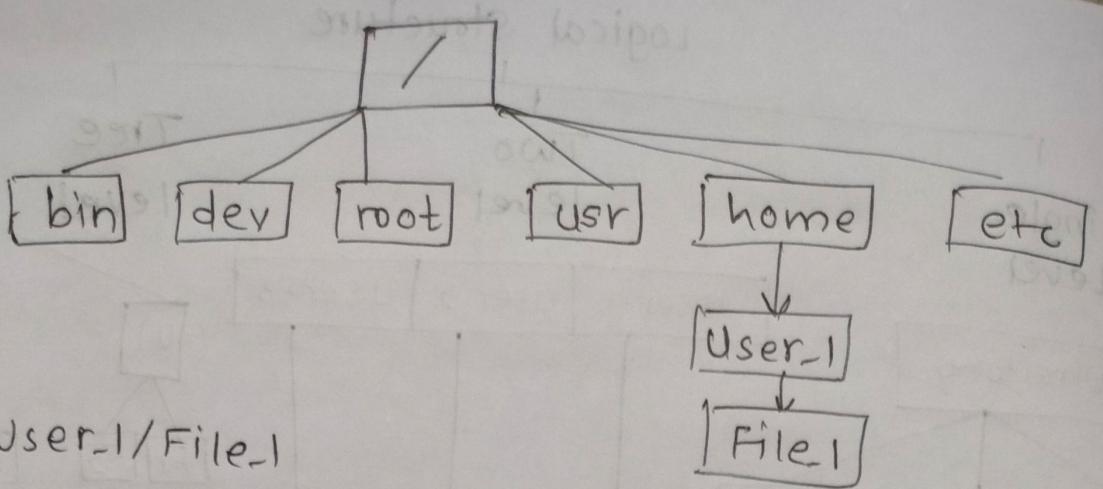
Volume info indicates the device in which file actually resides.  
Starting LOC of file in the disk

### Other info of the file

- Owner of file
- Date of file created
- Last access date of file.

## Logical Structure



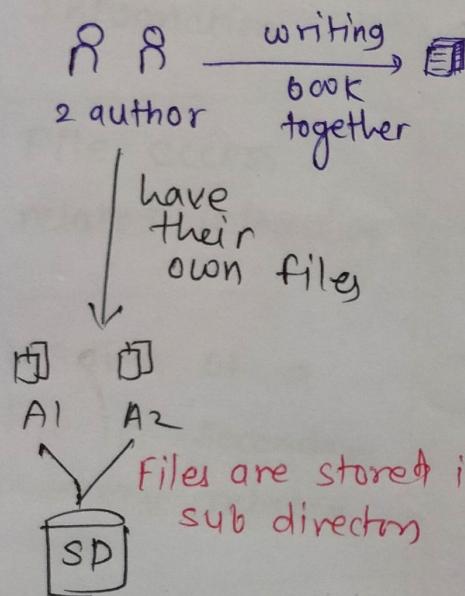


/home/User-1/File-1

Fig: Portion of the UNIX FS.

### Acyclic graph directories

- A directory or a file can be shared among the users performing common task.
- A SP or files therefore can exist in two or more places at the same time
- The need of SP or file can be described by an ex.



equal Responsible to  
W book so need  
file in their directory  
so it should be shared  
but hierarchical tree structure

- ★ An Acyclic graph directories & allow sharing of directories & files.
- Same file or subdirectory may be in 2 diff directory

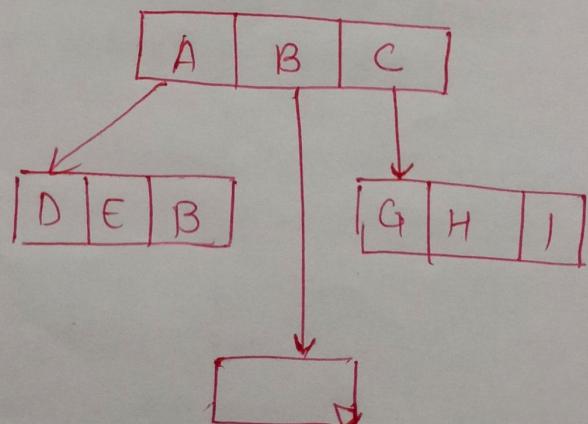


Fig: Acyclic graph directory

## Assembler

collection of mnemonic is Assembly lang

[Assembler is a translator that converts assembly lang into machine lang along with some info for the loader

syntax

[label:] mnemonic [oprand] [i]

LI Add R<sub>1</sub>, R<sub>2</sub> ; A

Assembly levelProg terms

LC - location counter

Literals - constant

Symbols - var - holder

Procedure - function like c

Adv

- easy

dis3-3H

- Need translator

- Binary no use

mnemonic

- AP ↑ processing