

ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ



VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

° 068320

KAKNATAKA, INDIA

FOURTH SEMESTER B.E.EXAM

Electronics & Communication Engineering

Name of the Student

: ASHWIN DESHPANDE

Father's / Mother's Name

SANJEEV DESHPANDE

Seat No.

2GI13EC020

Name of the College

KLS GOGTE INSTITUTE OF TECHNOLOGY, BELGAUM

SI. No.	Subject		Exar	Examination Marks			Internal Assessment Marks			Total Marks		
	Code	Title	Max.	Min.	Obtained		Min.	Obtained	Max.	Min.	Obtained	Month & Year of Passing
1 2 3 4 5 6 7 8	10MAT41 10ES42 10ES43 10EC44 10EC45 10EC46 10ESL47 10ECL48	Engineering Mathematics - IV Microcontrollers Control Systems Signals & Systems Fundamentals of HDL Linear ICs & Applications Microcontrollers Lab HDL Lab	100 100 100 100 100 100 50	35 35 35 35 35 35 35 20	79 35 38 65 69 45 37	25 25 25 25 25 25 25 25 25 25	12	23 17 25 19 23 21 24	125 125 125 125 125 125 125 75	50 50 50 50 50 50 50 32	102 52 63 84 92 66 61	of Passing JUL 2015 JUL 2015 JUL 2016 JUL 2016 JUL 2015 JUL 2016 JUL 2016
	1020146	HDL Lab	50	20	35 FII	25	12	24 TOTAL	75 900	32	542	JUL 2015

First Attempt Total Marks (in words) :

FIVE HUNDRED AND FORTY TWO ONLY

Result of the Semester

FIRST CLASS

DATE

JUL 05,2016



