

ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ



VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

KARNATAKA, INDIA

188443

BE Third Semester January 2015 Electronics & Communication Engineering

Name of the Student

: ASHWIN DESHPANDE

Father's / Mother's Name

SANJEEV DESHPANDE

Seat No.: 2GI13EC020

Name of the College

: KLS GOGTE INSTITUTE OF TECHNOLOGY BELGALIM

SI. No.	Subject		Exar	Examination Marks			Internal Assessment Marks			Total Marks		
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	Subject Result
1	10MAT31	Engineering Mathematics-III	100	35	72	25		23	125	-50	95	Pass
2	10ES32	Analog Electronic Circuits	100	35	45	25	-	23	125	50	68	Pass
3	10ES33	Logic Design	100	35	57	25	2	19	125	50	76	Pass
4	10ES34	Network Analysis	100	35	56	25	-	16	125	50	72	Pass
5	10IT35	Electronic Instrumentation	100	35	57	25	2	25	125	50	82	Pass
6	10ES36	Field Theory	100	35	59	25	-	19	125	50	78	Pass
7	10ESL37	Analog Electronics Lab	50	20	44	25	12	25	75	32	69	Pass
8	10ESL38	Logic Design Lab	50	20	45	25	12	24	75	32	69	Pass
		13					-	3		\$ P	17	
GRAND TOTAL									900	364	609	717

Total Marks Obtained (in words) : SIX HUNDRED AND NINE ONLY

Result of the Semester

: FIRST CLASS

DATE:

APR 16,2015

Registrar (Evaluation)

NE - Not Eligible