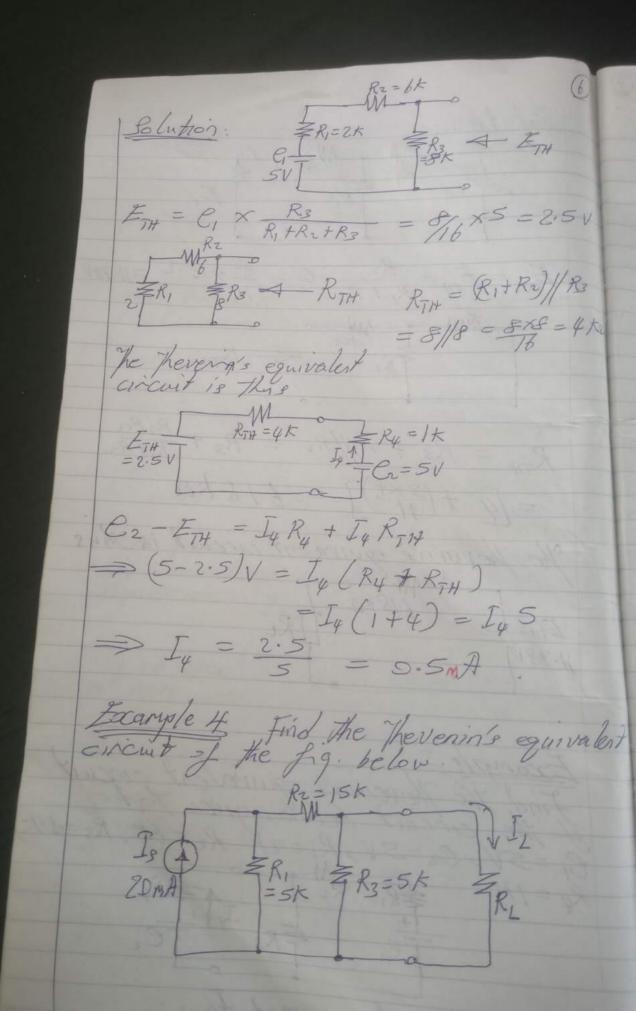
1 ELE 311 CIRGUIT THEORY INTRODYCTION In electrical circuits various # methods have been developed for use in solving for unknown currents and voltage drops in de networks. The laws which determine the current and voltage drops in de networke are: - Dhors law Laws of resistors in series and in parallel Kirchists laws (current and voltage laws Beside these three laws, there are sher circuit feorems created for solving problems in electrical networks, namely - Superposition theorem - Thevenin's theorem Norton's theorem - Maximum power transfer theorem Recall that Kirchhoft's current law states that at any junction in a network, the total current flowing towards the junction is equal to the total current flowing away from the junction. Also recall that kirchof's voltage law states that in any closed loop in a network the algebraic sign of the voltage drops taken round the loop, is equal to the Now, lets look at theverin's theores 1.1 THEVENIN'S THEOREM Any two terminal network may be represented by a series combination of a

voltage source and, a resistor. The value of this equivalent source is the voltage The value of the equivalent resistance is the value of the network with all energy? sources, replaced by their respectible internal resistances. The current in any passive circuit element (say a resistor Rc) in a network is the same as would be obtained if Re were supplied with a source voltage Es in series with an equivalent registance Re Es being the open Execut voltage at the terrinale from which Rc has been removed, and Re being the resistance that would be greasured at these terminale, after all sources have been replaced by their internal resistances. The venin's theorem states that any linear two-terminal network may be replaced by a voltage source whose value is equal to the open circuit terminal voltage (say Eoc) in series with an equivalent irisedance (Zeg) whose value is the impedance seen at the pair of network terminals. the following examples will be used to demonstrate the theorem.

6 Jolytion & FRI 1 83 4 ETH ITH = RI X Is = 5 X 20MA = 4 MA 5 V ETH = UR3 = I2 R3 = ITH R3 = 4 MA x 5 to 2 = 20V 193 Resulting cct.

R74 = R3/(18+R1)

P74 4 tz $=\frac{5\times10}{25}=\frac{100\times}{25}=4\times10$ Therefore, the Therenin's equivalent cct. is shown below. # TH = 4KN = RL Alternatively, FR = 5K => ES | les 7 Es = IsR, = 20MA 85XN = 100V



85 [, +25 [r = 60 => 17 [, -5 [r = 12 -- 0] Loop 2 Er-R3 (Ir-I,)-R4 Ir-R5 In+ E3 =0 Solving eg2s (1) and (2) 17 I, -5 In = 12 -5 I, + 27 In = 20 Using Determinant, $\begin{bmatrix} 17 & -5 \\ -5 & 17 \end{bmatrix} \begin{bmatrix} I_1 \\ I_2 \end{bmatrix} = \begin{bmatrix} 12 \\ 10 \end{bmatrix}$ $\mathcal{D} = \begin{vmatrix} 17 - 51 \\ -5 & 27 \end{vmatrix} = 17 \times 27 - 25 = 459 - 25 = 434$ $I_1 = \frac{1}{9} \begin{vmatrix} 12 & -5 \\ 20 & 27 \end{vmatrix} = \frac{1}{434} \left(324 + 109 \right) = \frac{424}{434} = 0.977 \text{ Brops}$ and In = 1 | 17 12 | = 1 (340+60) - 400 = 0.922 Amps 15 NODE VOLTAGE ANALYSIS Procedure: Unlike in Mesh current analysis where variables are used in conjunction with KVL, nodal analysis depends on a choice of voltage used as variable in conjunction with KGL. A general

4 eT R, FR2 Eoc Ecc = ETH = R2 xe = \$ x16.2 = 11.78V Ry LR3 - RTH RTH = R3 + R1//R2 = R3 + R1 R2 $=[4+(3\times6)]=6.18$ The the verin's equivalent circuit is thus

Fit I 6:18 ku I RL

11.7817 itors Find the Theverin's equivalent circuit below, gives that Example 3 8, =5V, e2=5V, R1=2K, R2=6K, R3=\$K, R4=1K, =1K, R2 = 6K, R3=\$K, Te, R2=6K, R3=\$K, Te, R2=6K, R3=\$K, Te, R2=6K, R3=\$K, 4tre Find also the current I4.

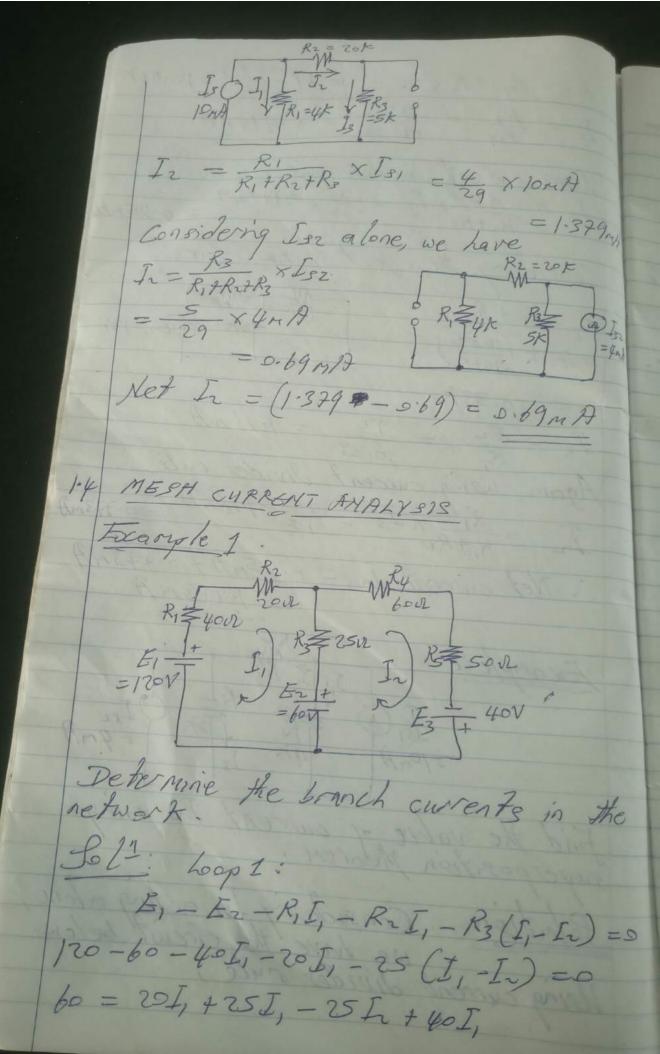
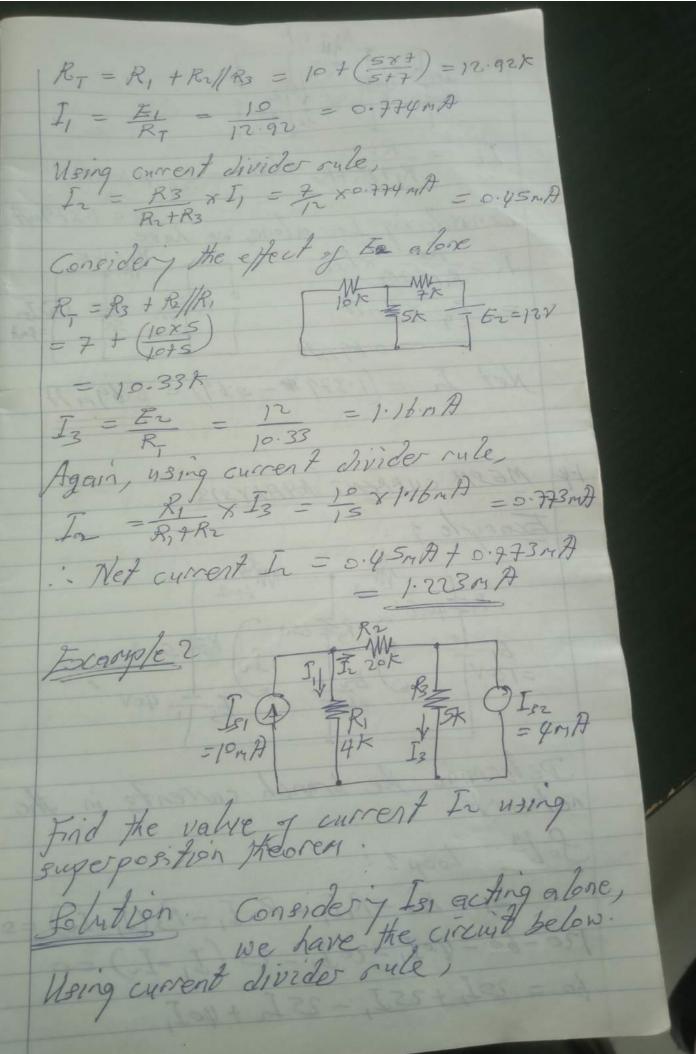


Fig 2 completely replaces fig (), but with its characteristics mountained (For is the same, Isc the same). To determine short circuit current, Isc = C and this will be the gurrent if we short-circuit perminals A and B. From Thereno's equivalent values Isc = Zoc = ex Rr/R, +Rr) = e -(11)
Reg RiRoz/R, +Rr) = R, which confirms that Ise is the same for either circuits (fig 1 and fig 2). It is good to note that too Zee and Ise are always related by the expression to = Isc × Zee --- (1V) Zeg is used to replace Reg, since it is a ripre general expression and it applies to networks containing capacitors and inductors as well. Escargle 2: Deterringe the Theresin's equivalent circuit of the fig. below, gives that 6=16.21, R,=3tur, R2=8tur, and R3=4tur WRI RS LI RL



= 1+j Rwc = 10 x fr L-45° Zeg is found by subshiting zero shore as the inspedance of the source (c), and find, Zeg = R/6 = R. (5wc) = R R+(5wc) = 1+ jRwc $= \frac{5(-j5)}{5-j5} = \frac{5}{\sqrt{2}} \frac{1-45^{\circ}}{1} \mathcal{I}$ Therefore the equivalent Theresin's cct. is To verify the results, we determine Isc. Isc = 1 = 27 A short cot across A-B terminals will exclude consideration of the capacitor since the capacitor ist shorted. From the equivalent network, Isc = Zoc = & (1/(1+jwRc)) = %

Reg (R/(1+jwRc)) = % which is the same.

(b) Add a sov batter in series with the sood resistor so that its positive terminal is connected to node A, and its negative terminal to the top of the resistor. Solve for the current in the 4000 resistor. $\begin{array}{c|c}
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 & &$ 9 Angs Determine the value of IB using any method. lysus) Tuborial Questions (Superposition Theorem).

Ryperposition Theorem Theore Determine the branch correst IRZ by using superposition theorem

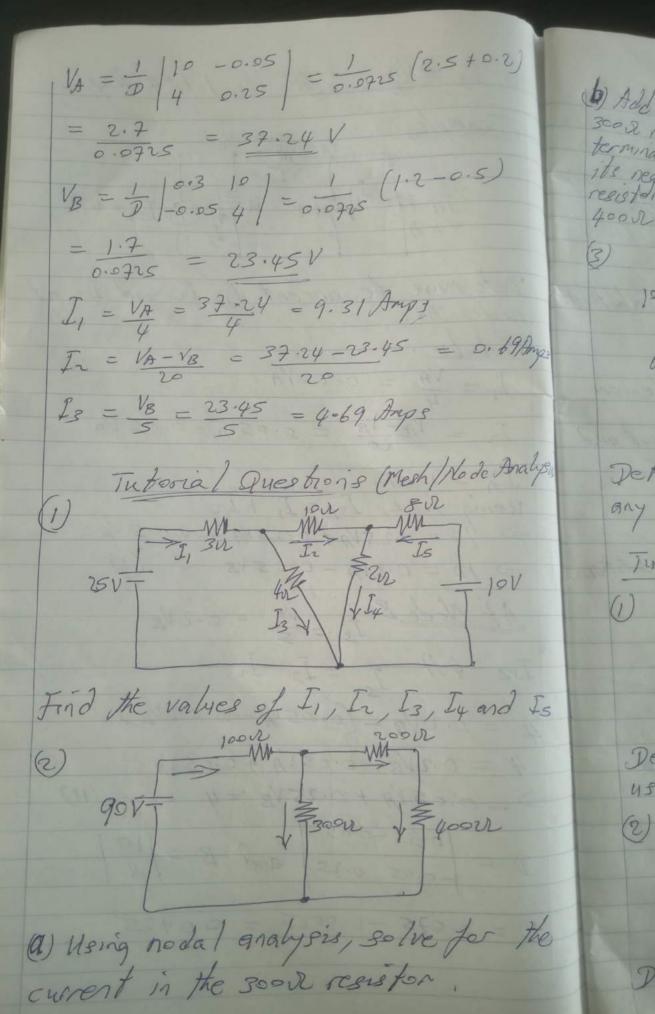
Ri=4t Many

Ri=4t Many

Ri=4t Fried

Ri=4 Determine the current through the resistor Rz.

 $E_1 + E_2 = I_1 R_1 + I_1 R_2 = I_1 (R_1 + R_2)$ $I_1 = \underbrace{E_1 + E_2}_{R_1 + R_2} = \underbrace{\frac{14V}{10K\Omega}}_{10K\Omega} = 1.4 \text{ mA}$ I, R2 = ETH + E2 => ETH = I,R2-E2 = (1.4 mA)(6 kNb)-4V = 8.4-4 = 4.4 V FR, FR. - Rit RIH = RI/R = RIR2 = 4x6 por = 2.4 km N/ 11' ON The resulting Theveris cct. 18 = 4.4V R7H = 2.4K IL = 5K at. $I_L = \frac{E_{TH}}{R_{TH} + R_L} = \frac{4.4}{2.445] \times 10^3} = \frac{4.4 \times 10^{-3} A}{7.4}$ = 0.595 mAExample 6 Determine the Theresin's equivalent cct of the cct selow. $e \circ \beta = |e| \sin i \phi t$ $e \circ \beta = |e| \sin i \phi t$ C = 0.00 $X_c = -j S$ Soli Eoc = ex-Xc = ex/jwc

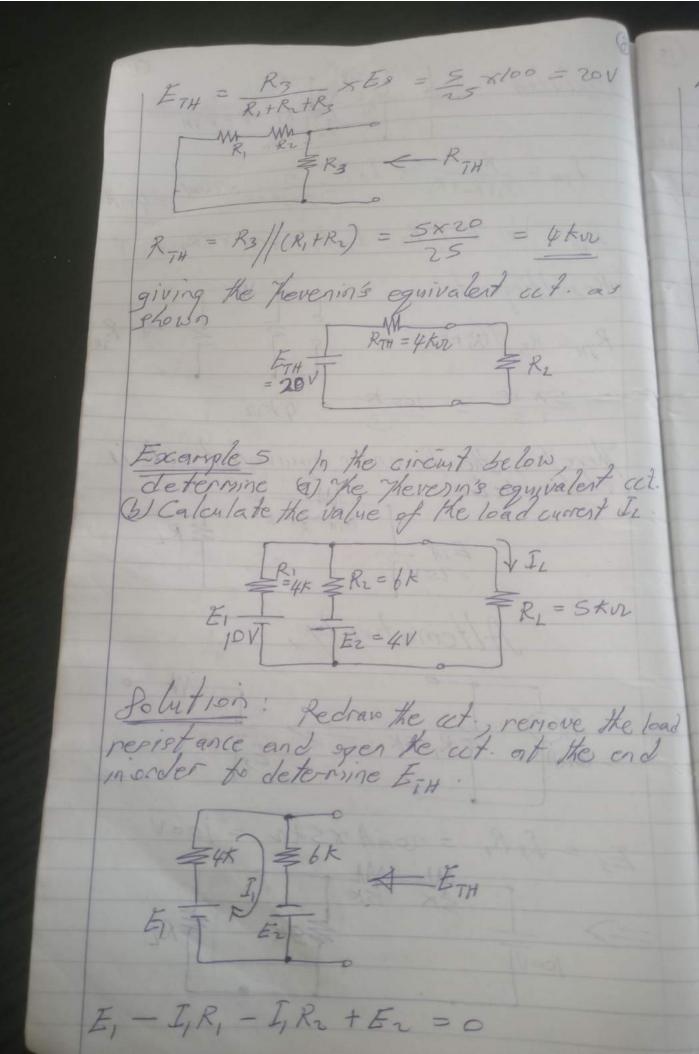


(b) Add termina! ite nega registar 4002

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by node voltage analysis is follows to covert Sources, if necessary
convert all voltage sources

sources, if necessary
for which the node voltage are
from which the node voltage are
incasured.

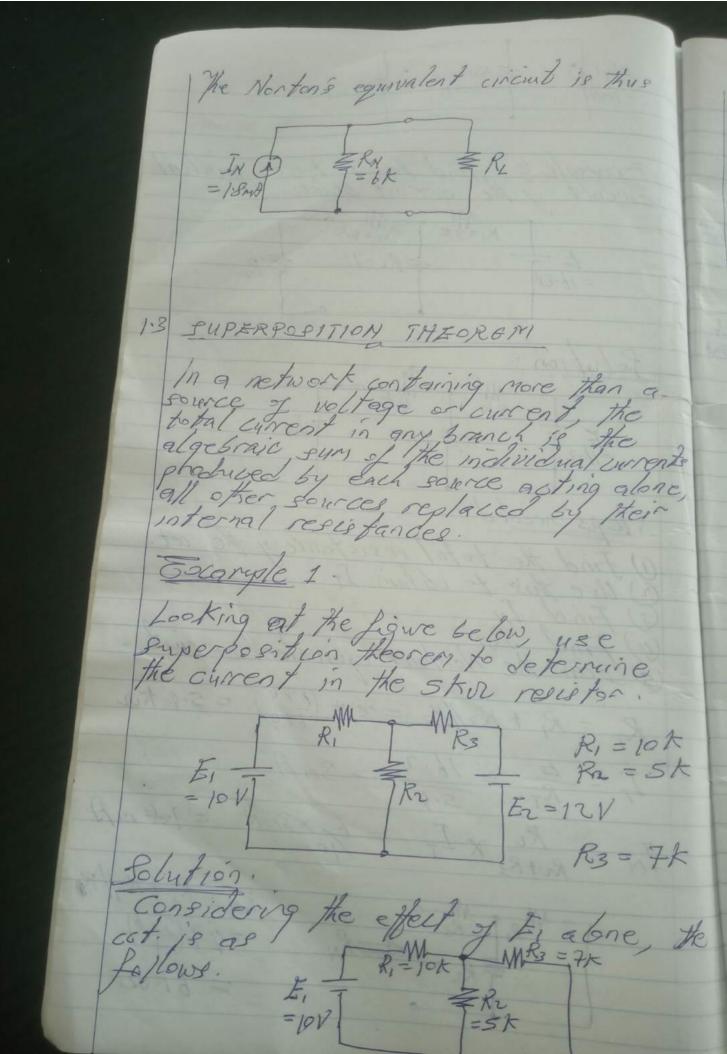
(3) The number of node voltages is generally
swhere IN is the total number of nodes
in the network. The number of nodes
equations = the number of node voltages

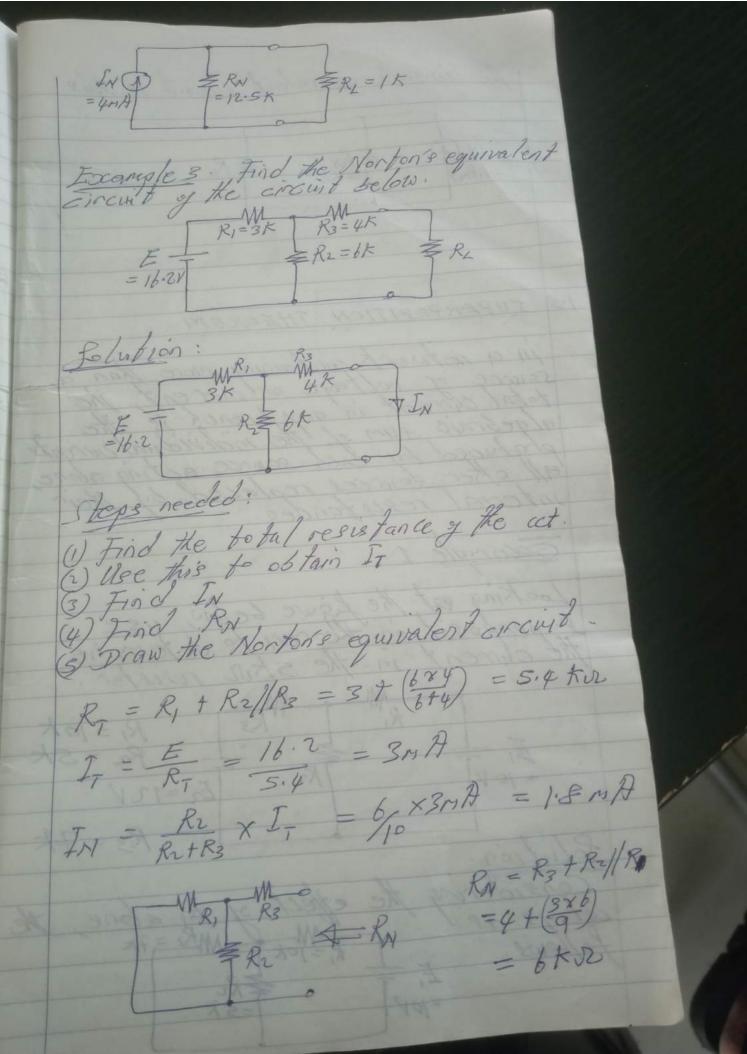
(4) Apply KCL at each node except the
reference node. référence node. (5) The the set of simulteneous finear equations for the node voltages. Example 1. Determine the current in the Solution: At node A, I, + Iz = I3 $I_1 = 12 - V_A = 3 - 0.25 V_A$ In = 10-VA = 5-0.5 VA I3 = VA = 0.2 VA But I3 = I, +In So, 2, +12-23 =0 3-0.25 VA +5-0.5 VA -02 VA =0 28-0.95 VA =0 -> VA = & = 8.42V

Looking at fig. 1, the spen circuit voltage voltage may be determined from voltage divides theory to be Ru

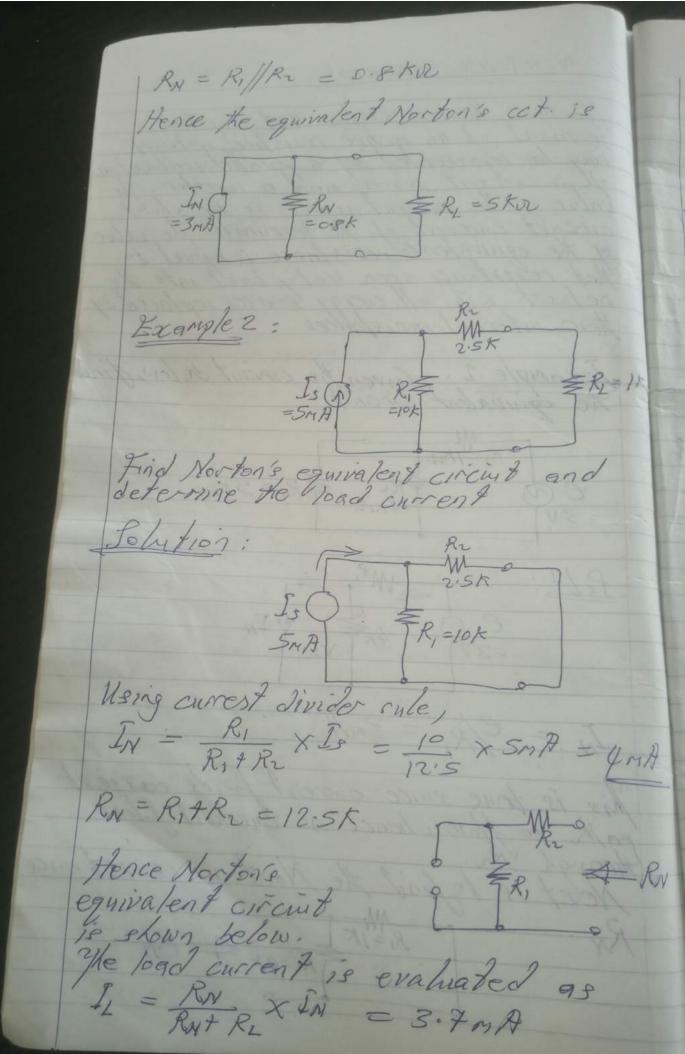
Voc = VAB = C X R, +Rr Example 1: 10 e S ZR Va= circust voltage fig.1 næ t The equivalent resistance seen at the ATS Herograls is generally determined by replacing any sources with their internal resistances. For this circuit, R, is in parallel with Rr, so the equivalent resistance Reg is given by Reg = R, +R2 -- (19) The theresin's equivalent circuit is
thus as shown in fig.

MReg he Foc P Fig. 2





equations and solutions, only that the quetions of some complimentary element. are intercharged the is this switchasility or interchargeability that is known as the principle of Duality. Dual Pairs Conductance G Resistance R Current Source Voltage V Voltage source KCL, KVL Norton Thevenin Capacitance C Inductione L Short circuit Open circuit Mesh, Node Parallel path Serial porth It is note worthy that power does not appear in the table, because power has no dual, This is because of the not, linear, power cannot, be dual. Also note that the pornuple extends to circuit elements and theorems. Two circuits that are described by the variables are switched are said to be Ival of each other.



1.6 MAXIMYM POWER TRANSFER THEOREM It states that the power transferred from a supply source to a logo is at its maximum when the resistance of the source of the source Hence, in the figure below, when cct. K=r, the power transferred from the source to the load is a maximum . Source Z load 1.7 DUALITY The concept of Duality is a time saving and effort-efficient way of solving circuit problems. In Duality, a gircuit is changed in that the components and or guantifies are interchanged. The quantities that often changed include () voltage and current (2) resistance and conductance (3) capacitance and inclustrance It so happens that in circuit analysis, the two different circuits have the same 14 NORTON'S THEOREM Norton's theorem states that any two termial of an getive resistive, network may be represented by a parallel combination of a current source and a resistor; The value of the current source is the short of the terminals. The value of the equivalent resistance is equal to that resistance seen looky back into the network, with, all energy sources replaced by their internal resistables. the equivalent circuit. $\begin{array}{c|c}
R_1 = 1 \times 1 \\
R_2 = 1 \times 1 \\
R_3 = 1 \times 1 \\
R_4 = 1 \times 1 \\
R_5 = 1 \times 1 \\
R_6 = 1 \times 1 \\
R_7 = 1 \times 1 \\
R_8 = 1$ IN = 6/R, = 3MA This is true since ourrest froids easiest through to flow, hence no current flow through the flow of the state of the stat Next is to find the Norton's resistance Ri=IK = RN Ri=4x A RN

