1. Importing source files into Vivado

```
V ■ Design Sources (1)

V ● DC_FT_design_8cn_TMR (DC_FT_design_8cn_TMR.sv) (48

V ● genblk1[0].DC_rx_rev0 : DC_rx (DC_rx.sv) (3)

V ● genblk1[0].DC_rx_rev1 : DC_rx (DC_rx.sv) (3)

V ● genblk1[0].DC_rx_rev2 : DC_rx (DC_rx.sv) (3)

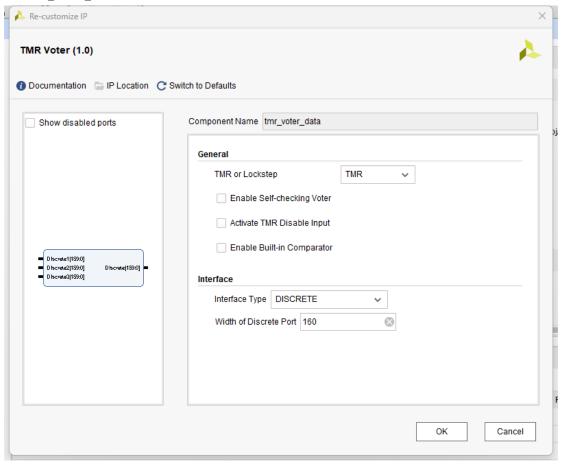
V ● genblk1[0].tmr_voter_data_real_inst : tmr_voter_data (tmr_voter_data)
```

Fig. 1 Structure of the project

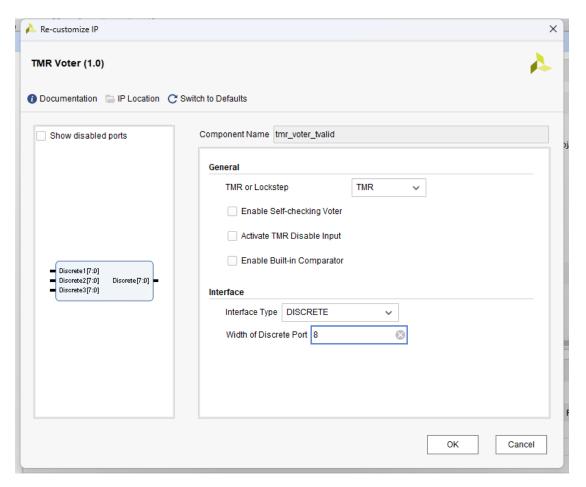
2. Configuration of IP cores

(1) TMR Voter

For tmr_voter_data:



For tmr_voter_tvalid:



3. Set "DC_FT_design_8cn_TMR.sv" as the top module