1. **Importing source files into Vivado**

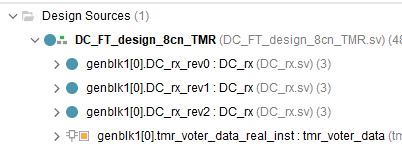
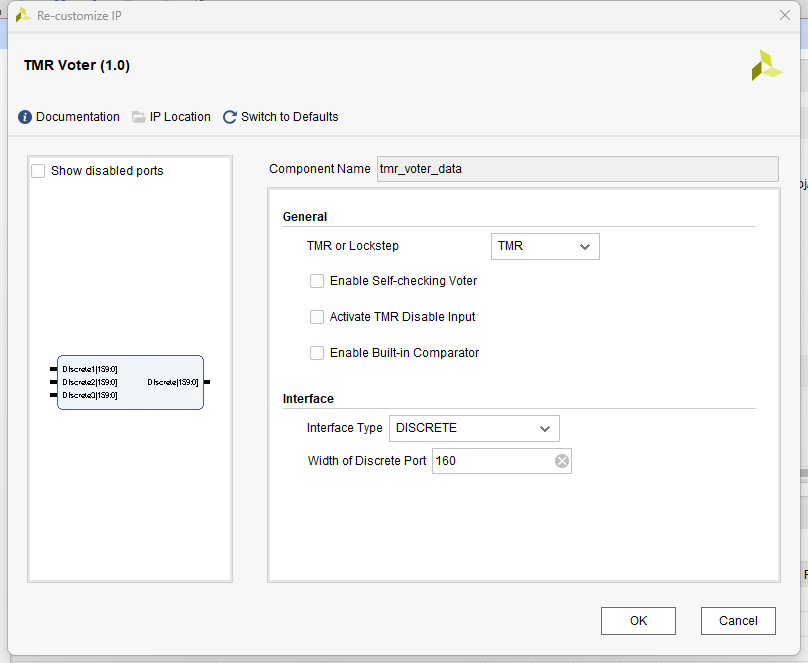


Fig. 1 Structure of the project

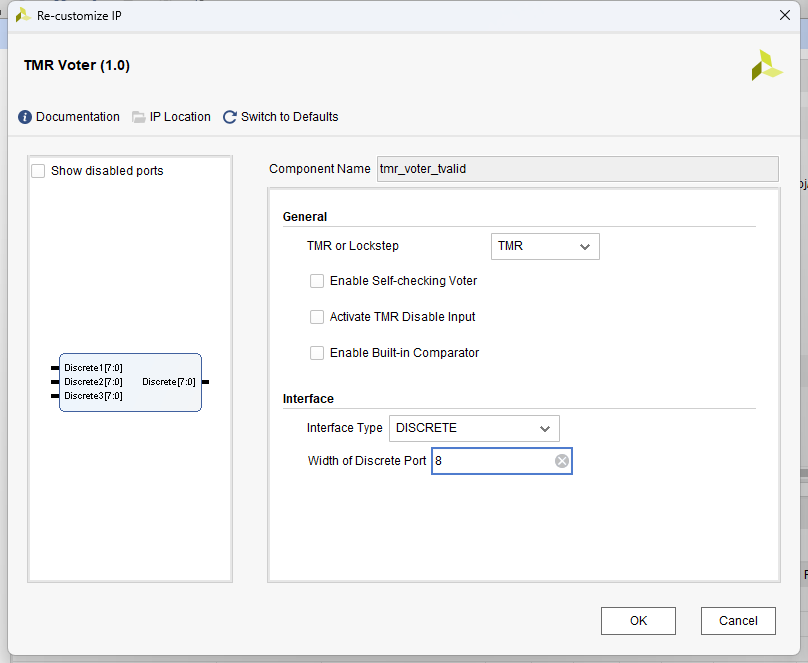
1. **Configuration of IP cores**

**(1) TMR Voter**

For tmr\_voter\_data:



For tmr\_voter\_tvalid:



1. **Set “DC\_FT\_design\_8cn\_TMR.sv” as the top module**