

Notes:

Make sure that the PCB grounds do not touch the inside of the case and isolate the TO-220 voltage regulators from the end panels of the case with silicone insulators and use plastic isolators on the metal screws.

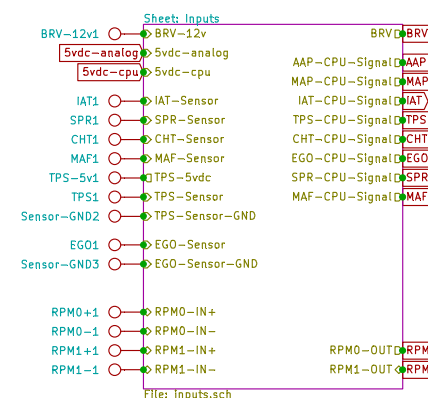
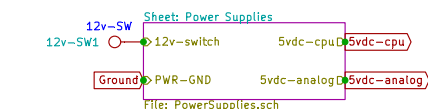
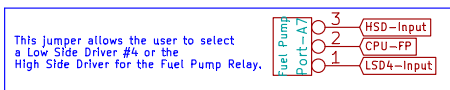
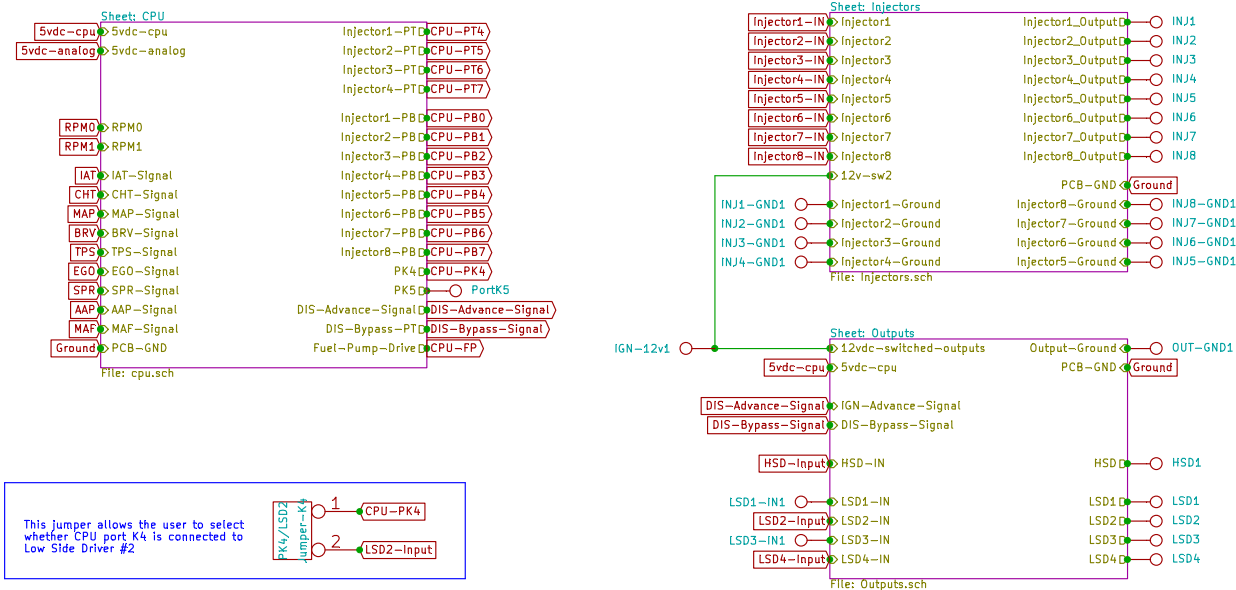
5vdc-cpu = VDD

5vdc-analog = VCC

Components that are missing from design changes:

C45, C46, C50, R62, R86, R93, R94, R96, R97, R98, R99

This PCB is intended to be used with either the specified Context Engineering enclosure or retro-fitted into a factory ECU enclosure.



For 6/8 cylinder using Port T with current code, run jumper wires from:

PT4-7	pin 1 to PB0-7	pin 1 = Bank 1
PT4-7	pin 2 to PB0-7	pin 3 = Bank 2
PT4-7	pin 3 to PB0-7	pin 5 = Bank 3
PT4-7	pin 4 to PB0-7	pin 7 = Bank 4

AND DO THE FOLLOWING

For 6/8 cylinder using Port T with current code jumper from:
(can be tie-bar shorting jumper if you installed a 0.100" pin header for Injectors-IN)

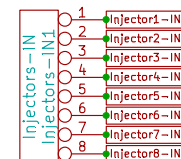
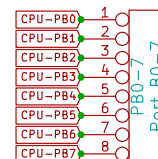
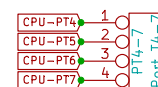
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Injectors-IN pin 1 to pin 2
Injectors-IN pin 3 to pin 4
Injectors-IN pin 5 to pin 6
Injectors-IN pin 7 to pin 8
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To use Port B with future XGATE code, run jumper wires from:
P33 to P34 pin for pin and
do not connect anything to P20.

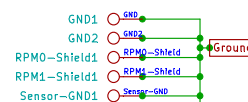
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PB0-7 pin 1 to Injectors-IN pin 1
PB0-7 pin 2 to Injectors-IN pin 2
PB0-7 pin 3 to Injectors-IN pin 3
PB0-7 pin 4 to Injectors-IN pin 4
PB0-7 pin 5 to Injectors-IN pin 5
PB0-7 pin 6 to Injectors-IN pin 6
PB0-7 pin 7 to Injectors-IN pin 7
PB0-7 pin 8 to Injectors-IN pin 8

```



PT4-7 and PB0-7 allow for selection to use either
Port T (4 cpu outputs MAXIMUM;current code)
or Port B (8 cpu outputs;future XGATE code)



Git: 3b0981b7b7

File: Jaguar.sch

Sheet: 7

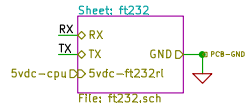
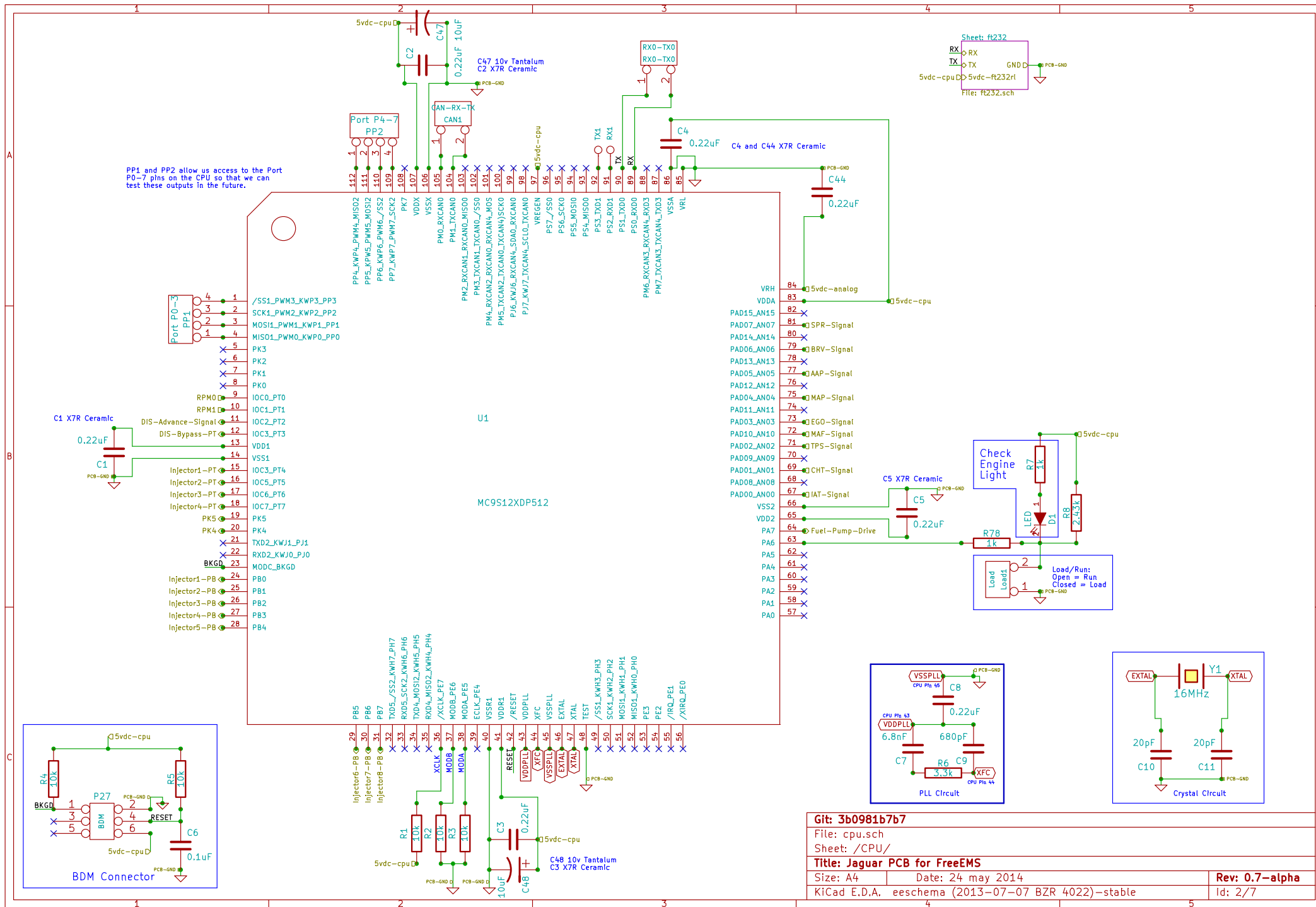
Title: Jaguar PCB for FreeEMS

Size: A4	Date: 24 may 2014
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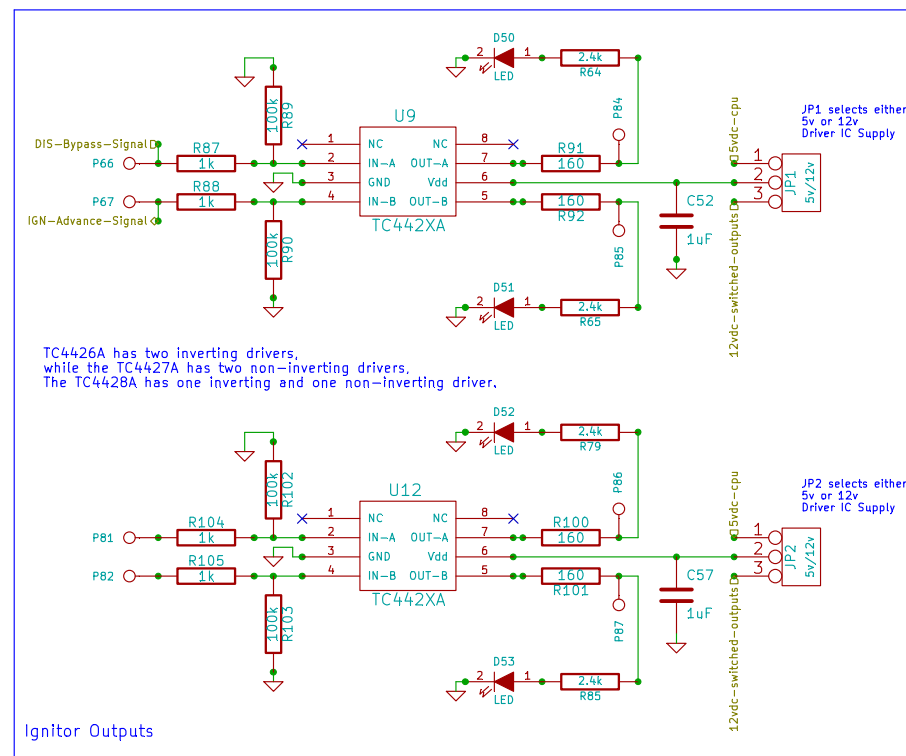
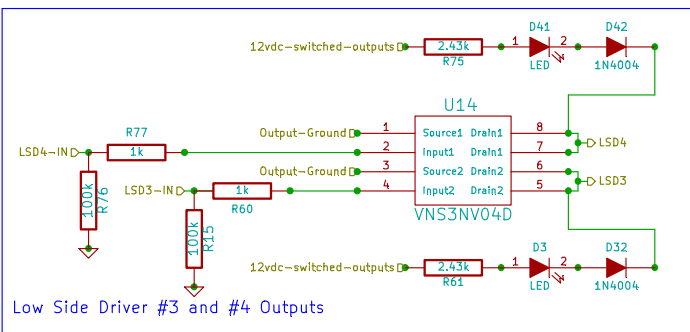
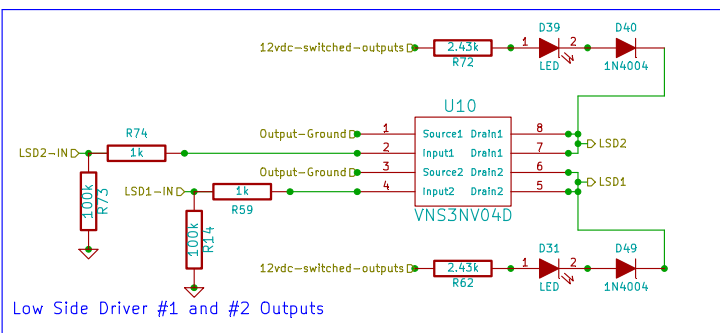
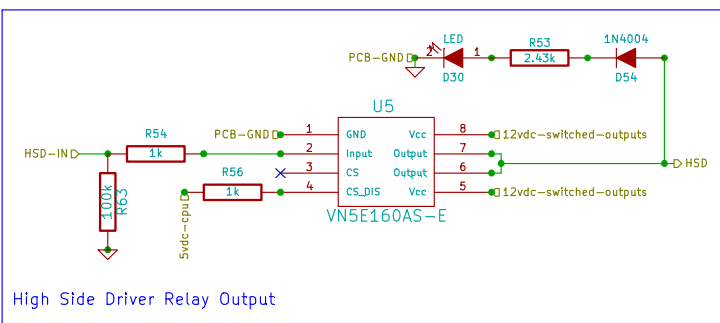
KiCad E.D.A.	eeschema (2013-07-07 BZR 4022)-stable
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Rev: 0.7-alpha

Id: 1/7



Git: 3b0981b7b7		
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Title: Jaguar PCB for FreeEMS		
Size: A4	Date: 24 may 2014	Rev: 0.7-alpha
KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable		
		Id: 2/7



Git: 3b0981b7b7

File: Outputs.sch

Sheet: /Outputs/

Title: Jaguar PCB for FreeEMS

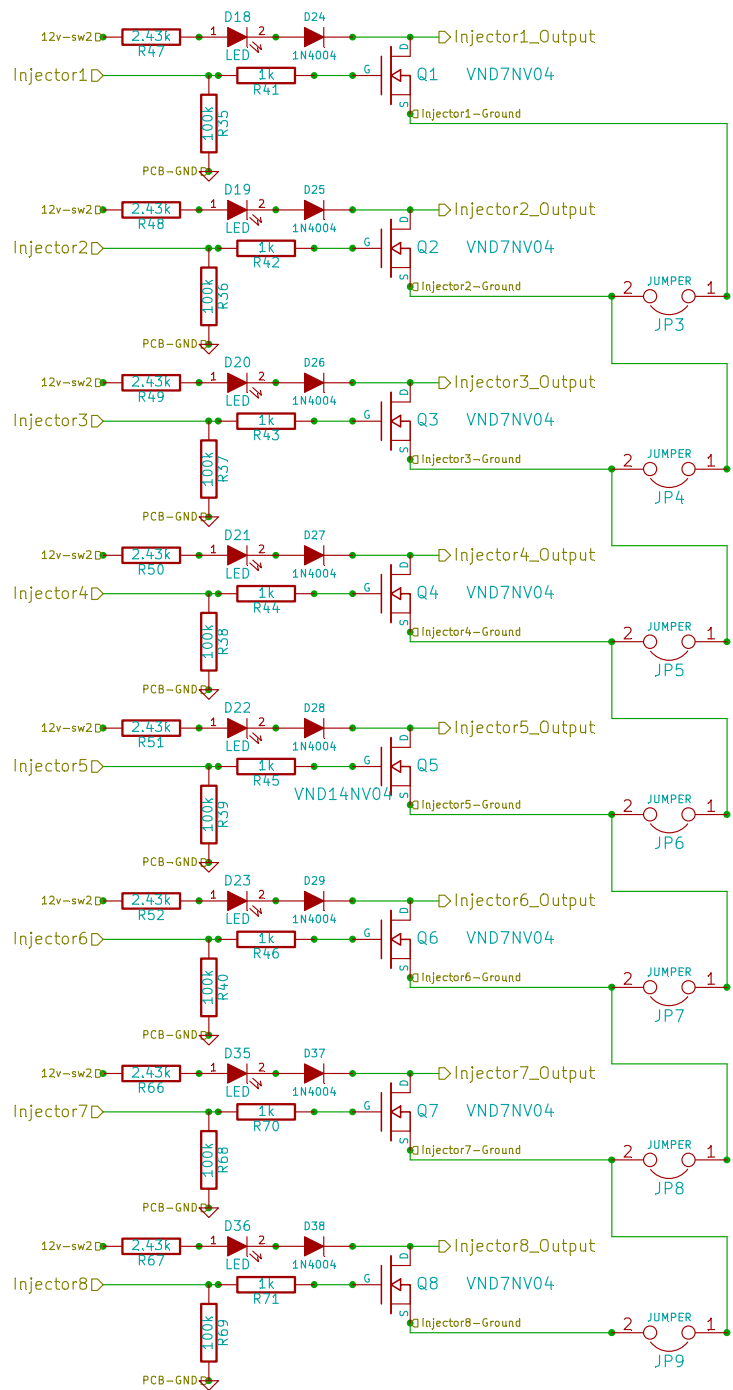
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Date: 24 may 2014

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Rev: 0.7-alpha

Id: 5/7



Git: 3b0981b7b7

File: Injectors.sch

Sheet: /Injectors/

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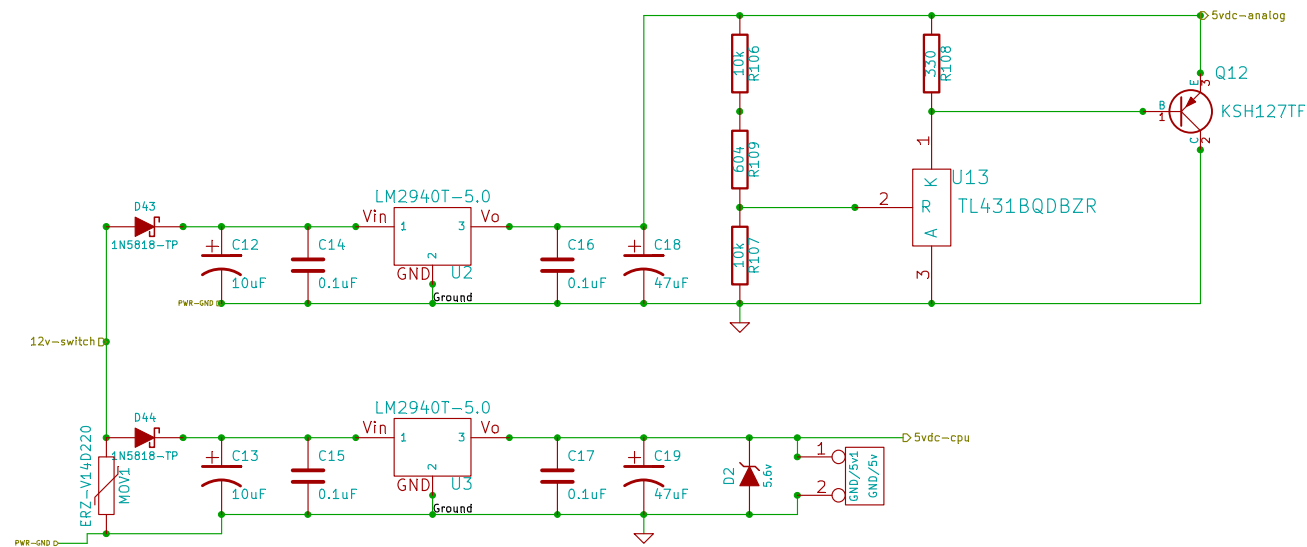
Size: A4

Date: 24 may 2014

Rev: 0.7-alpha

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Id: 6/7



C14, C15, C16 and C17 are 50v X7R Ceramic capacitors.

C12 and C13 are 35v Tantalum capacitors.

C18 and C19 are 10v Tantalum capacitors.

Git: 3b0981b7b7

File: PowerSupplies.sch

Sheet: /Power Supplies/

Title: Jaguar PCB for FreeEMS

Size: A4

Date: 24 may 2014

Rev: 0.7-alpha

KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable

Id: 7/7