

## Index

### System Block

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## Power Block

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## USB CHANEL

PCIINT#	Device
USB1	System -1
USB2	P-R1
USB3	System-2
USB4	PR -2
USB5	Bluetooth
USB6	Bay

## PCI REQ#/GNT#

REQ#/GNT#	Device
REQ#0	PCMCIA
REQ#1	IEEE1394
REQ#2	Unused
REQ#3	On Board LAN
REQ#4	Mini PCI
REQ#5	Unused

## PCI INTERRUPT

PCIINT#	Device
INT#0	PCIC USB#1 / IDE
INT#1	PCIC SMBus/AC-97
INT#2	Mini-PCI USB#3
INT#3	Mini-PCI USB#2
INT#4	On Board LAN
INT#5	IEEE1394
INT#6	Unused
INT#7	Unused

## SMBus

SMBCNT[2:0]	Device
0,0,0	DIMM SLOT-0/1
0,0,1	PMU
0,1,0	PLL
0,1,1	ADM1030

## Version History

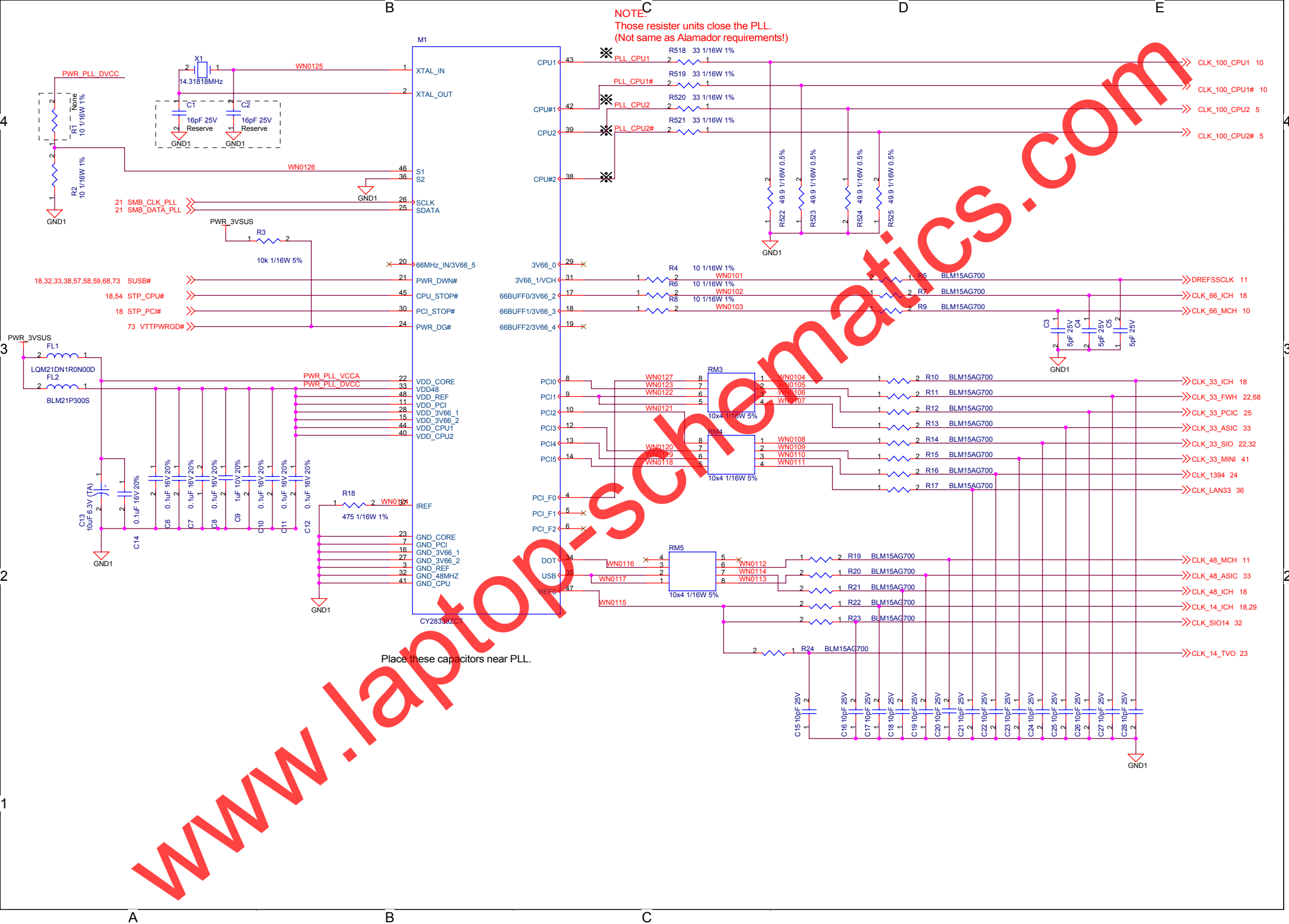
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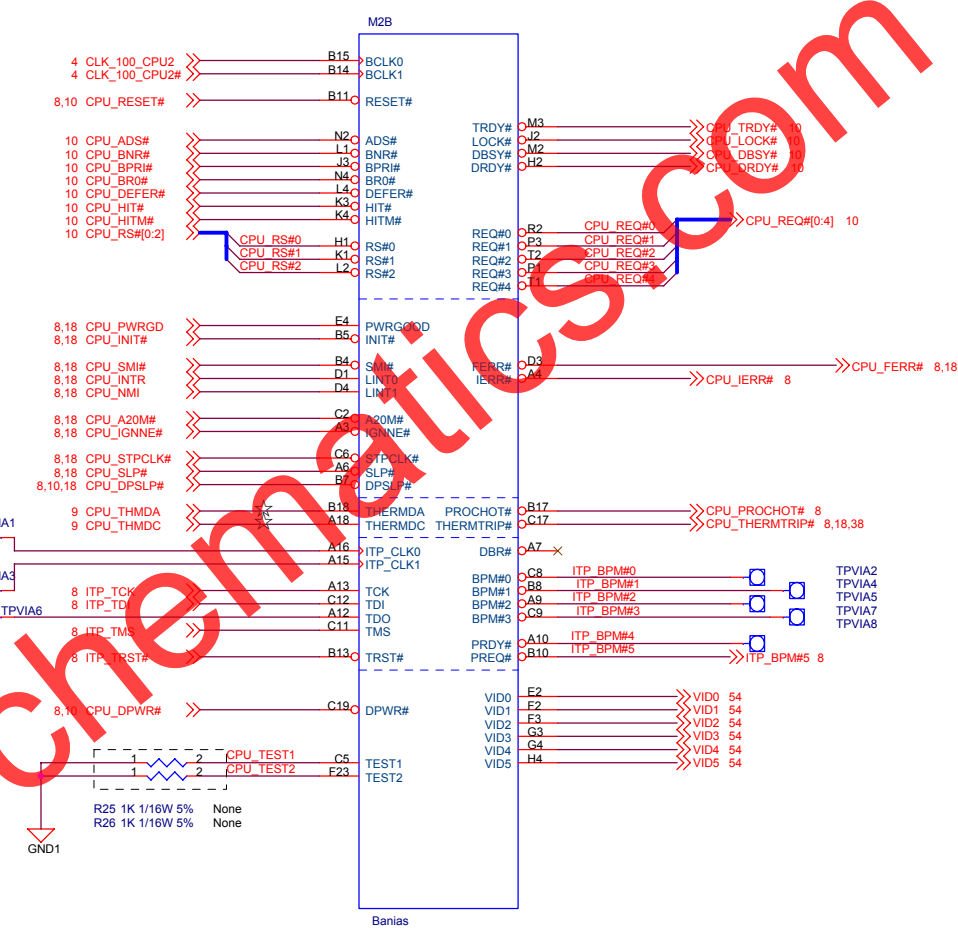
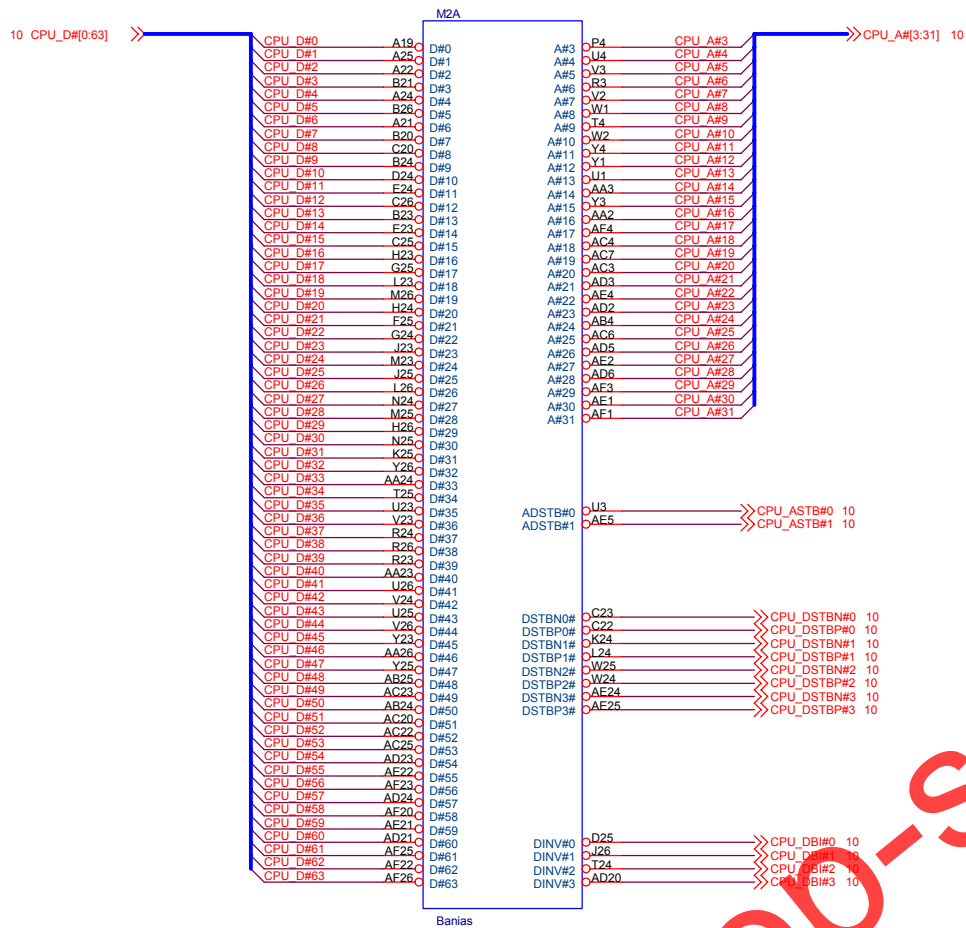
## PCI BUS NUMBER = #0

IDSEL	Dev.ID	Device
AD16	00h	GMCH(HOST/DRAM BRIDGE)
AD17	01h	GMCH(AGP BRIDGE)
AD18	02h	Unused
	1Dh	ICH4-M (USB2.0/USB1.1)
	1Eh	ICH4-M(Hub I/F to PCI Bridge)
	1Fh	ICH4-M(LPC,IDE,SMbus,AC-97)

PCI BUS NUMBER = #2

IDSEL	Dev.ID	Device
AD24	08h	Reserved for ICH4 (Internal LAN)
AD25	09h	OnBoard LAN
AD26	0Ah	PCIC
AD28	0Ch	On Board LAN
AD29	0Dh	MiniPCI
AD30	0Eh	IEEE1394
AD31	0Fh	Unused





CPU\_ASTB#0, CPU\_ASTB#1はそれぞれの両側をGND1にて囲うこと。  
Guard CPU\_ASTB#0, CPU\_ASTB#1 with GND1 respectively.

CPU\_DSTBx#[0:3]は各ペアごとに両側をGND1にて囲うこと。  
Each CPU\_DSTBx#[0:3] strobe pair should be guarded with GND1 respectively.

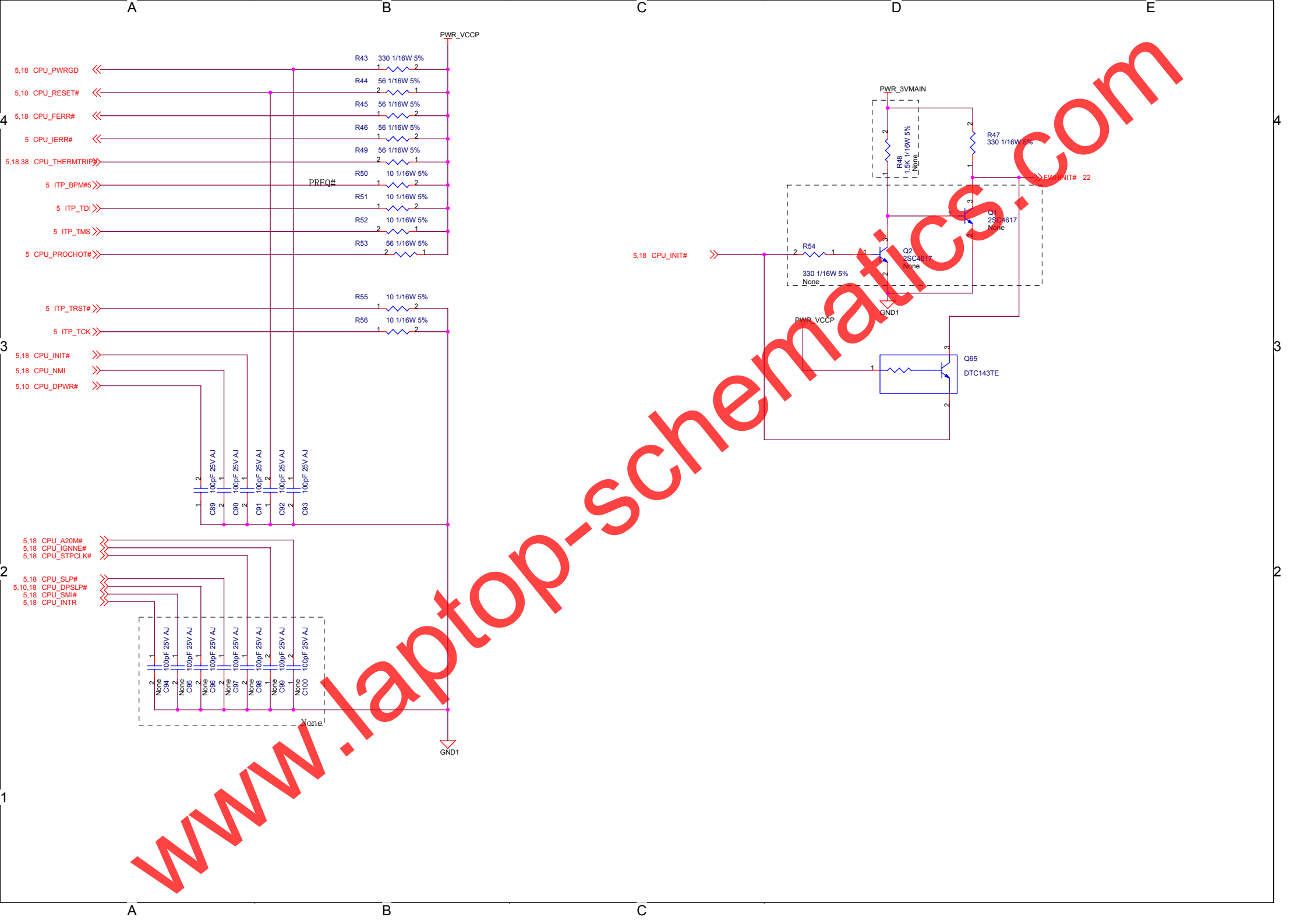
☆印の付いた信号線 (CPUTHMDA, CPUTHMDC) は、  
GND1で両側をガードすること  
(12ページ参照)

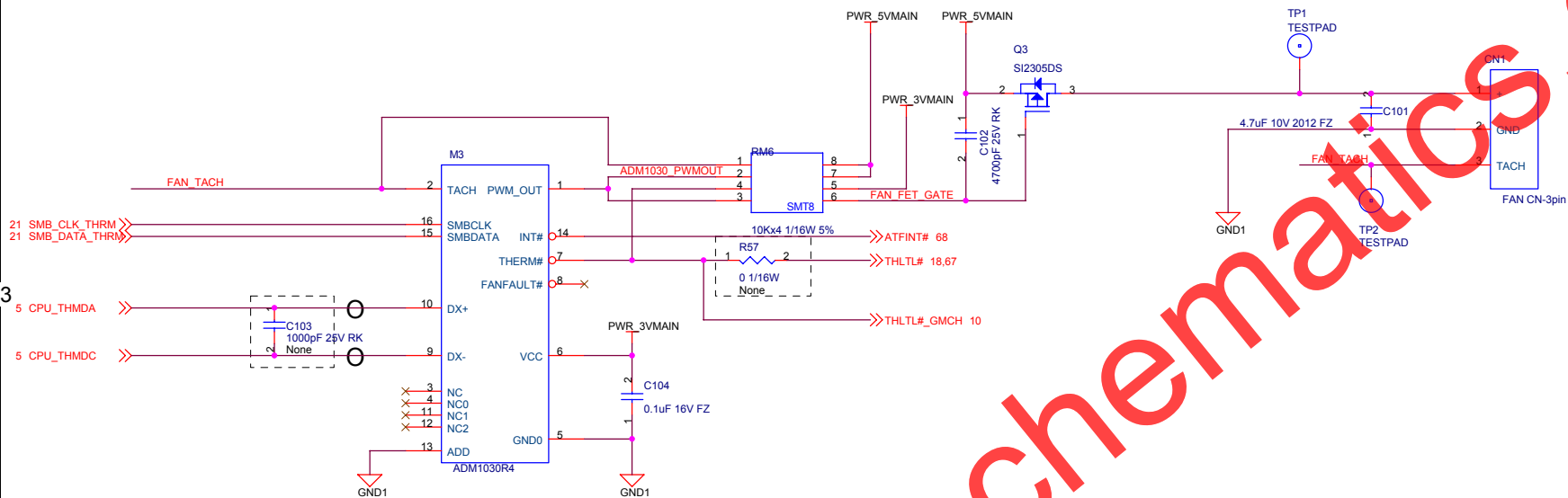
CPUTHMDA, CPUTHMDC with ☆  
is guarded with GND1

**BANIAS\_1**







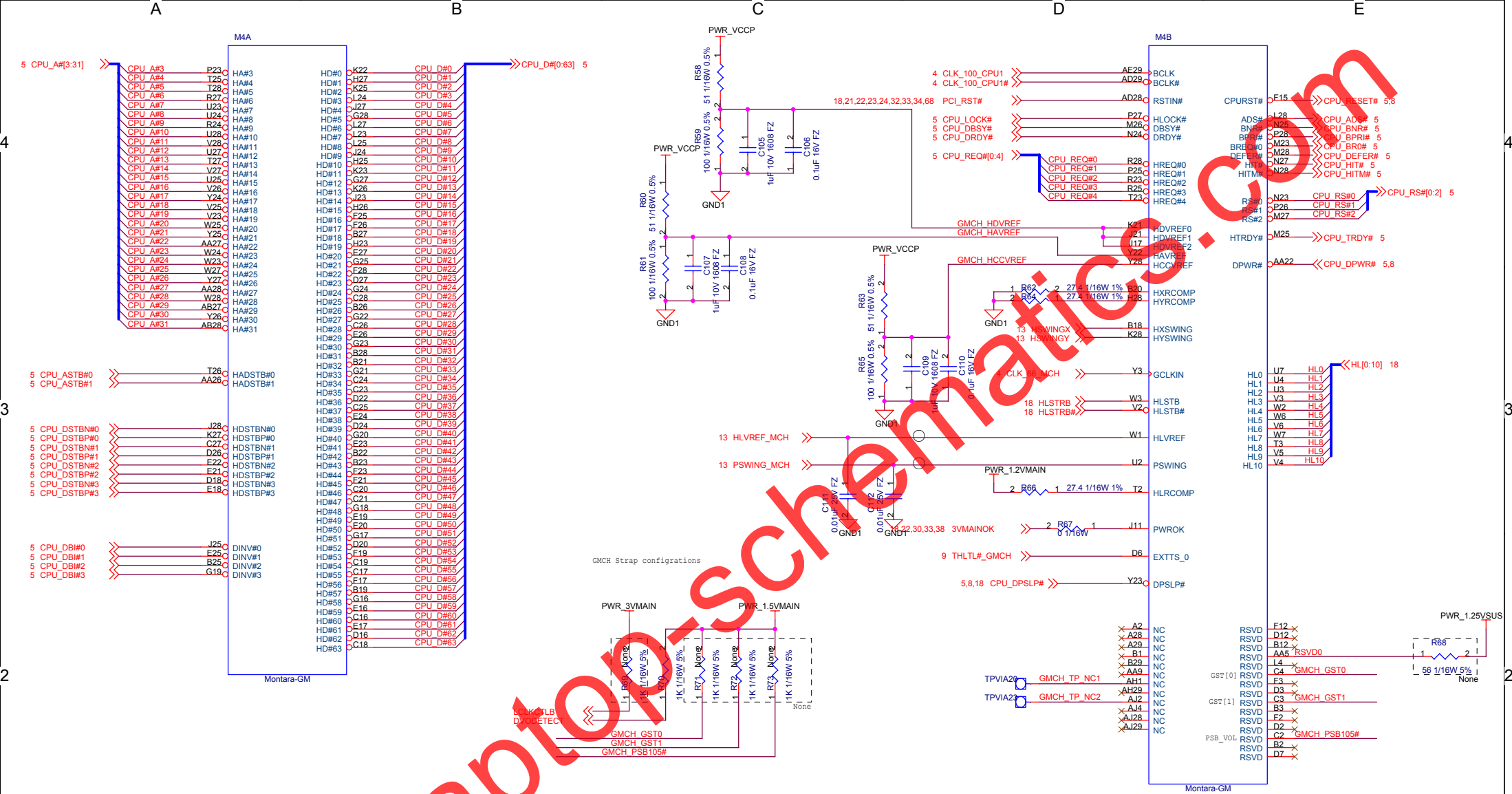


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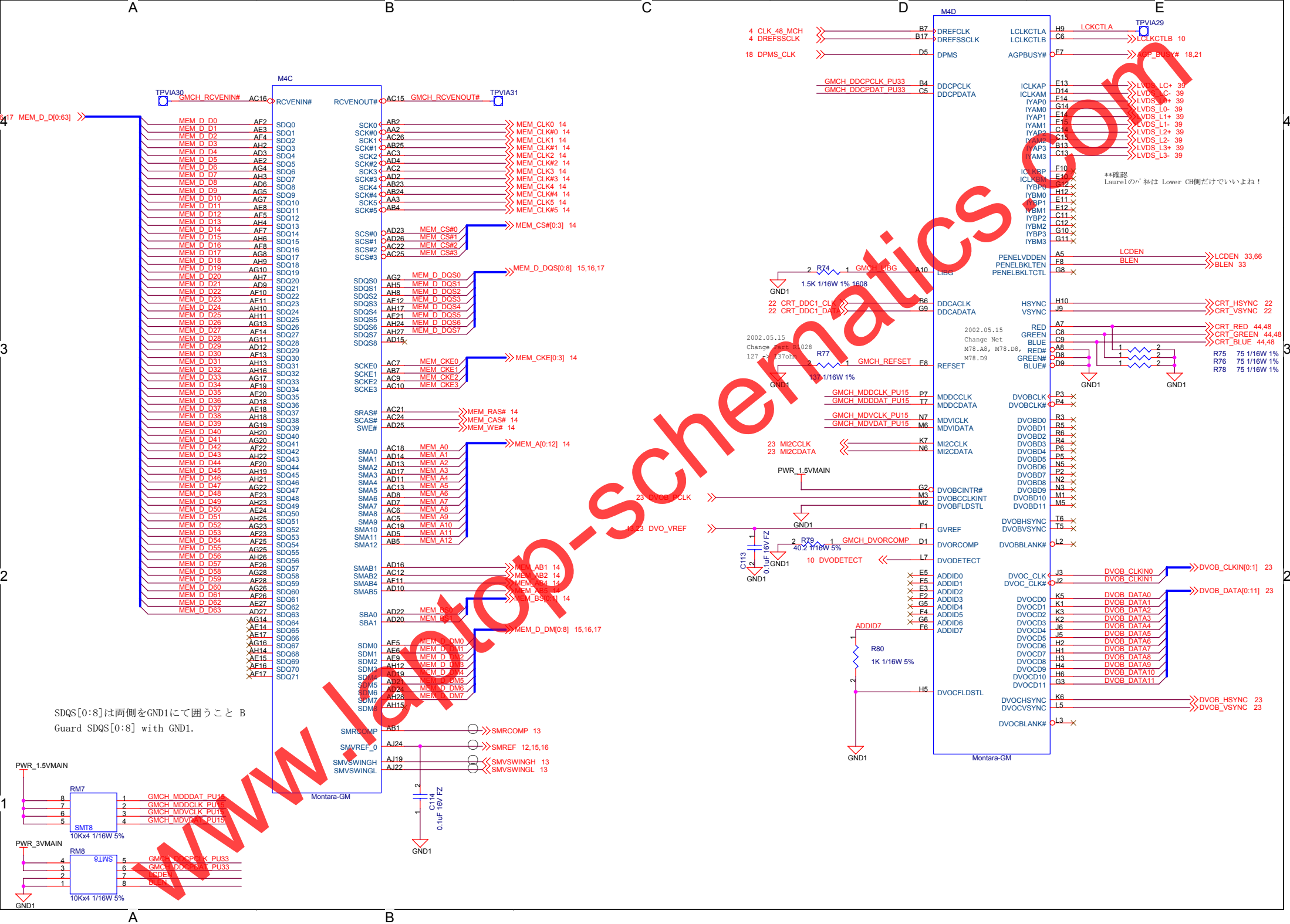
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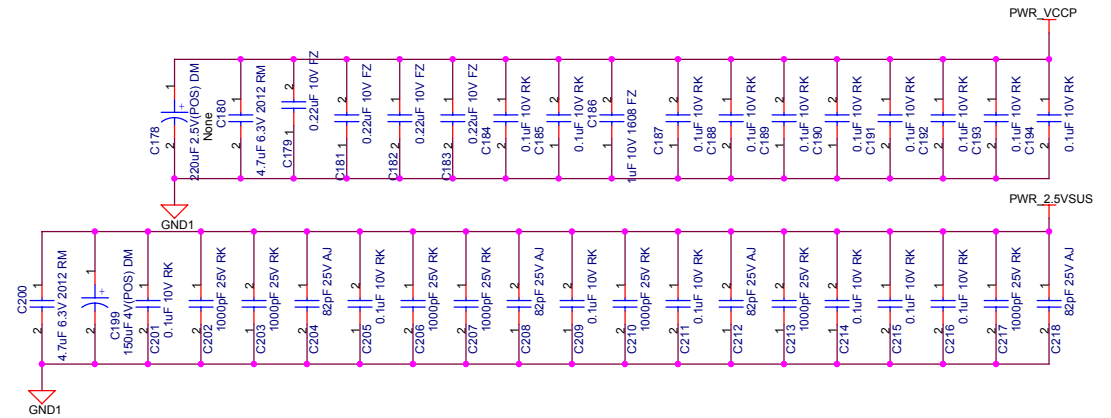
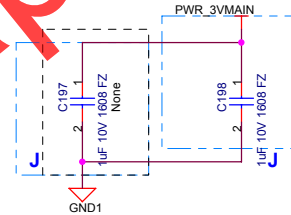
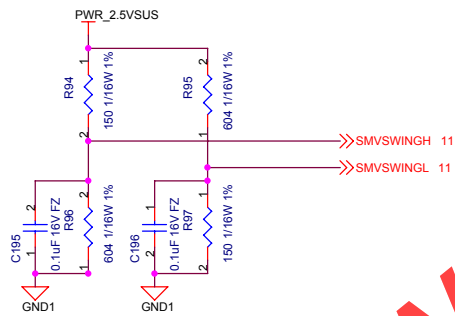
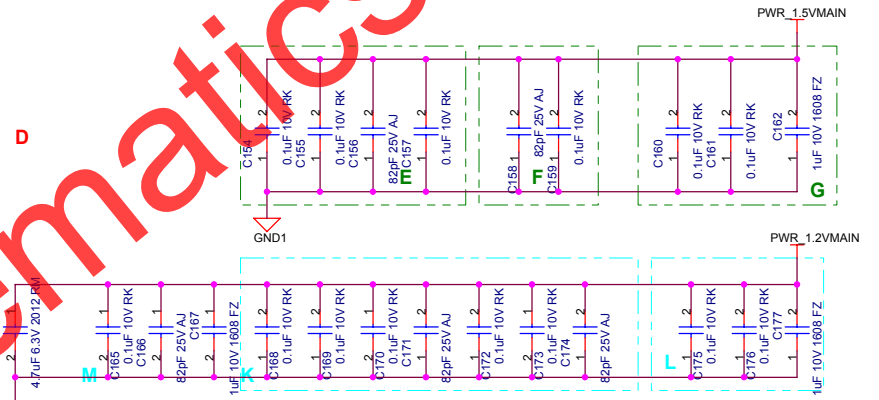
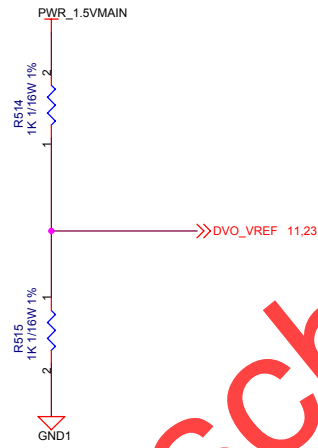
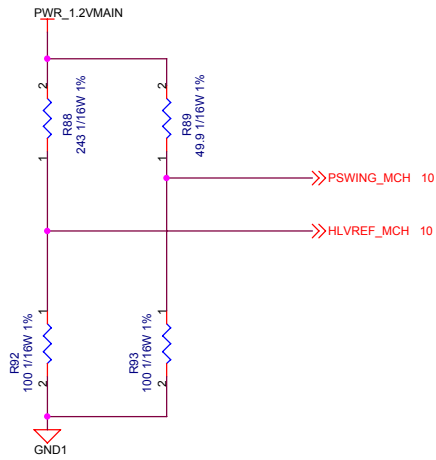
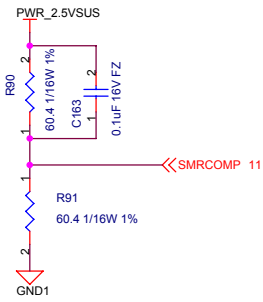
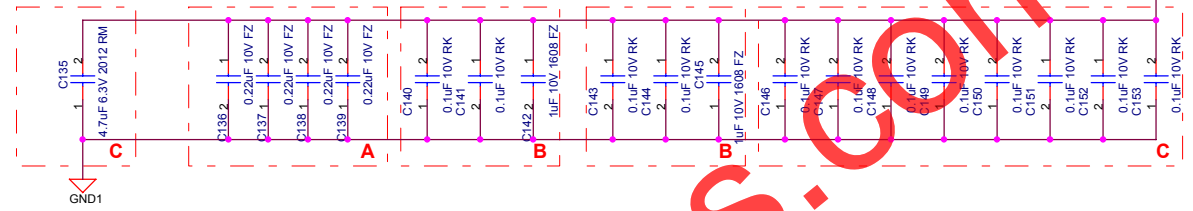
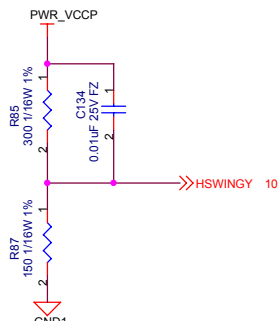
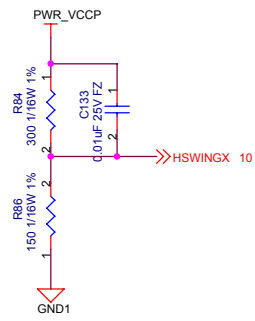
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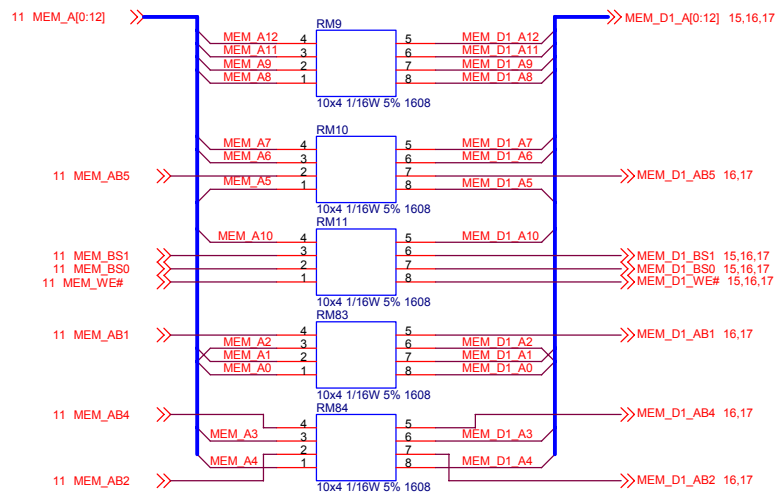




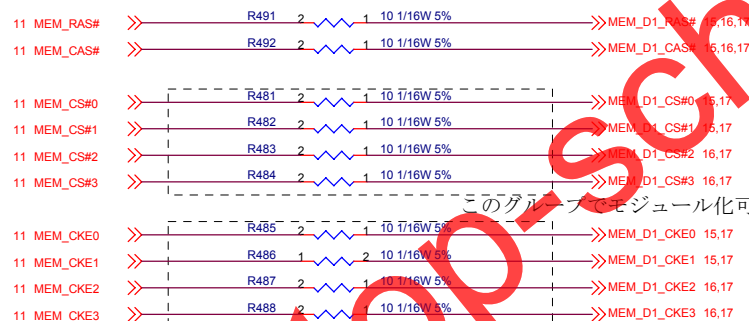








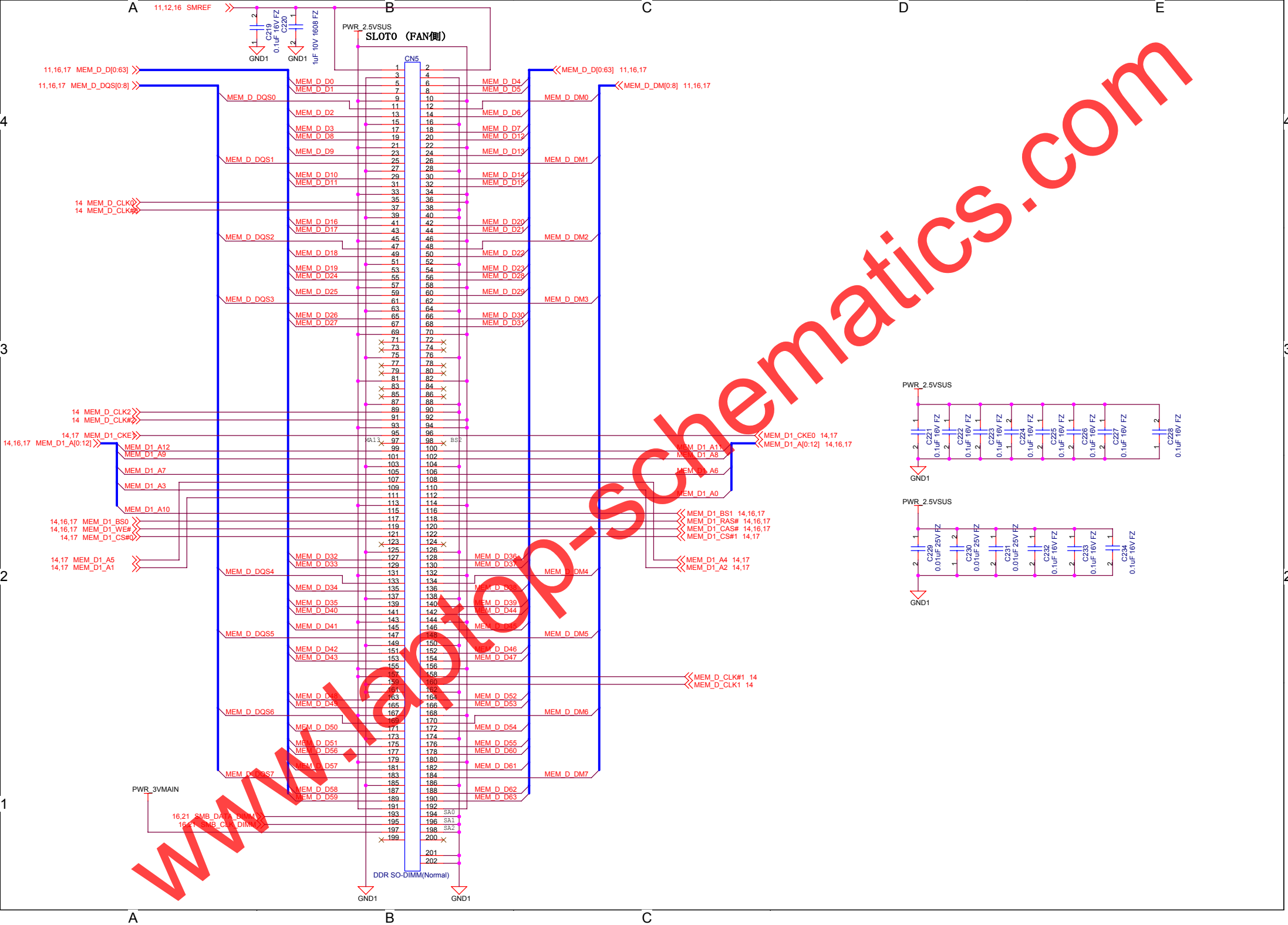
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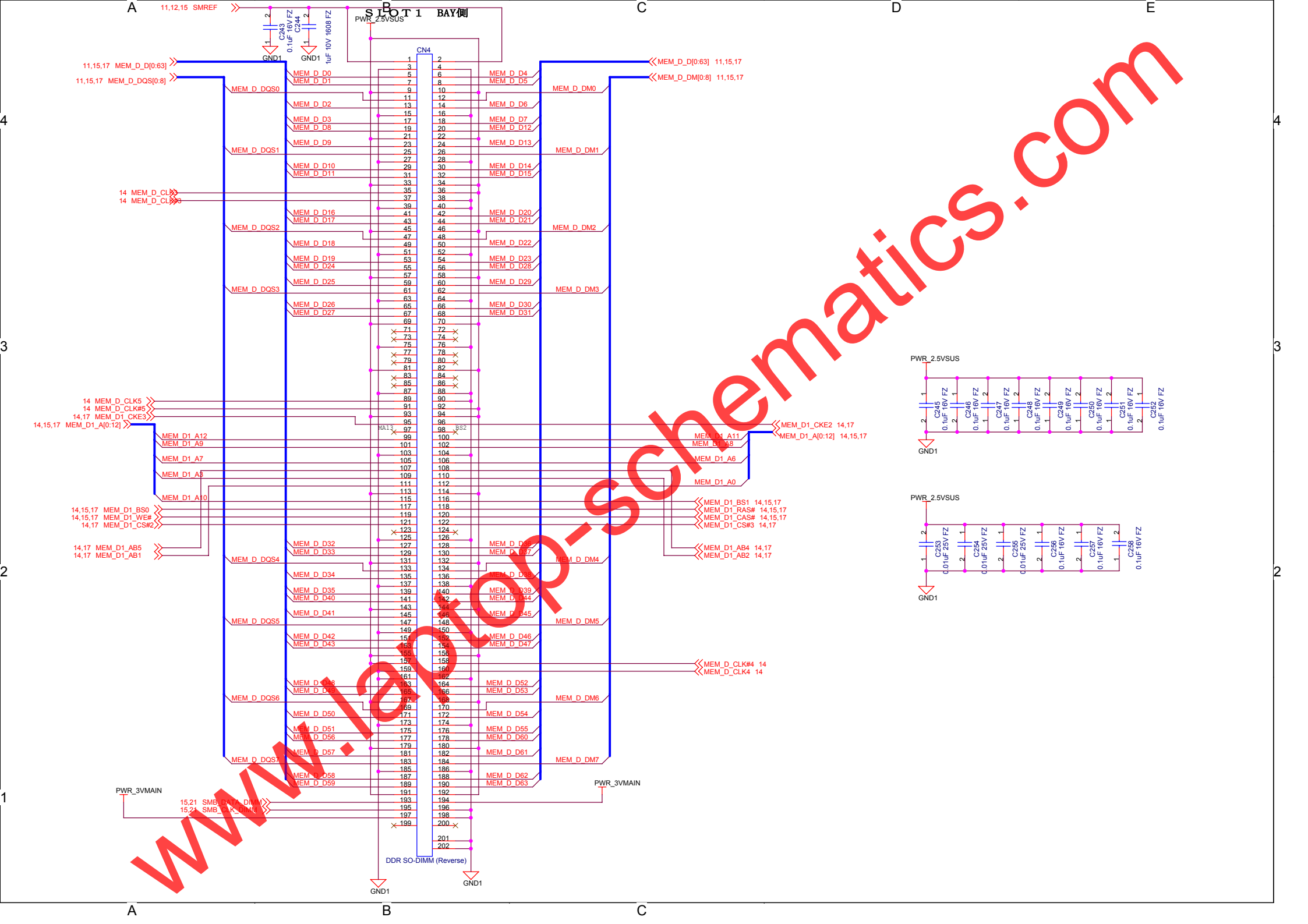


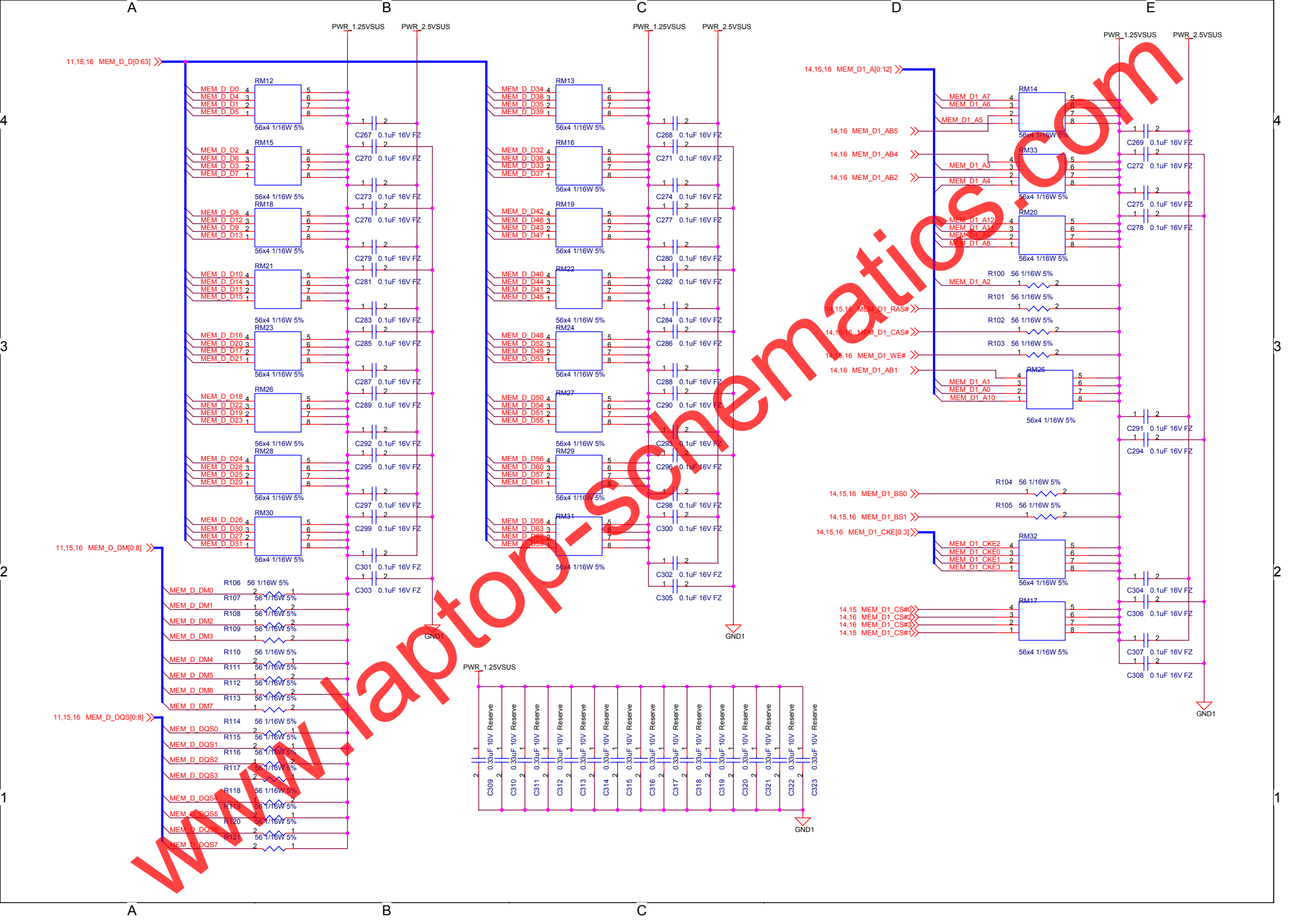
このグループでモジュール化可能

このグループでモジュール化可能

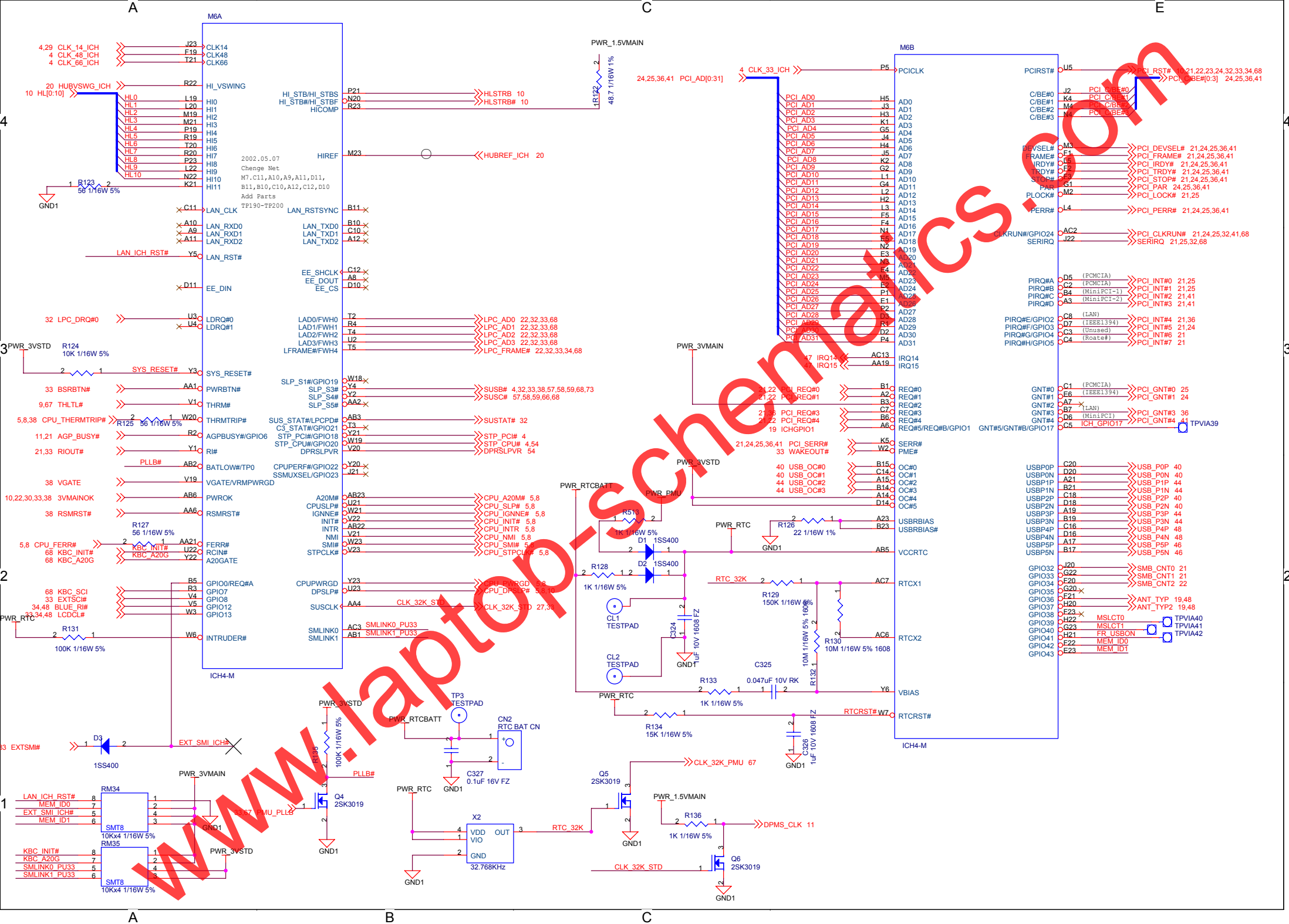




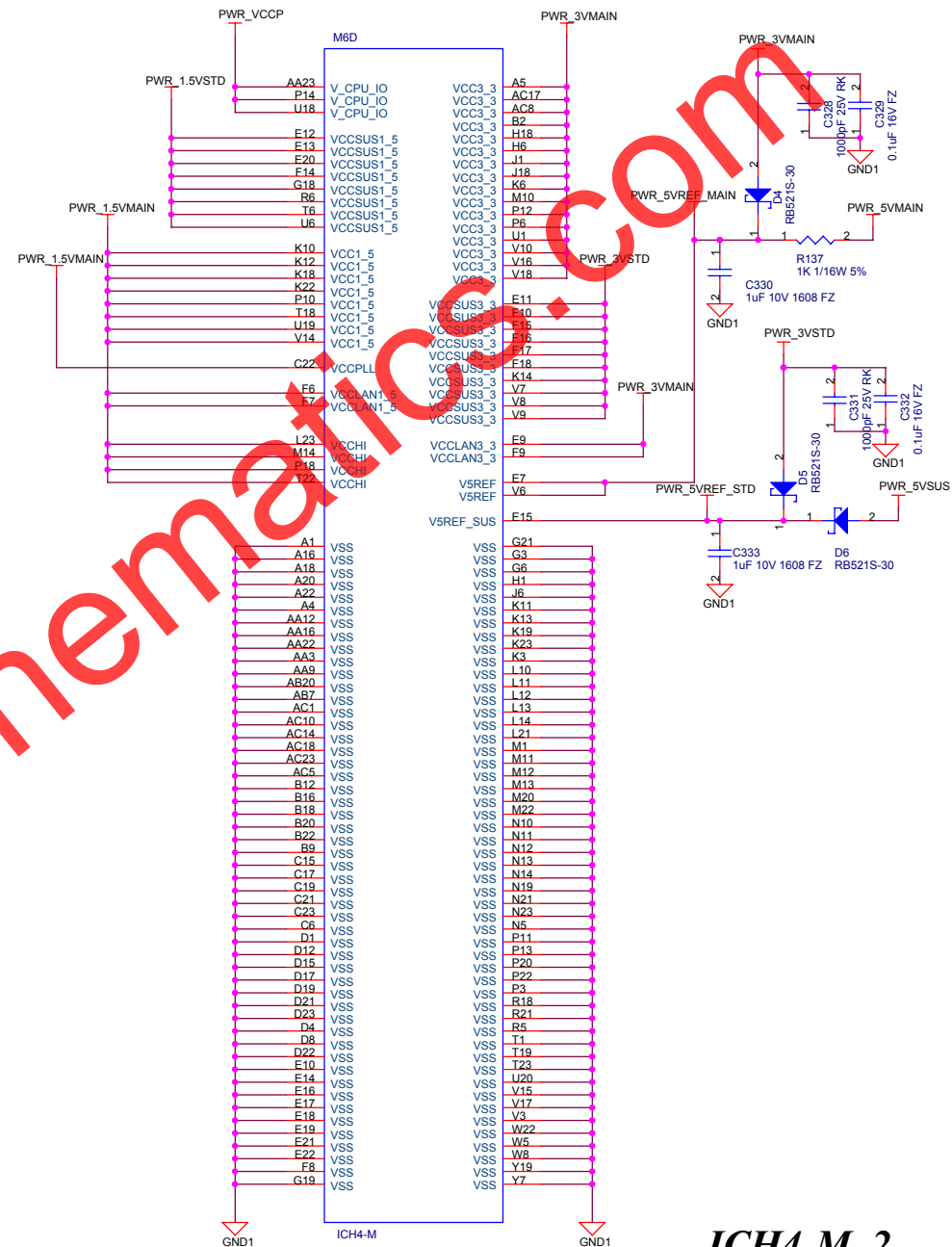
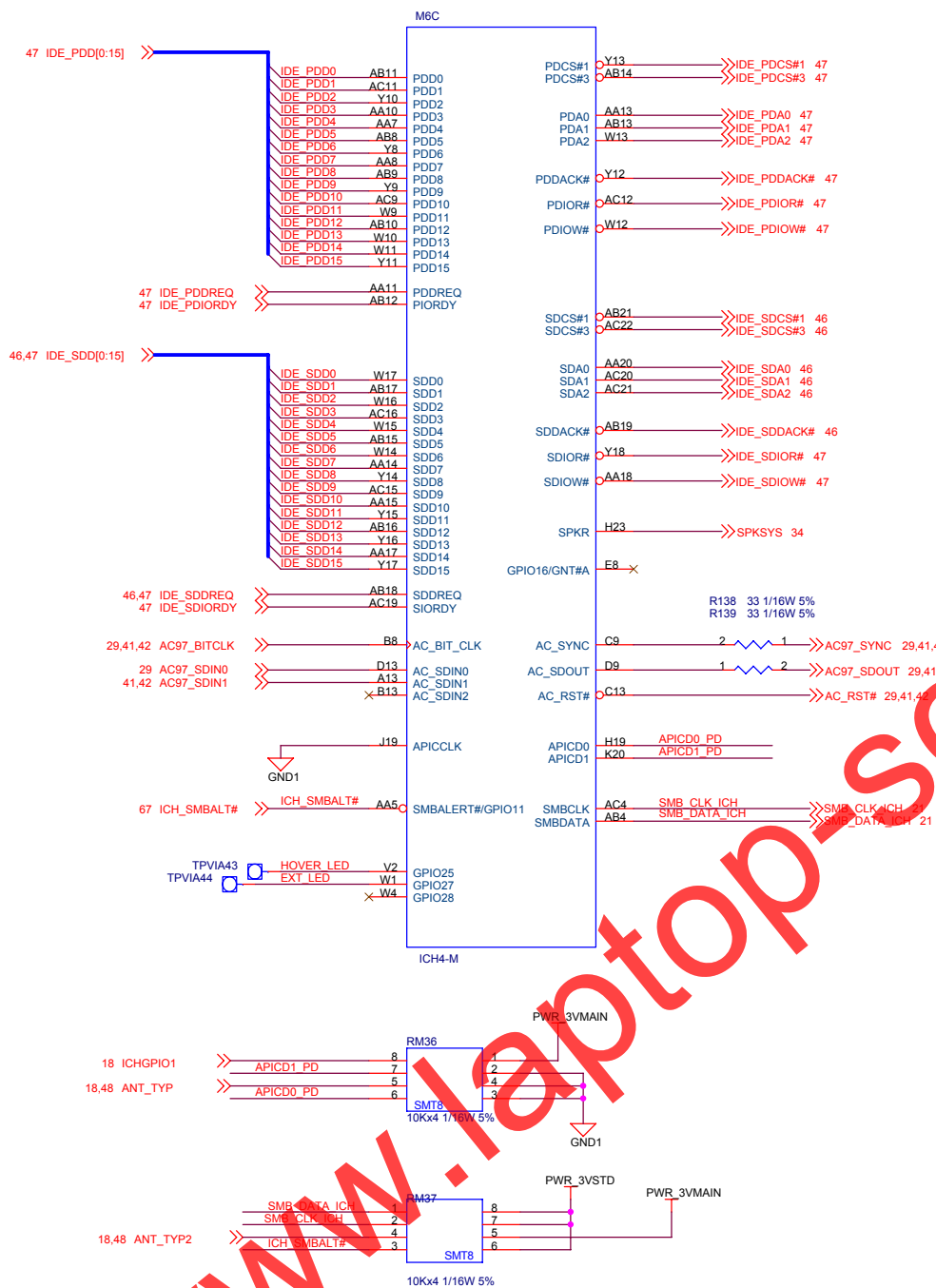




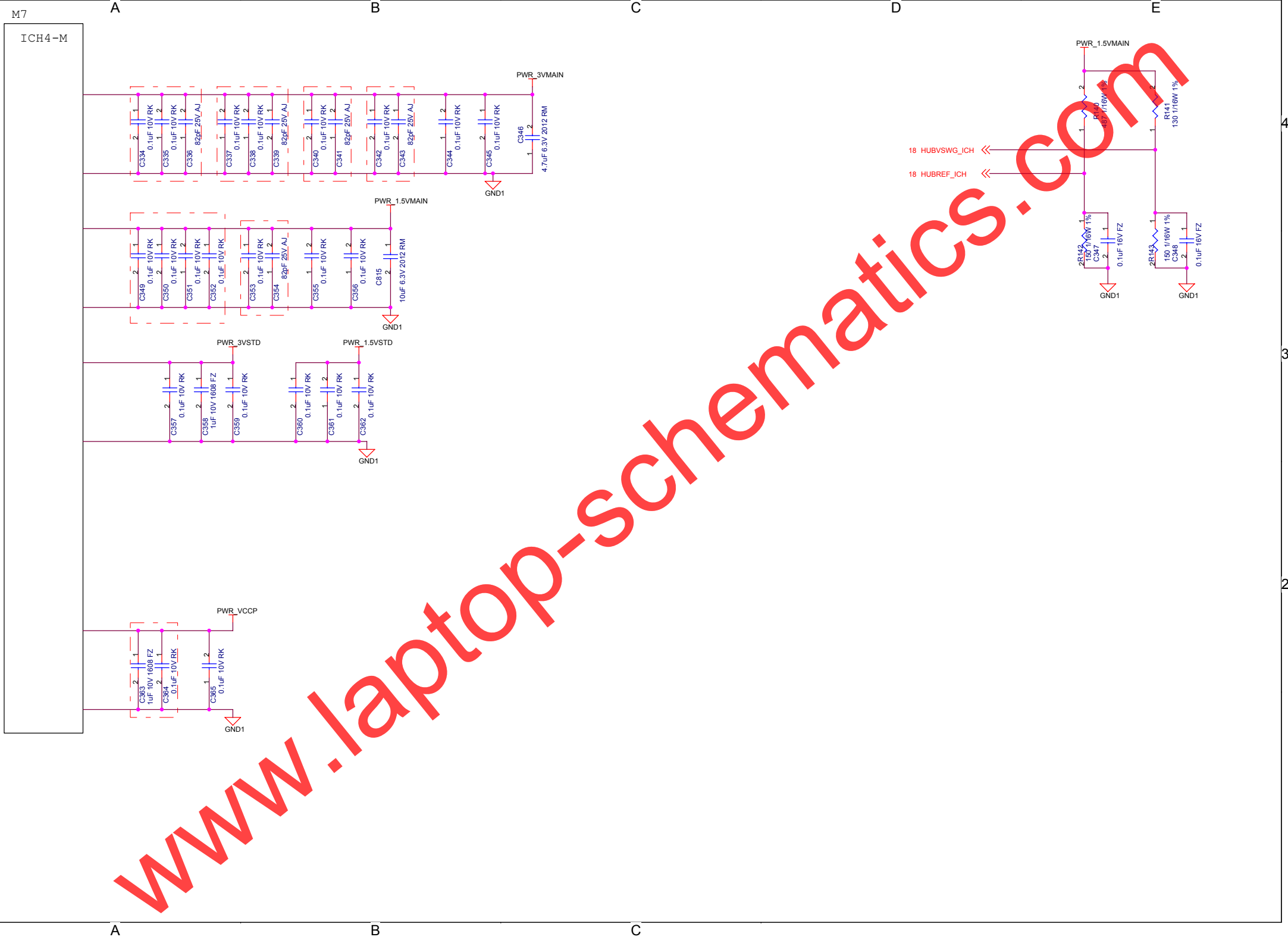


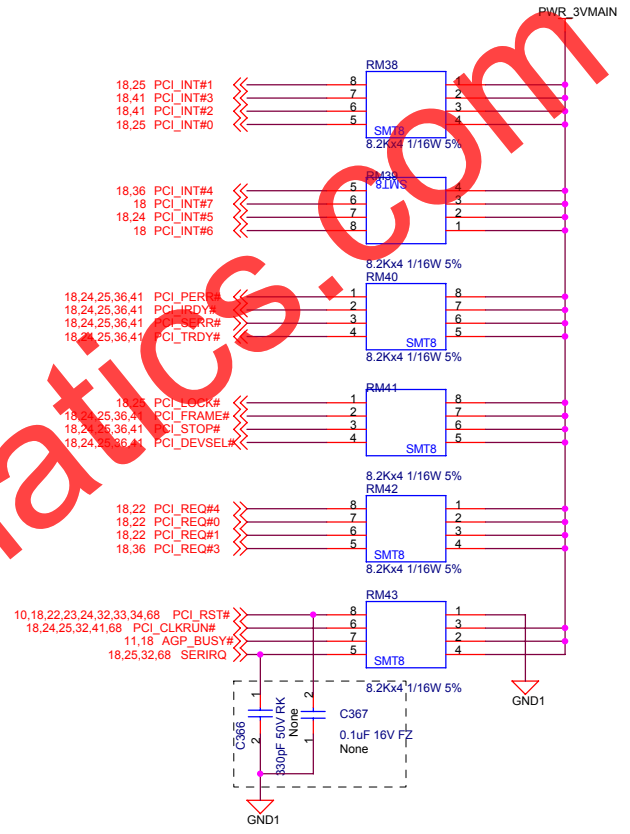
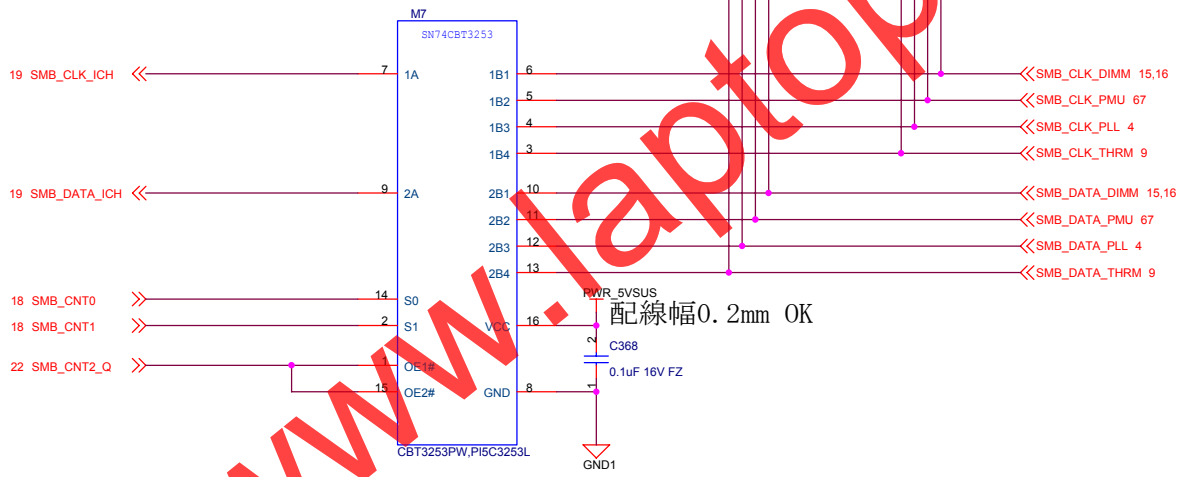
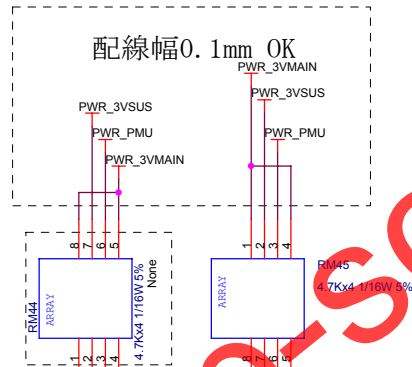
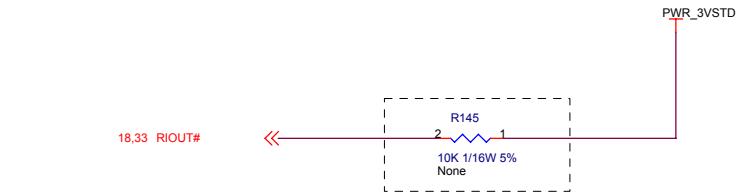


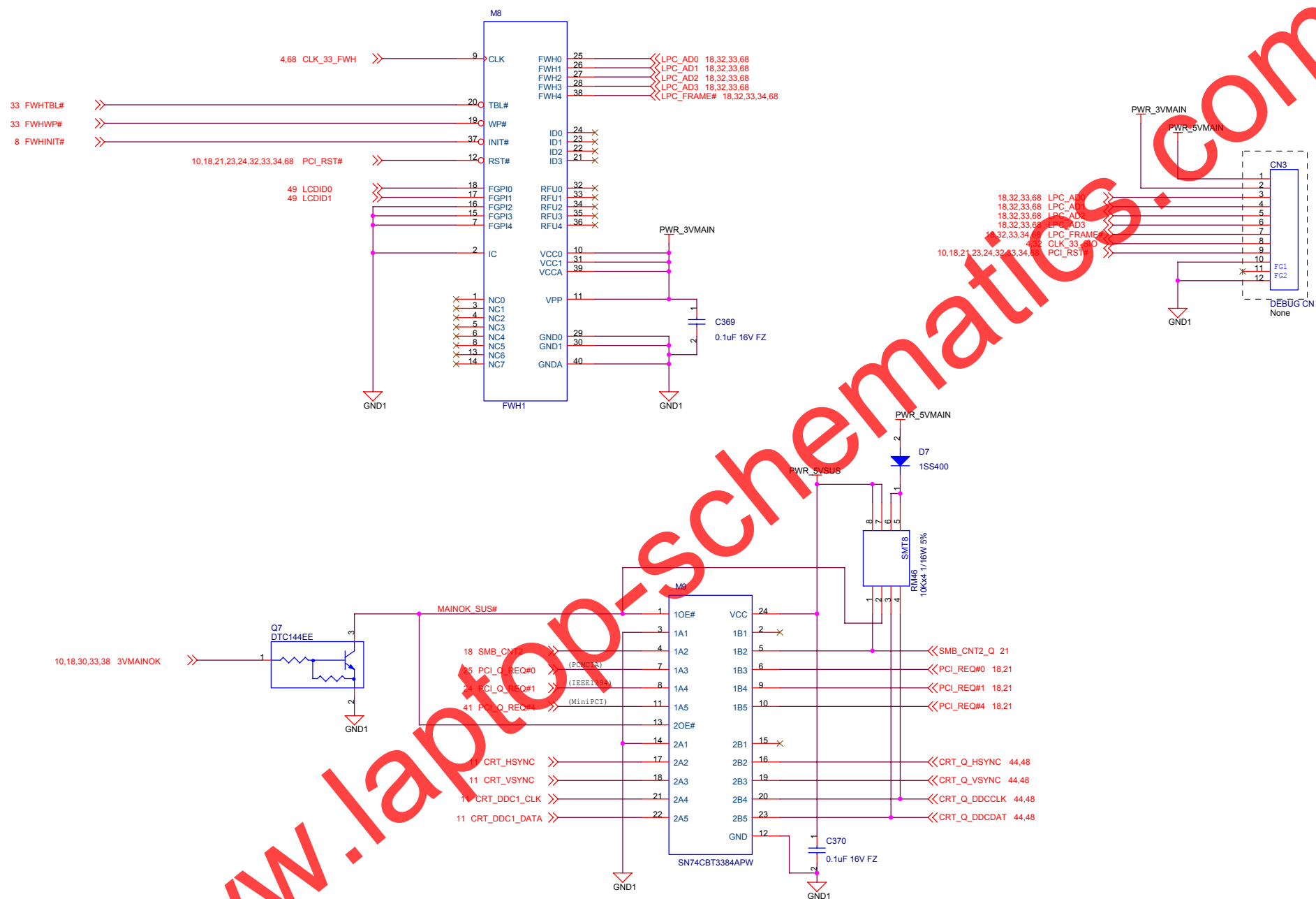


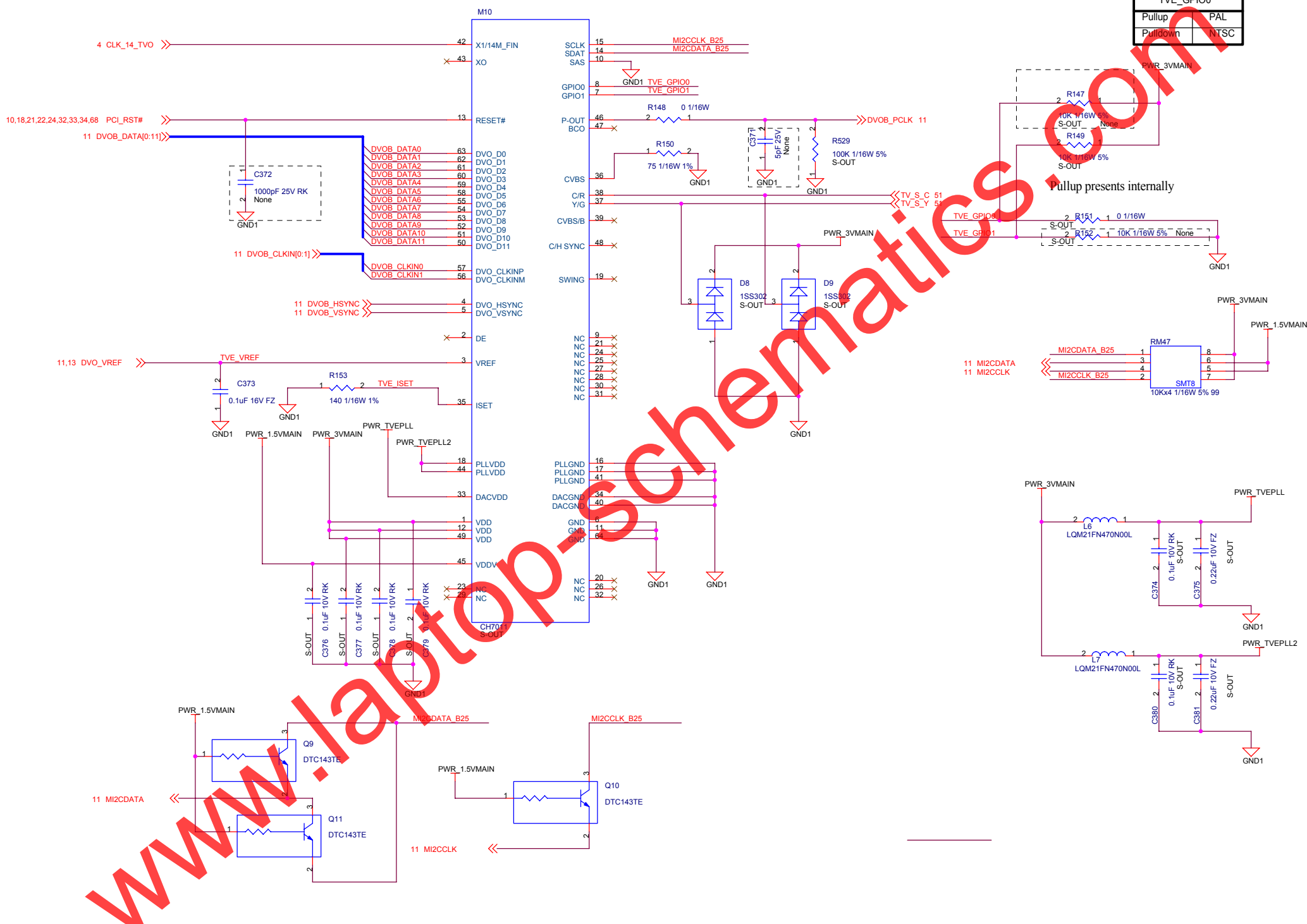


*ICH4-M\_2*



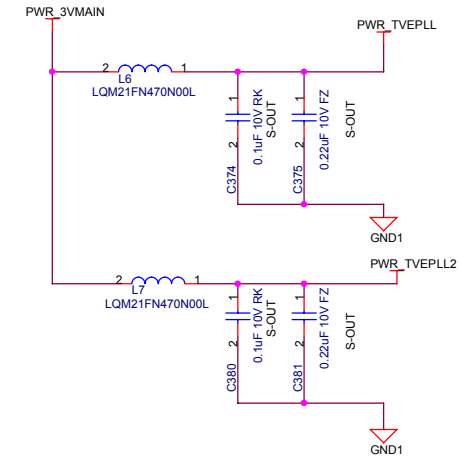
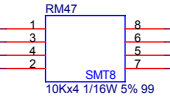




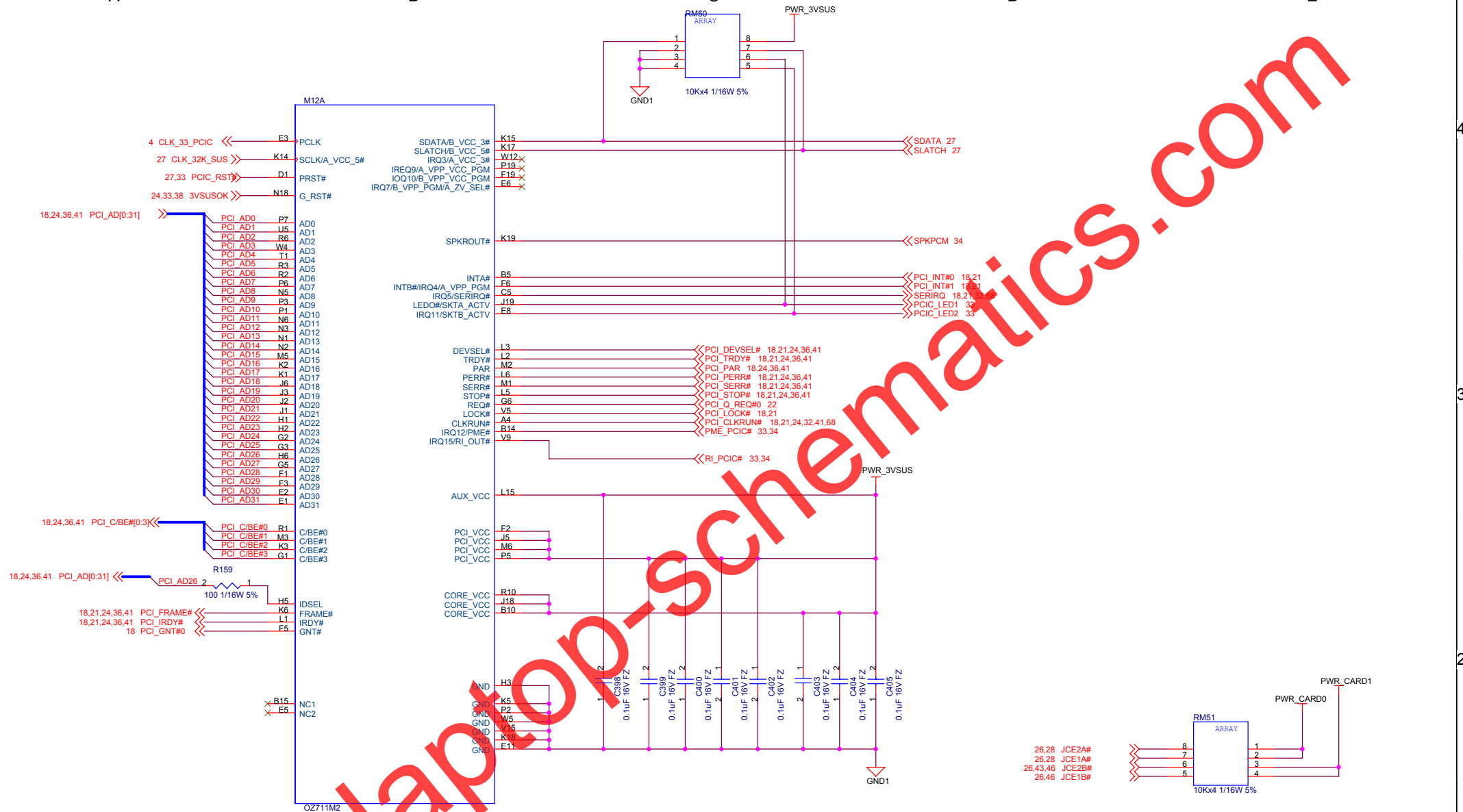


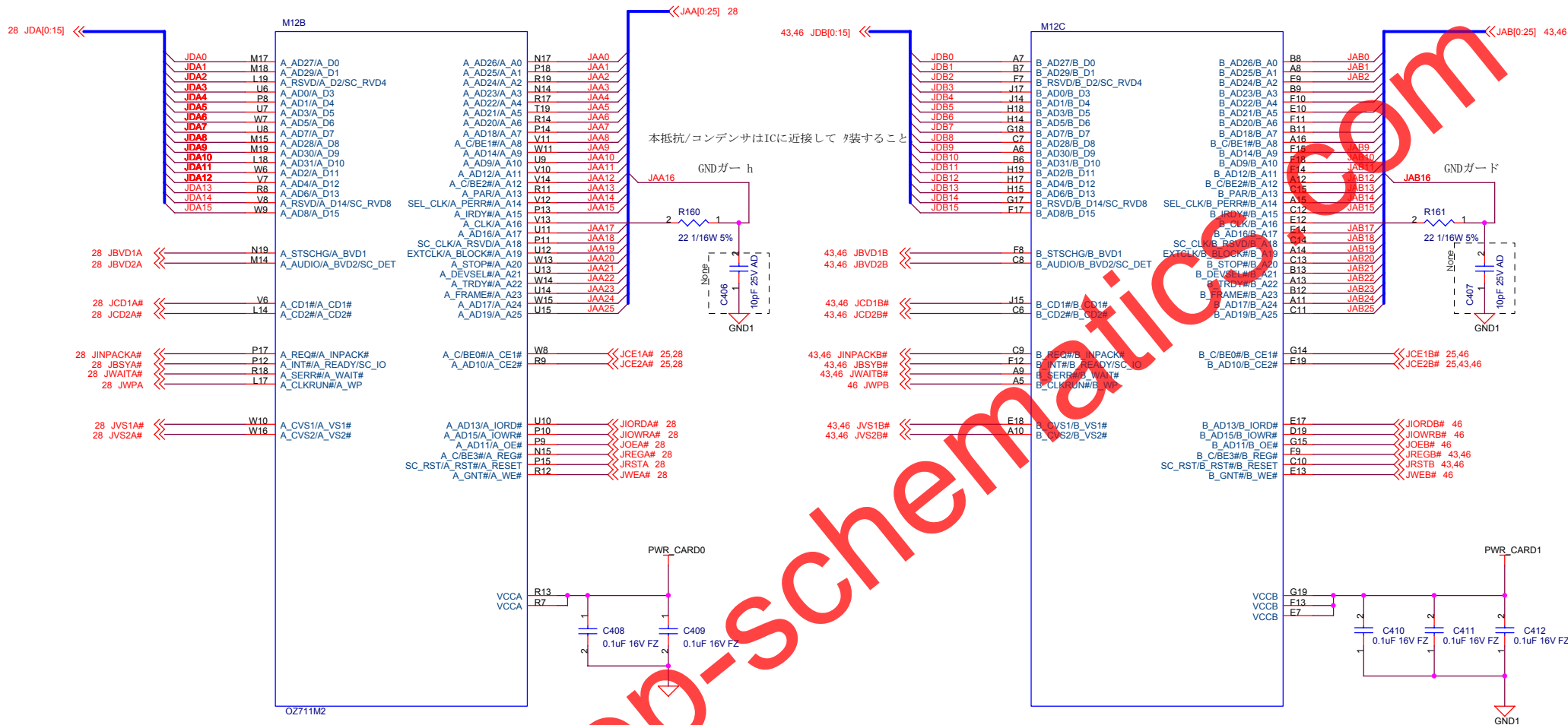
TV_MODESEL		
TVE_GPIO0		
Pullup		PAL
Pulldown		NTSC

Pullup presents internally



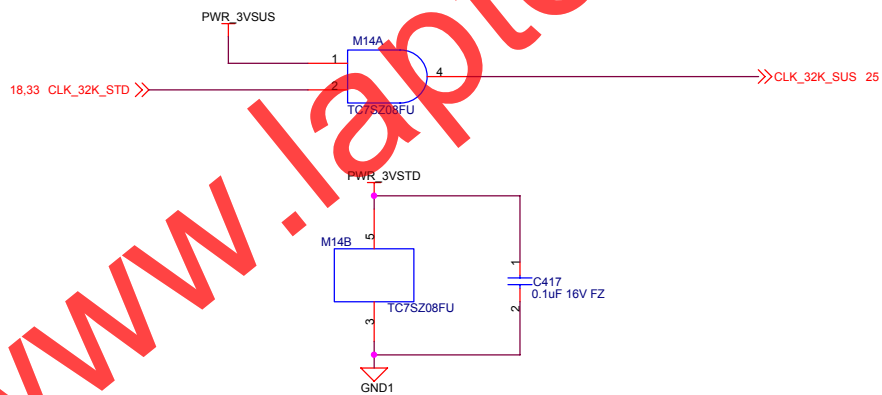
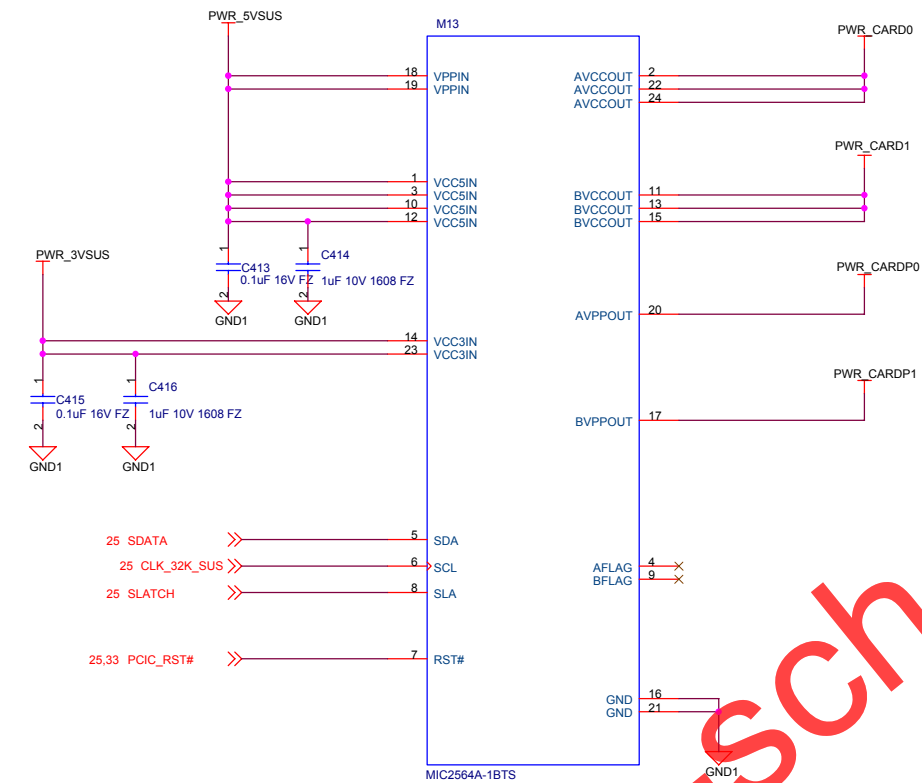


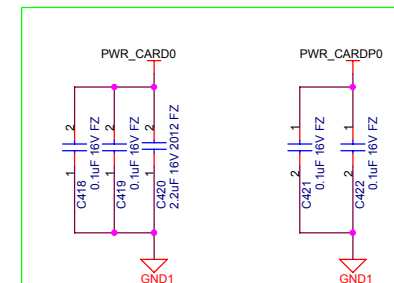
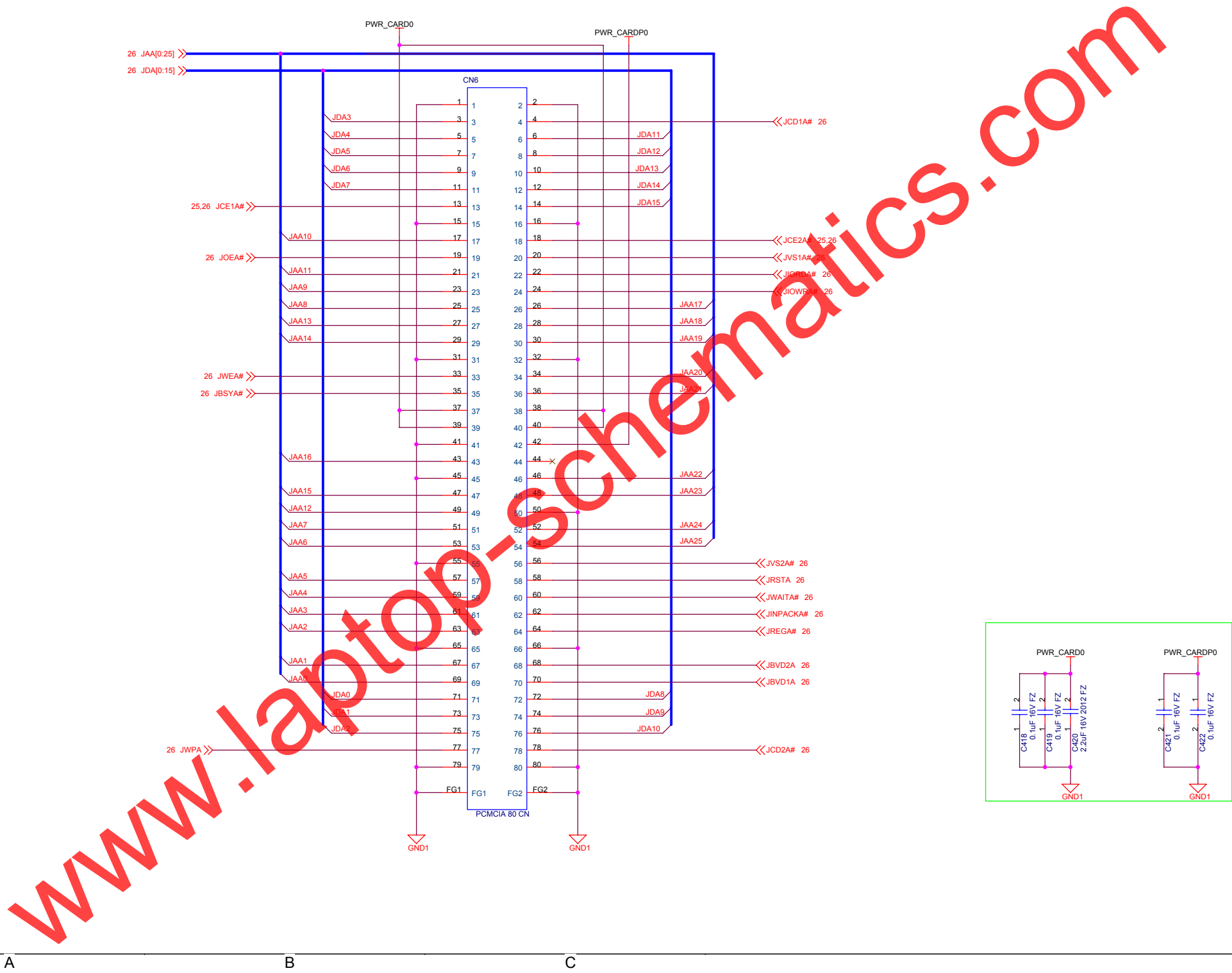


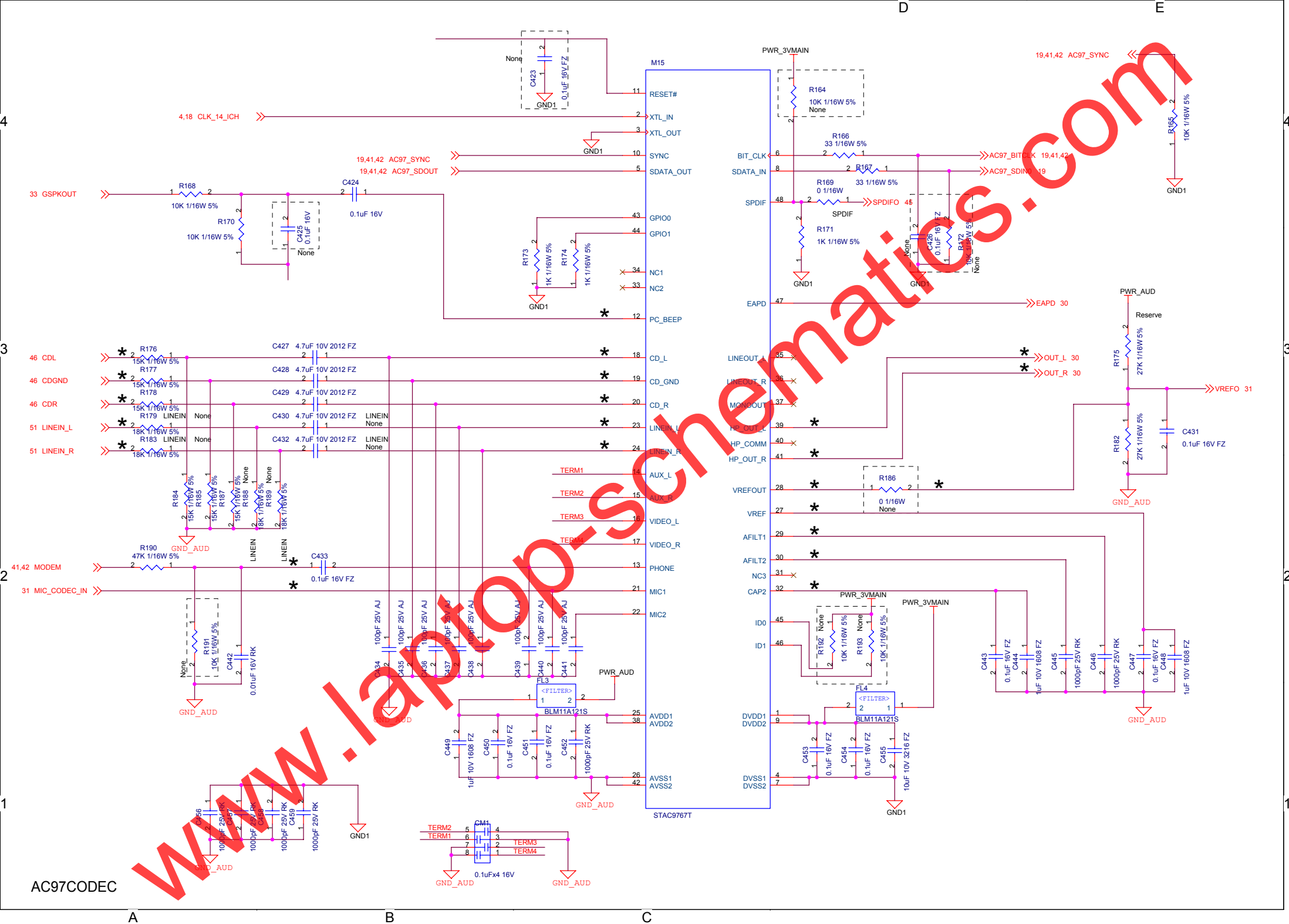


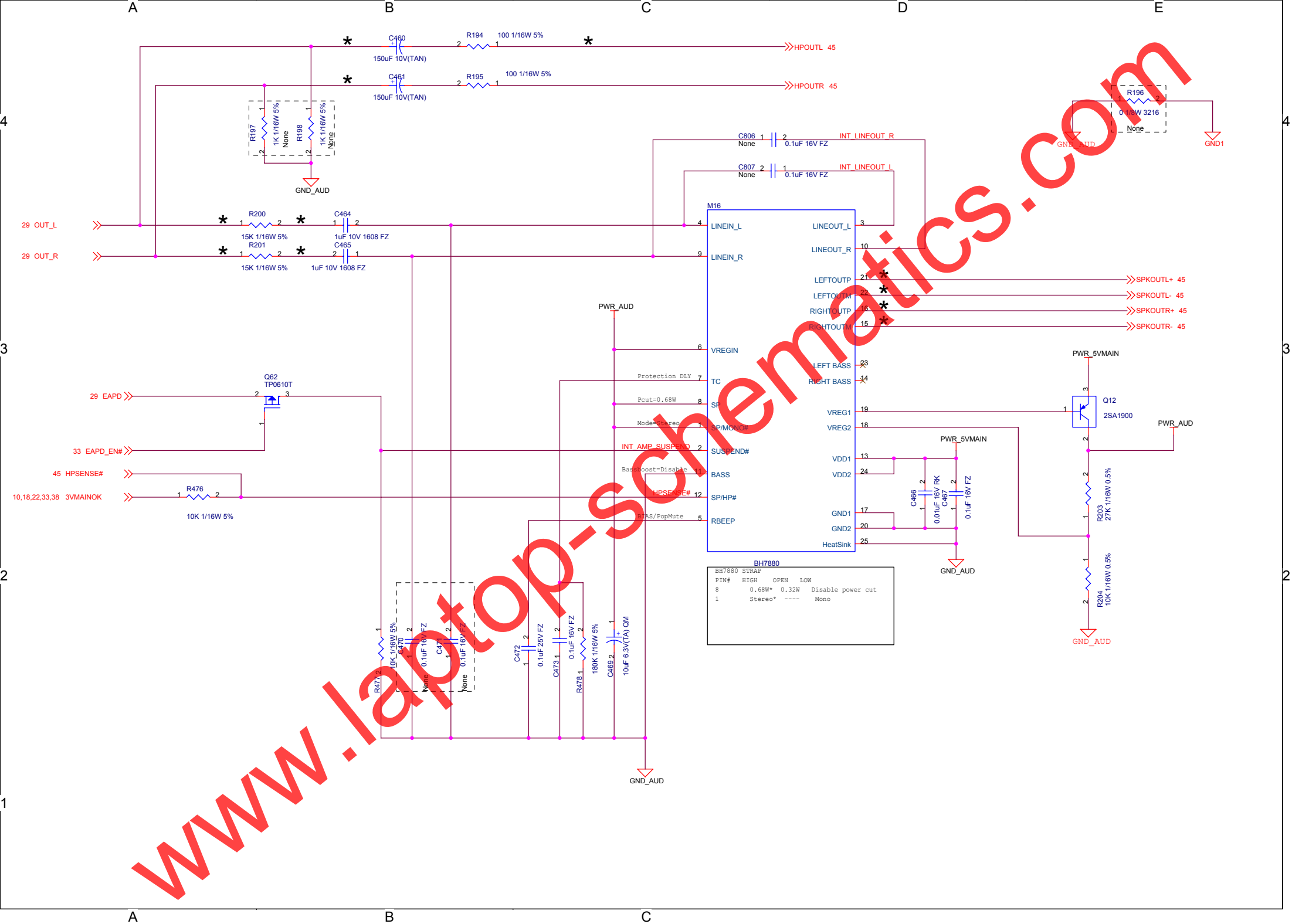
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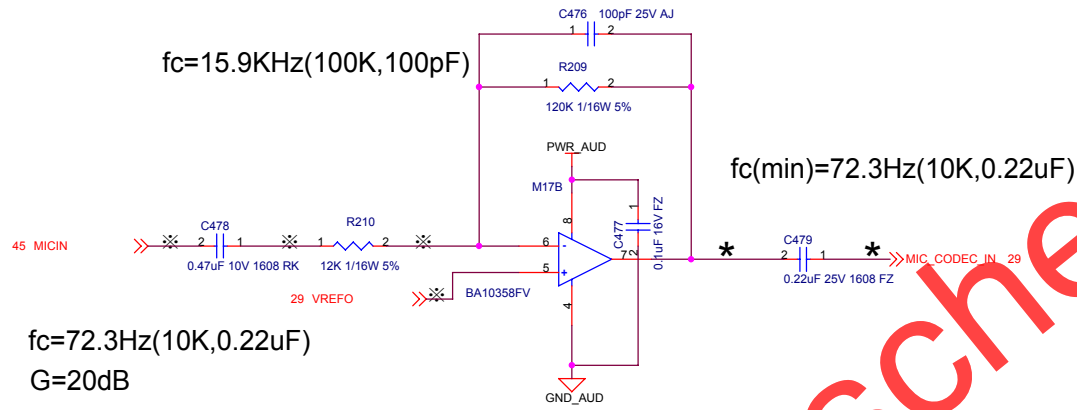
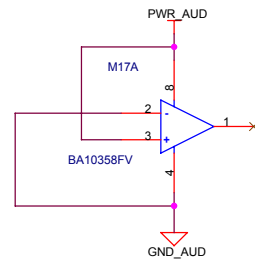




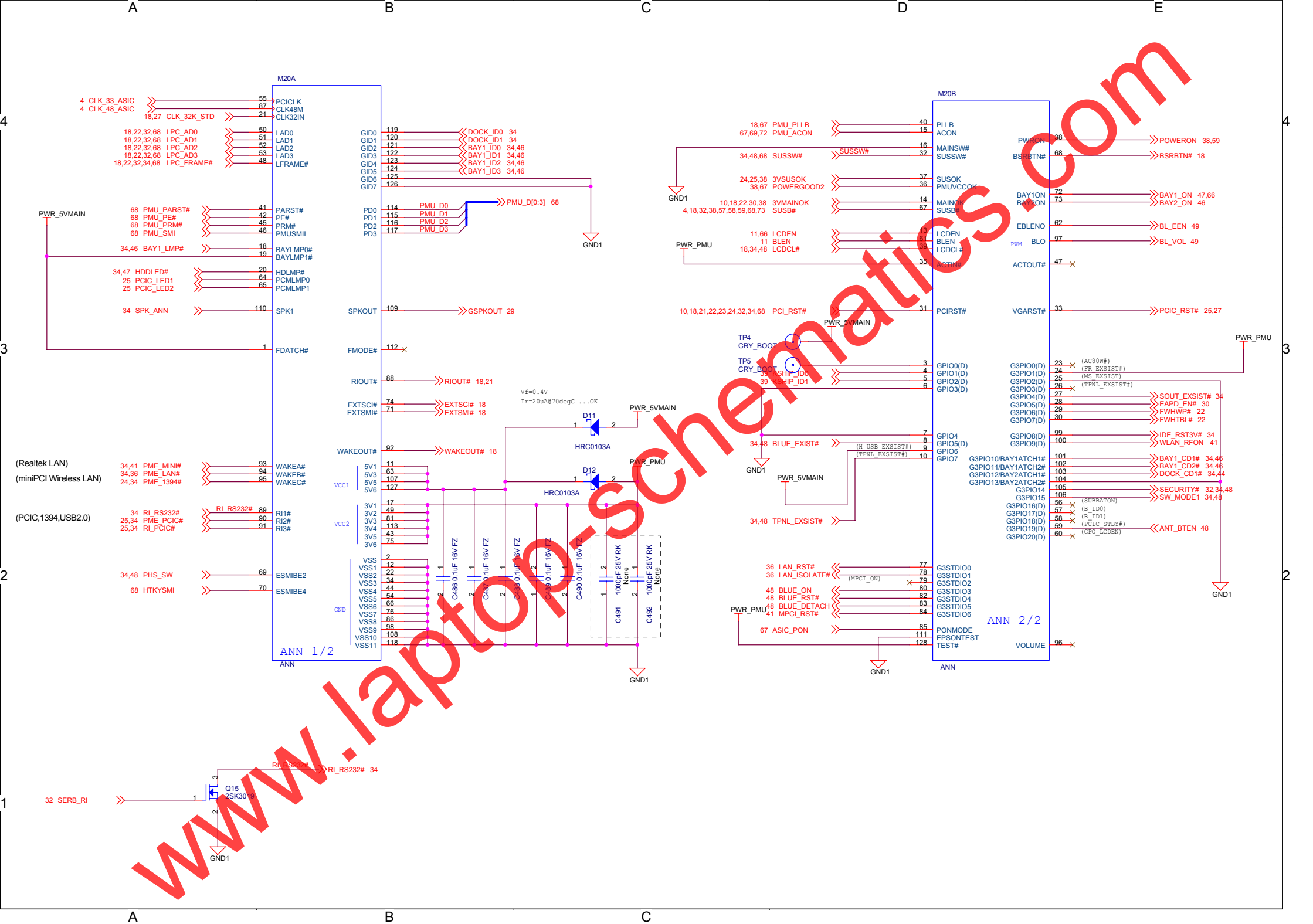


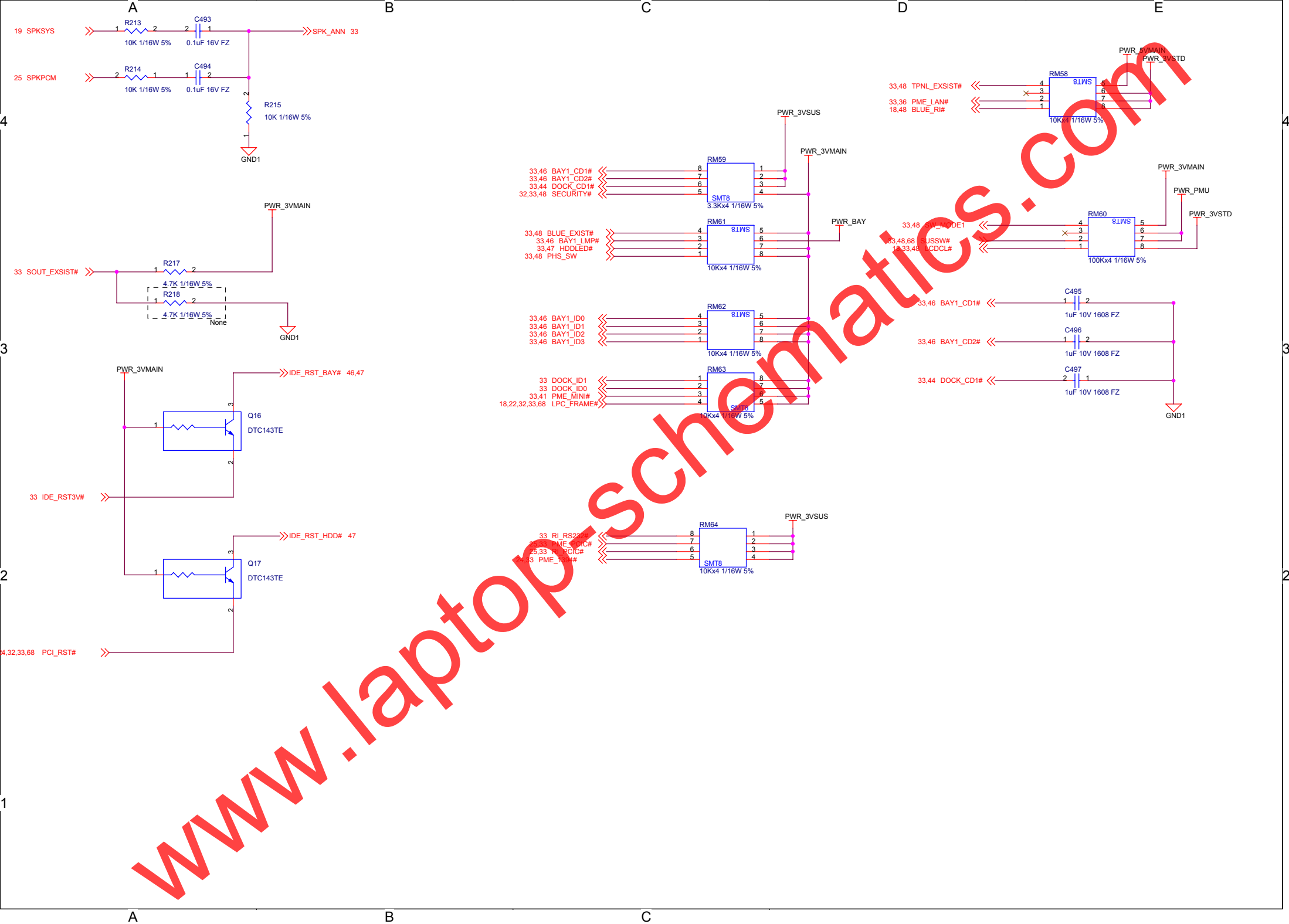




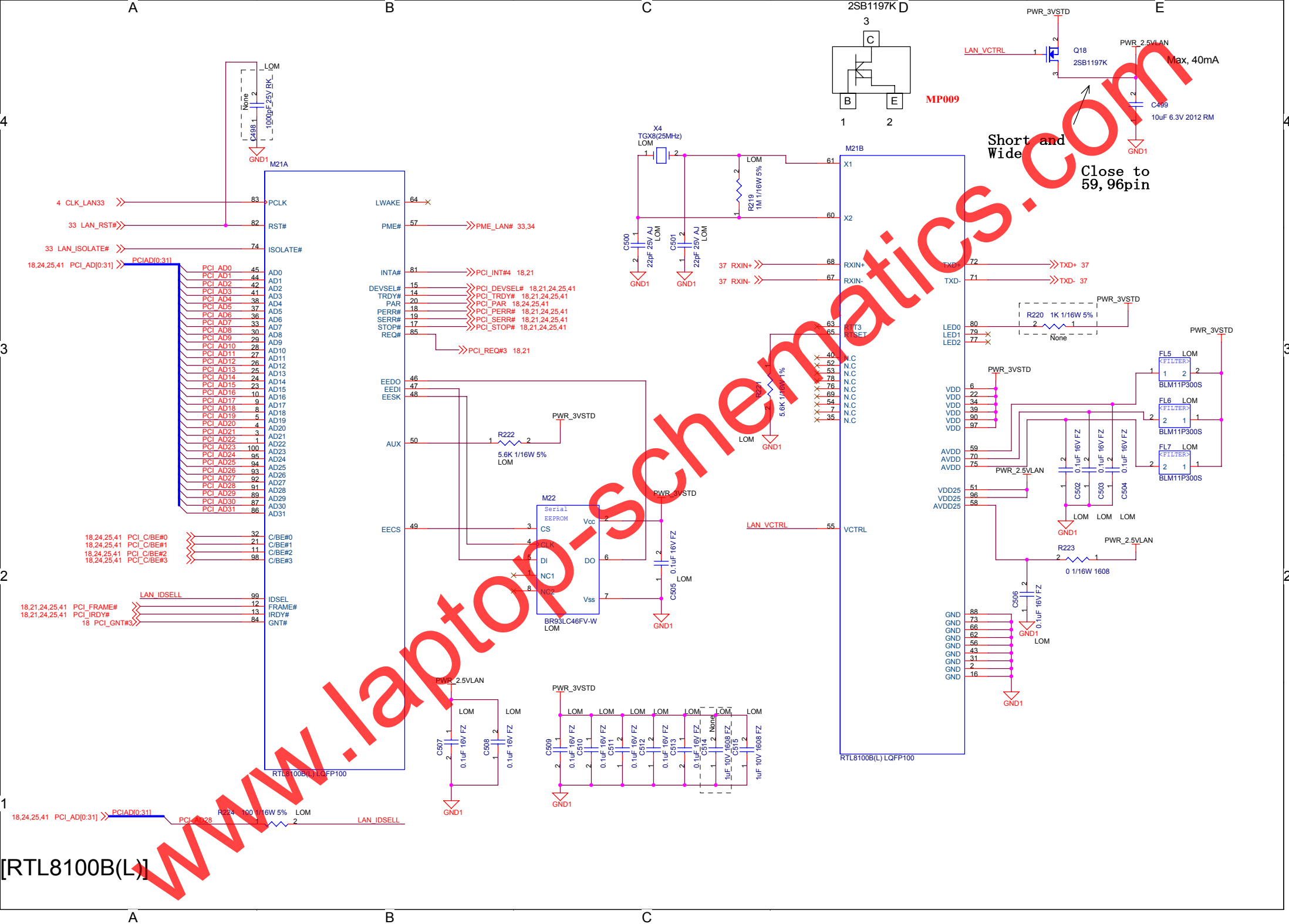


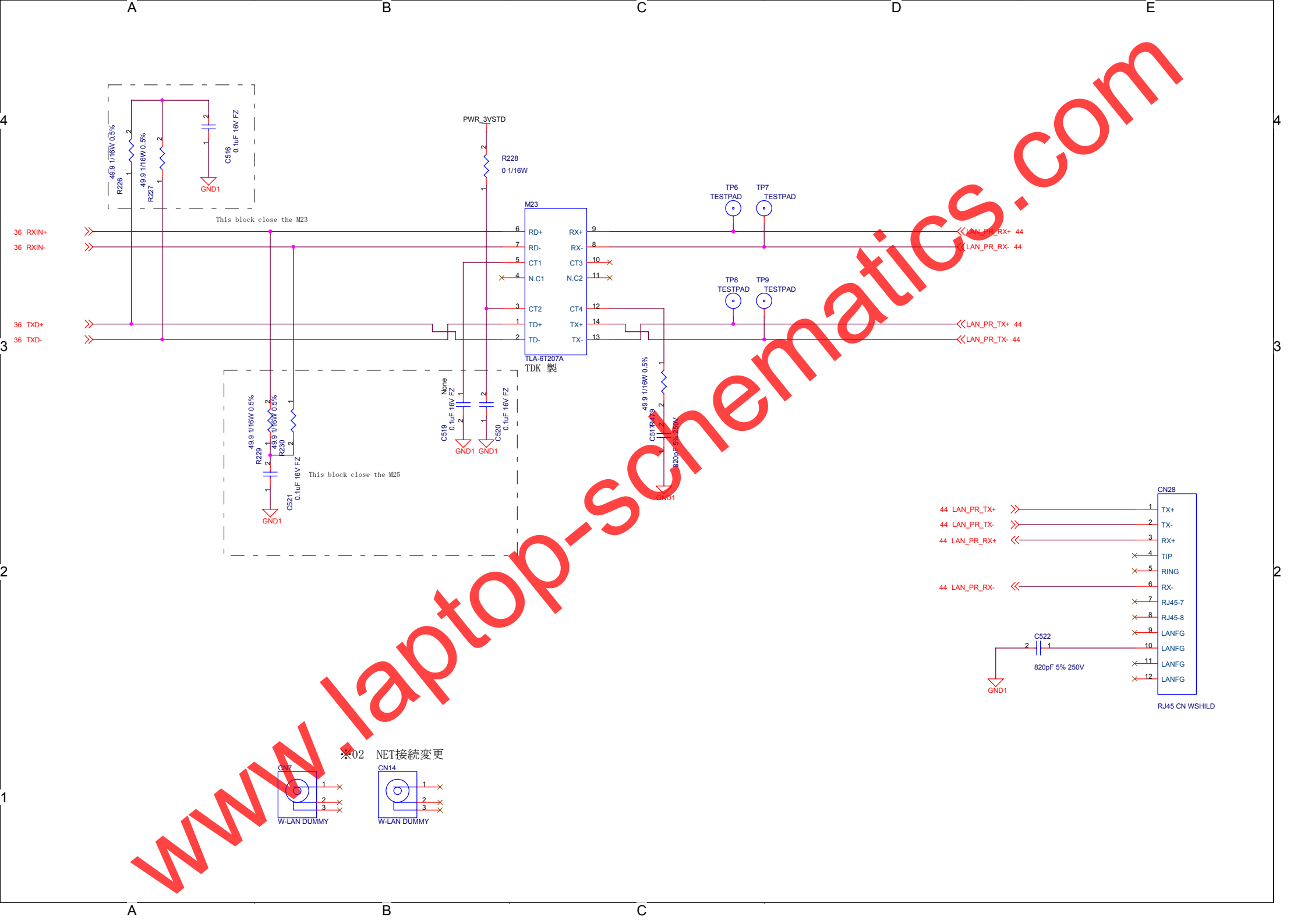




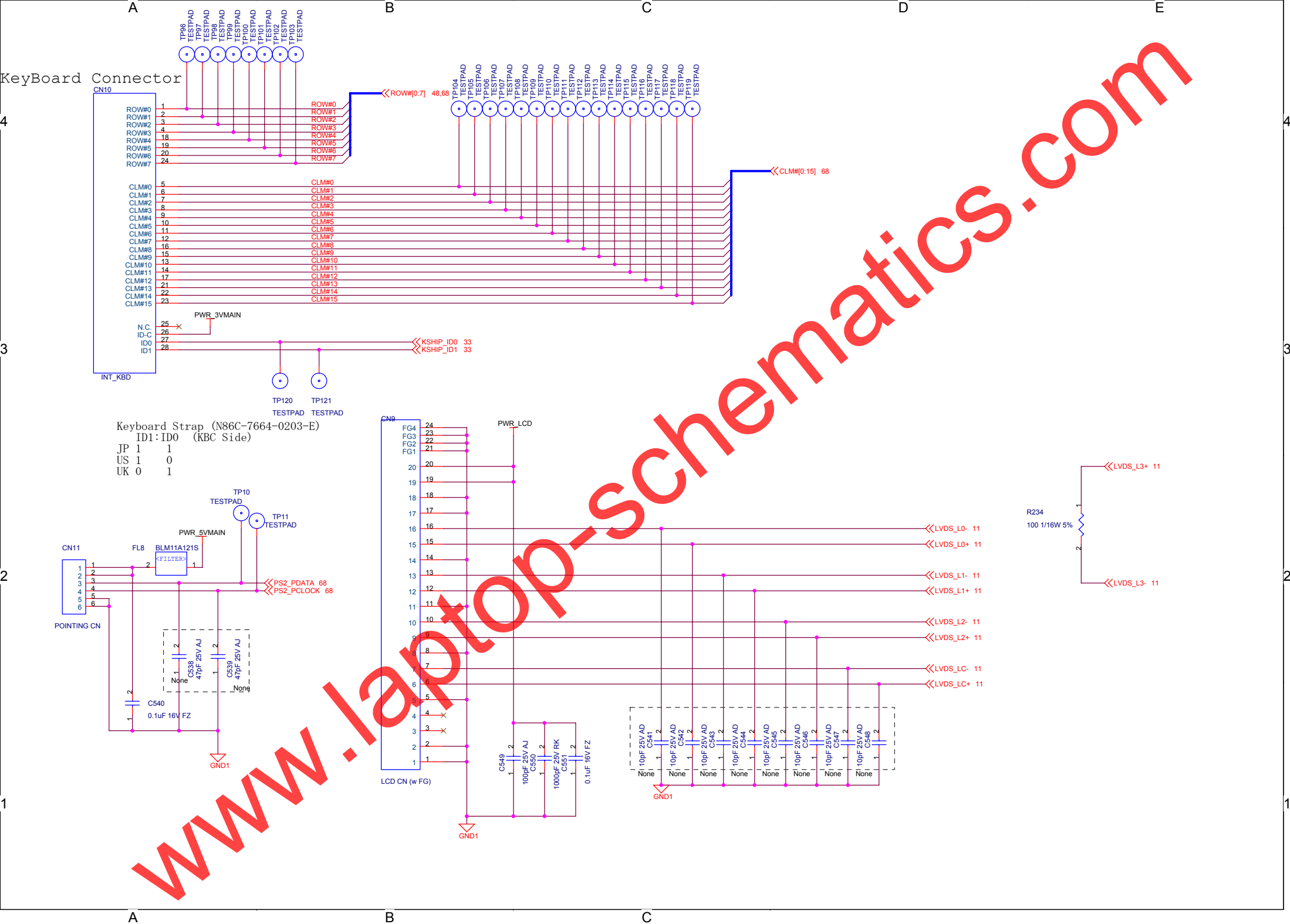




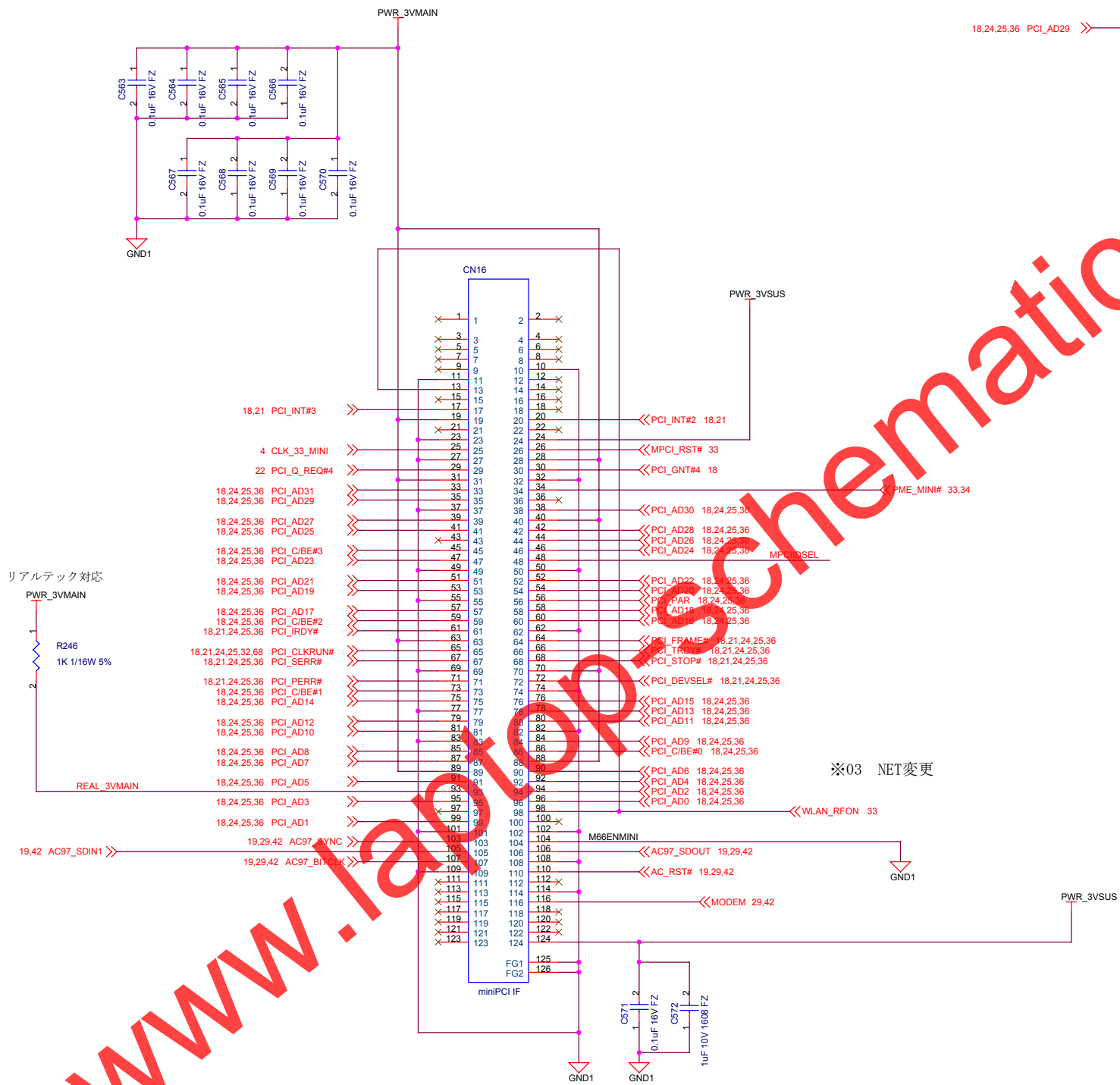


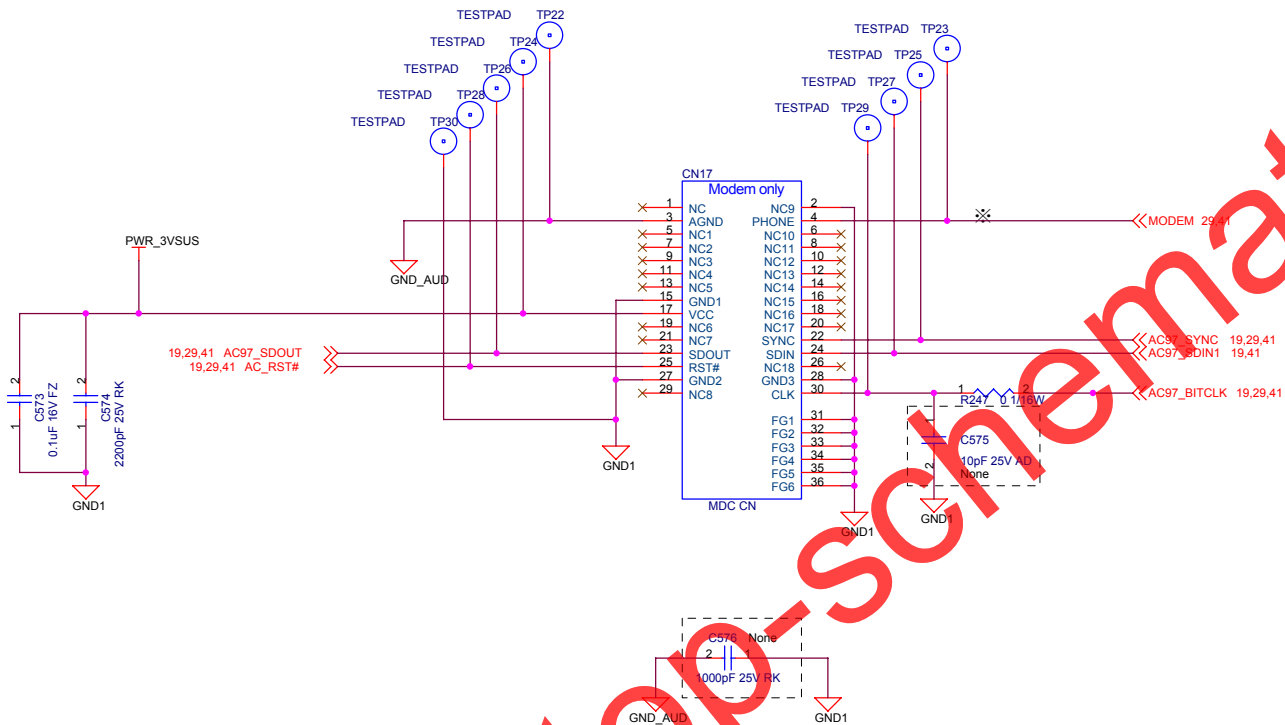










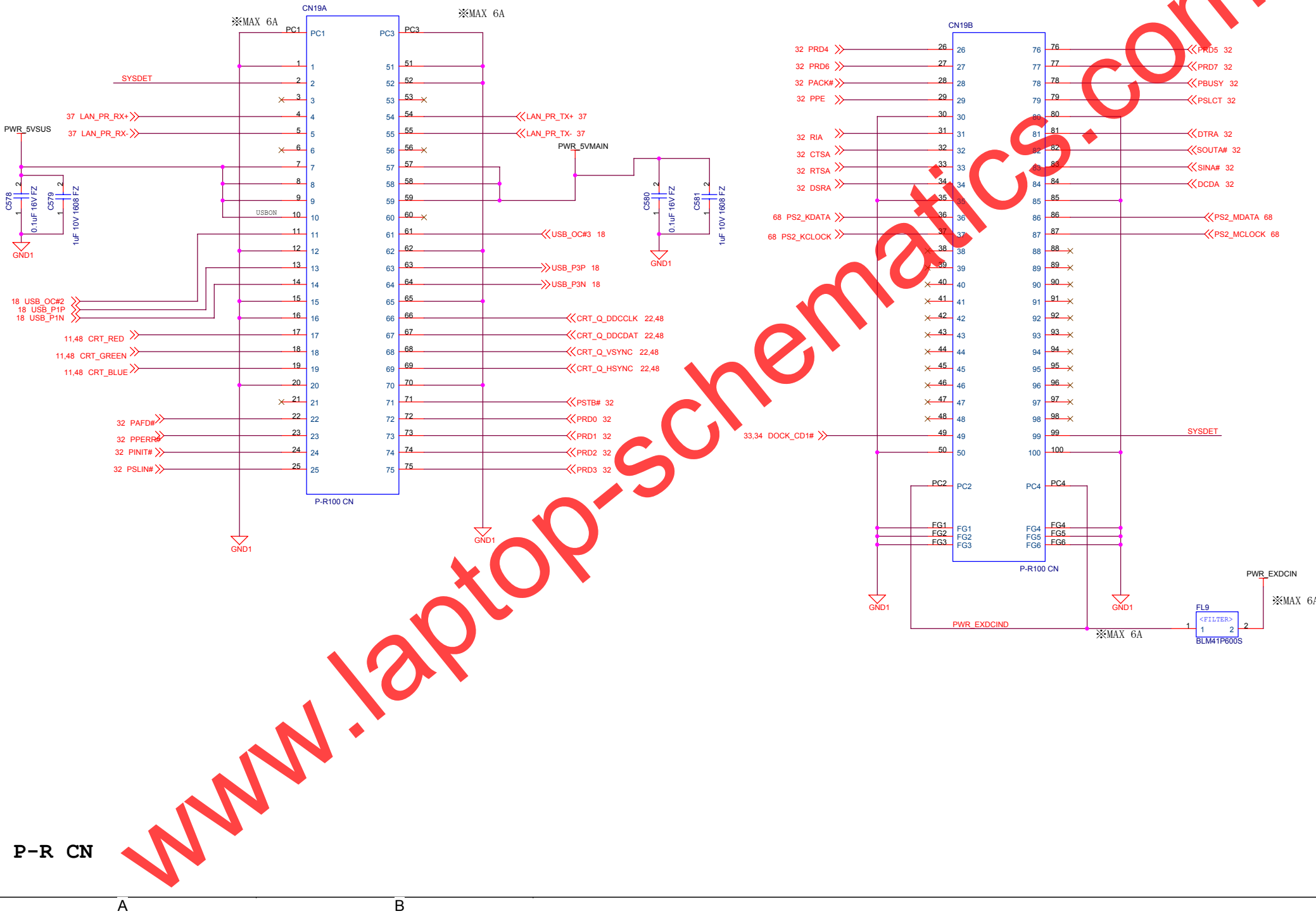


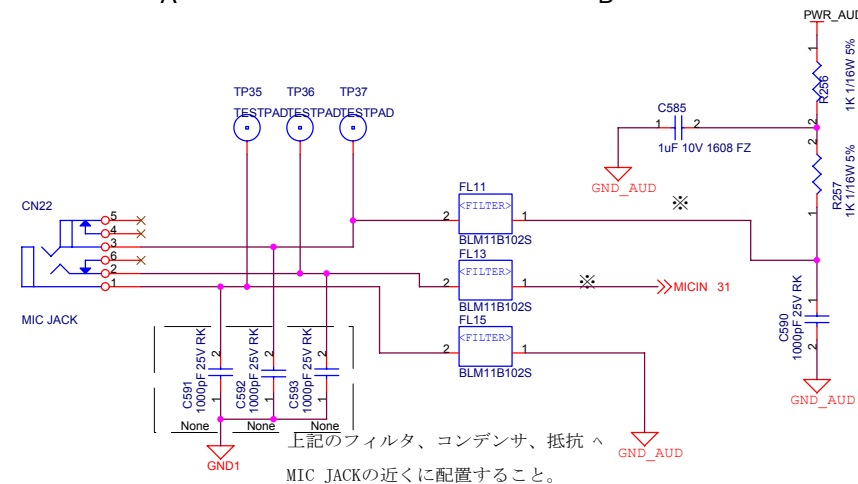
SD I/F	
JAB14	SD_DATA1
JAB19	SD_DATA0
JAB16	SD_CLK/MS_CLK
JAB20	SD_CMD
JBSYB#	SD_DATA3
JAB21	SD_DATA2
JAB15	MC_WP#



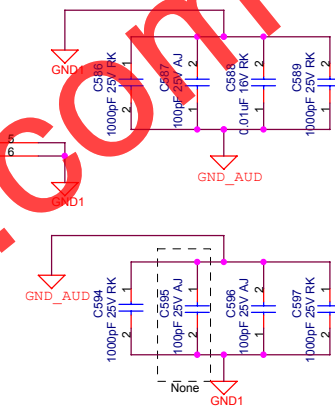
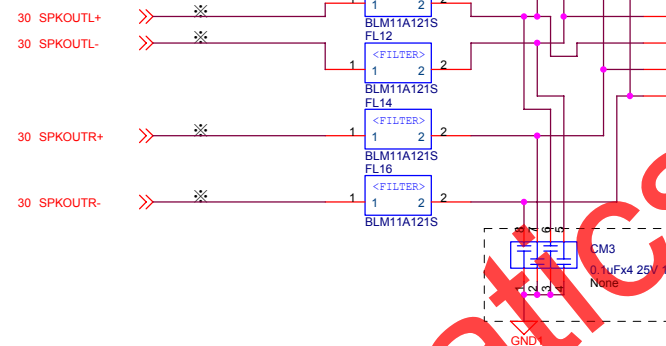
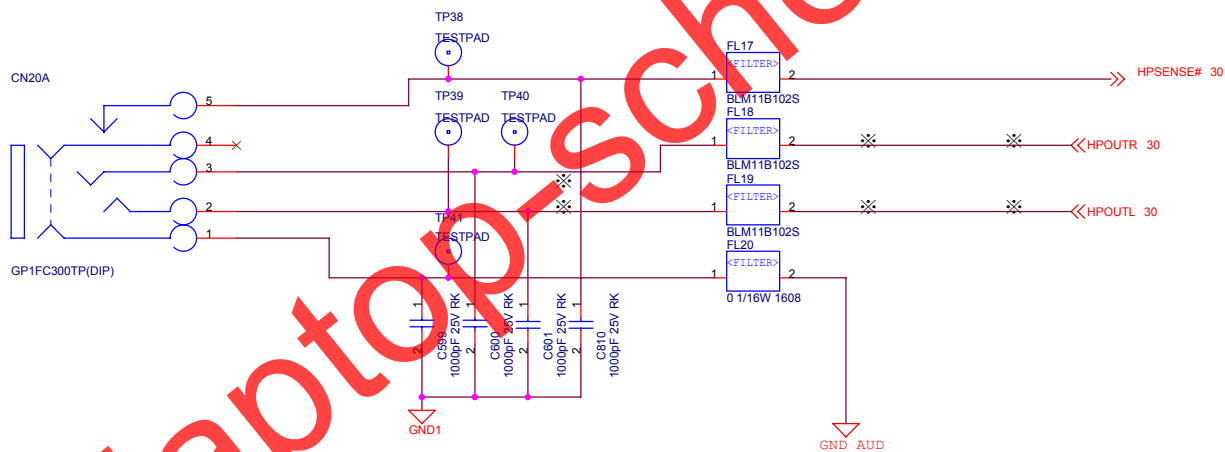
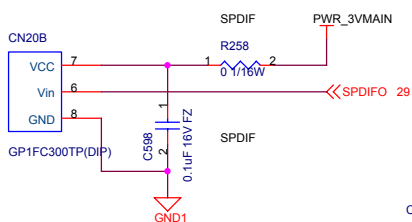


P-R CN

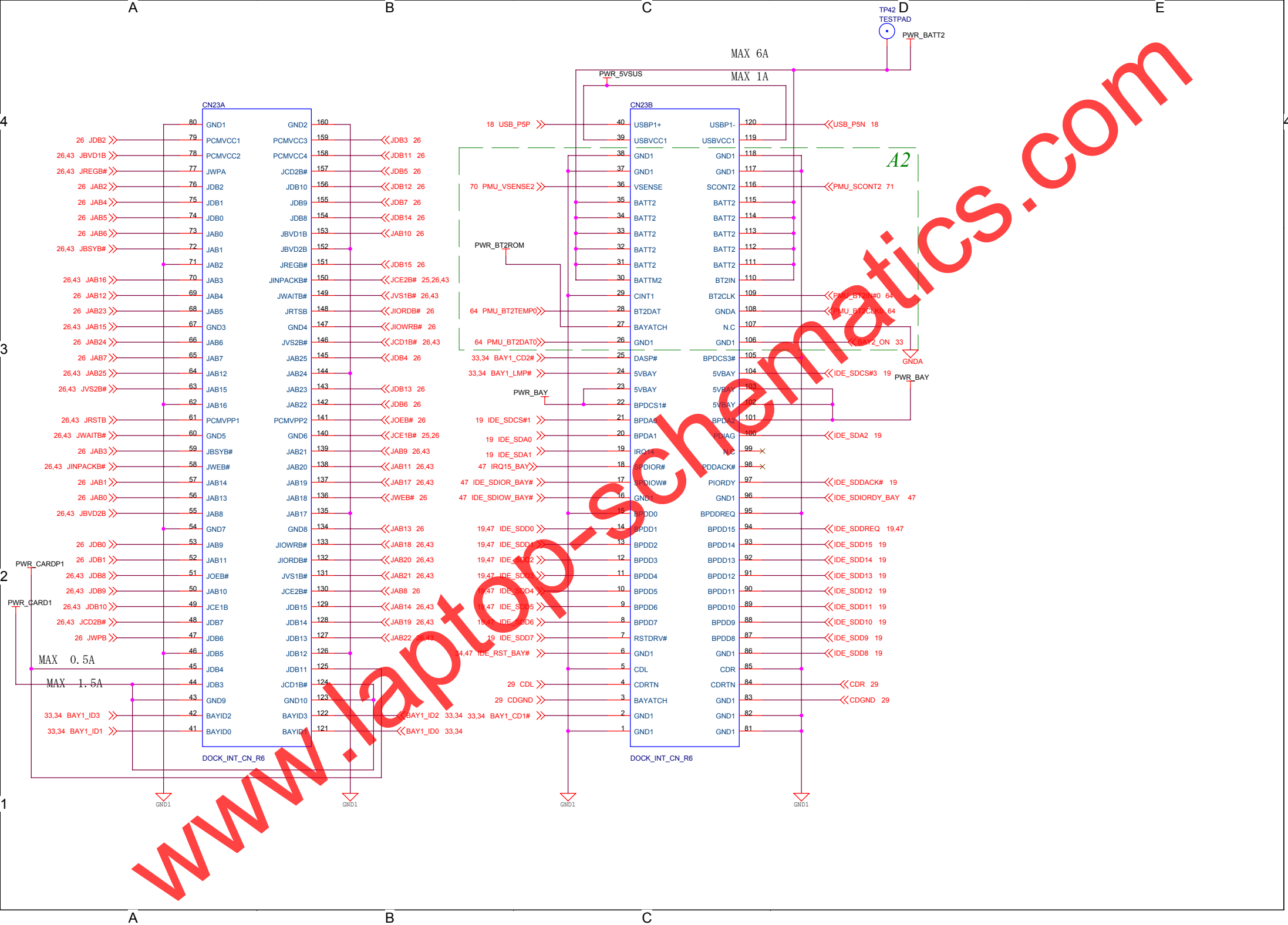


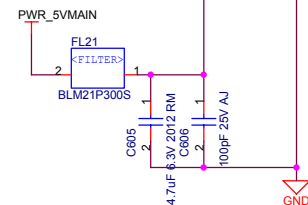
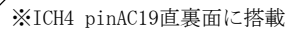


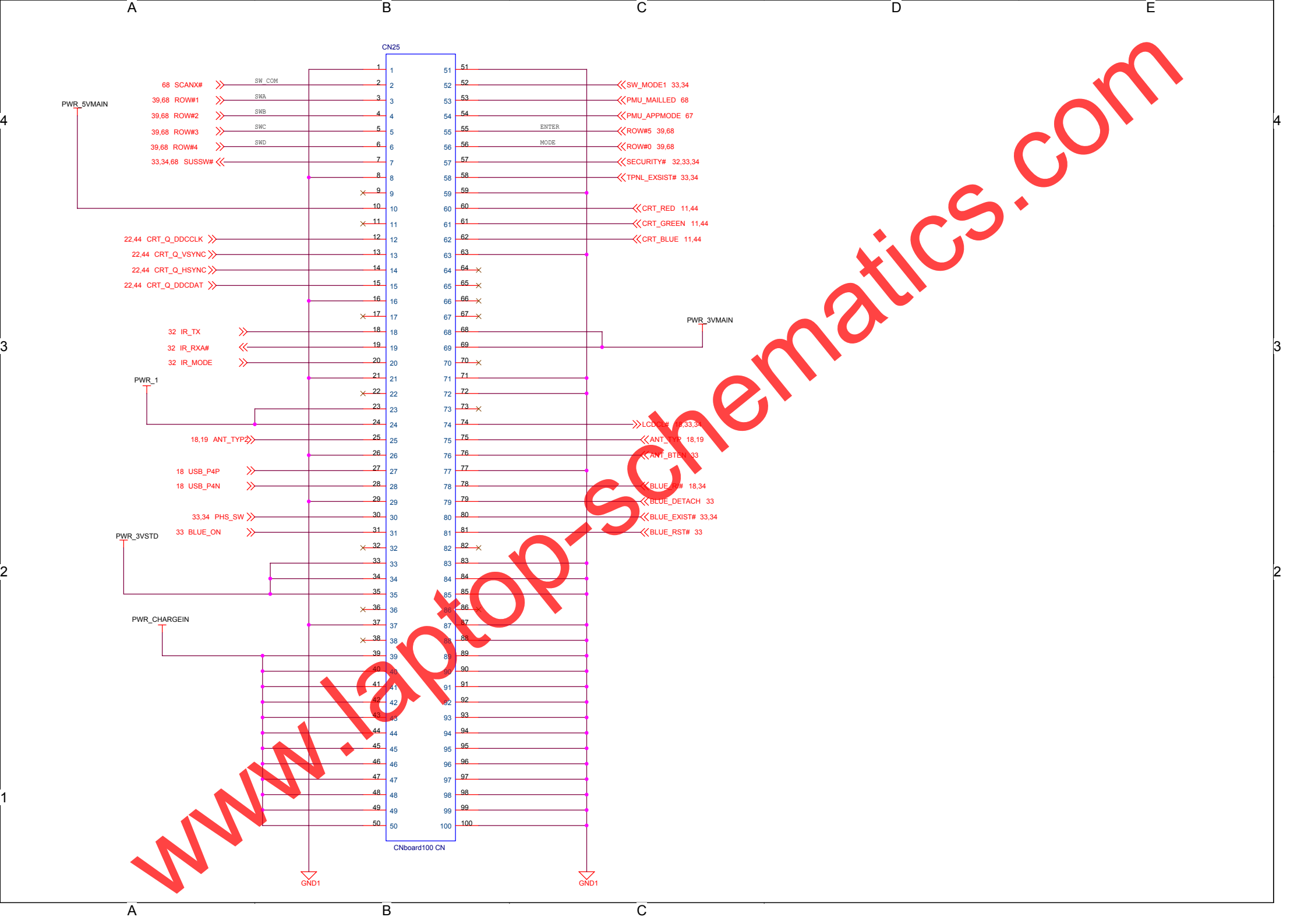
上記のフィルタ、コンデンサ、抵抗へ  
MIC JACKの近くに配置すること。

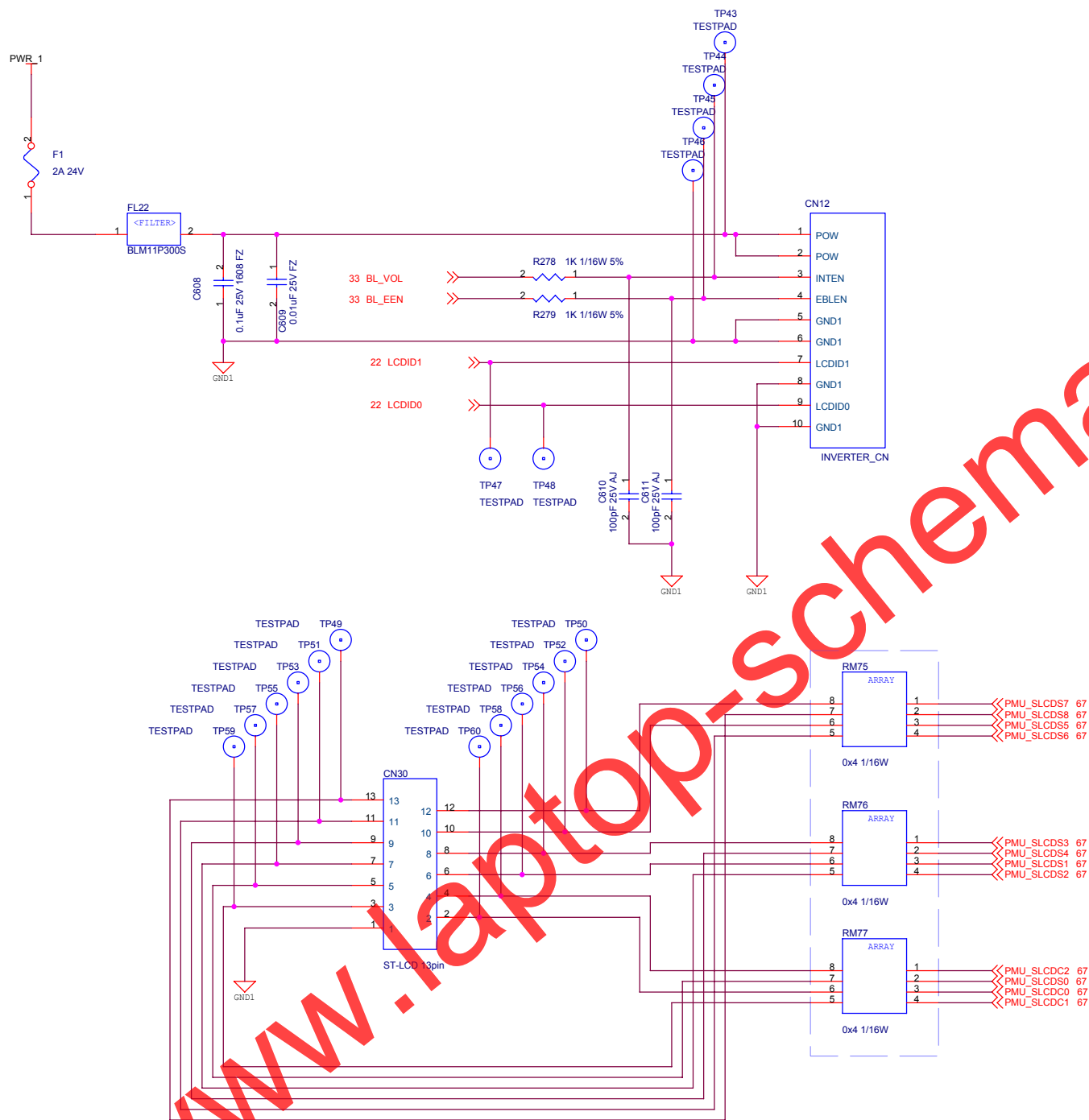


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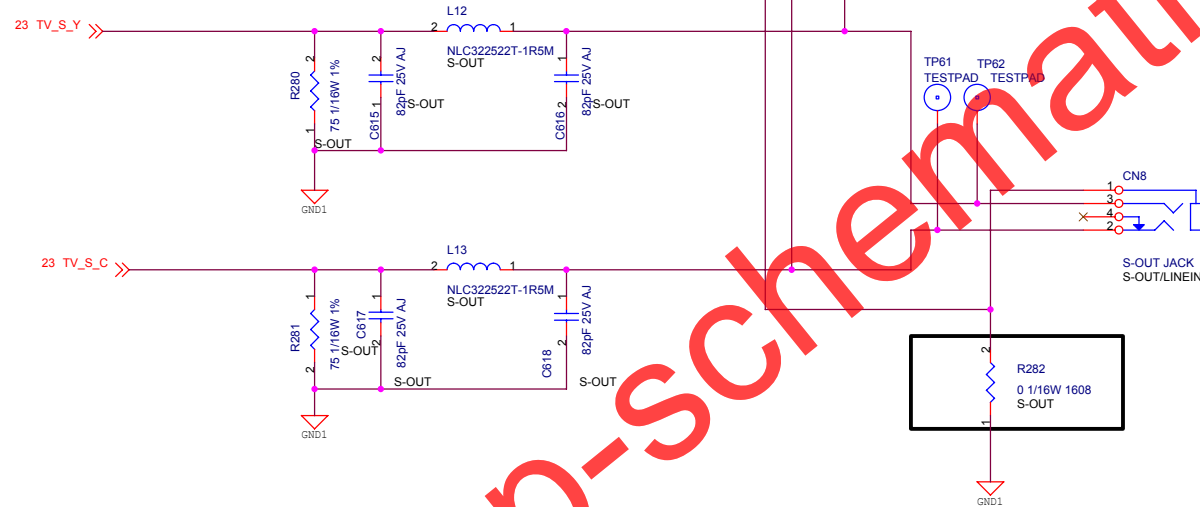
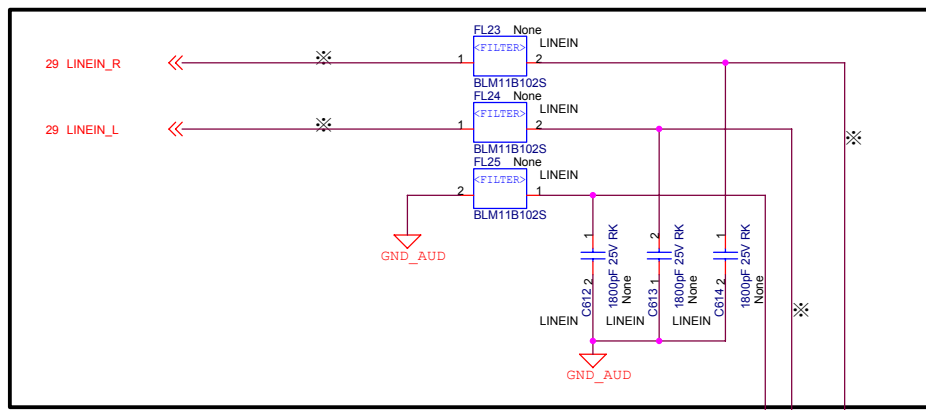




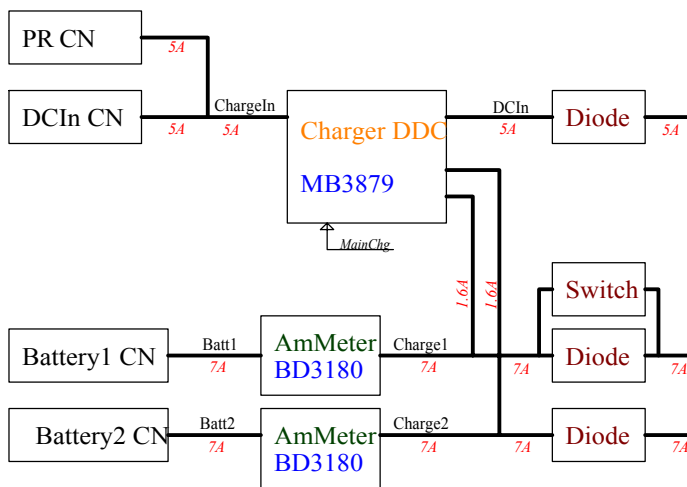
A

B

C



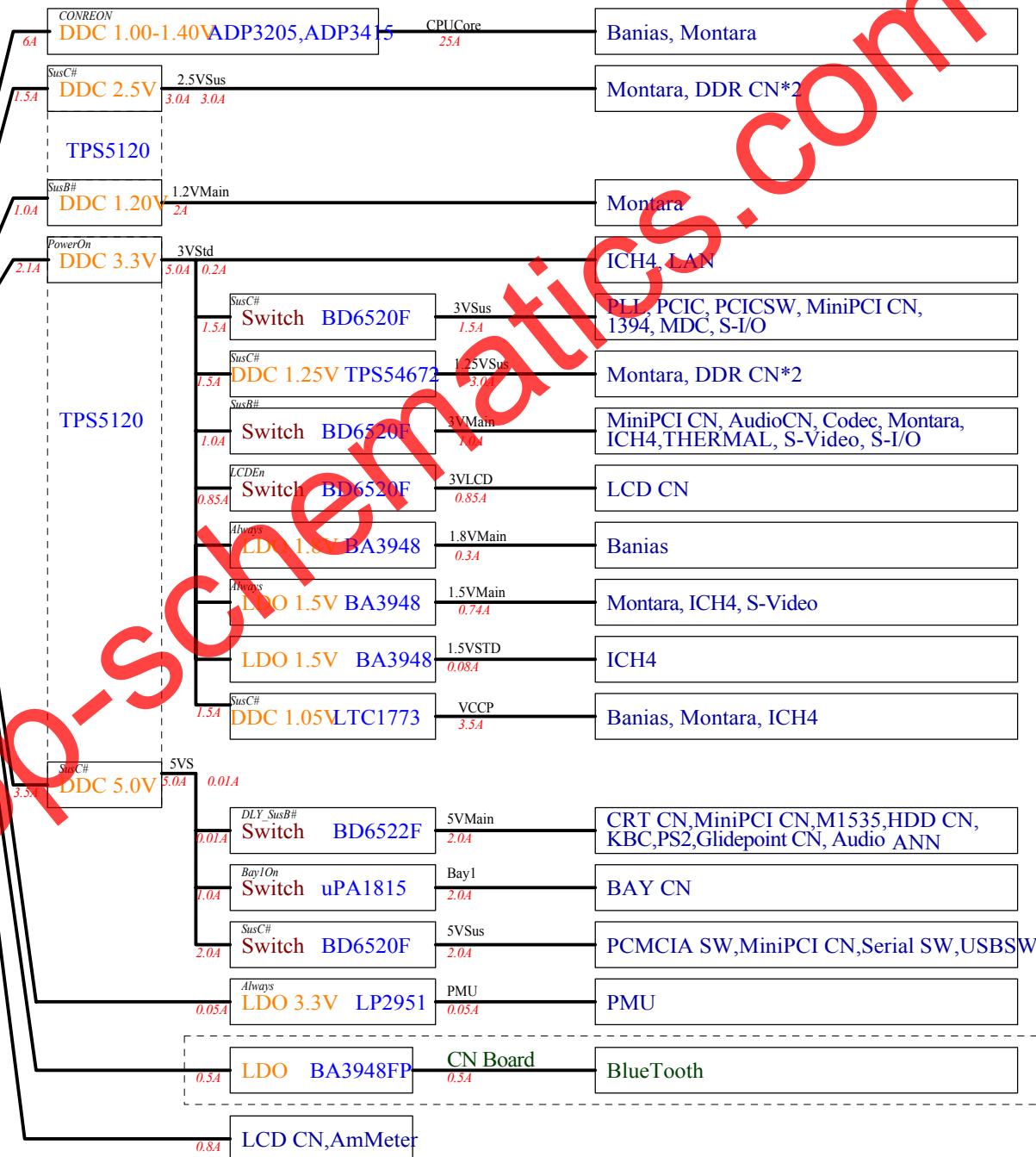
# Power Tree



## Power Contents

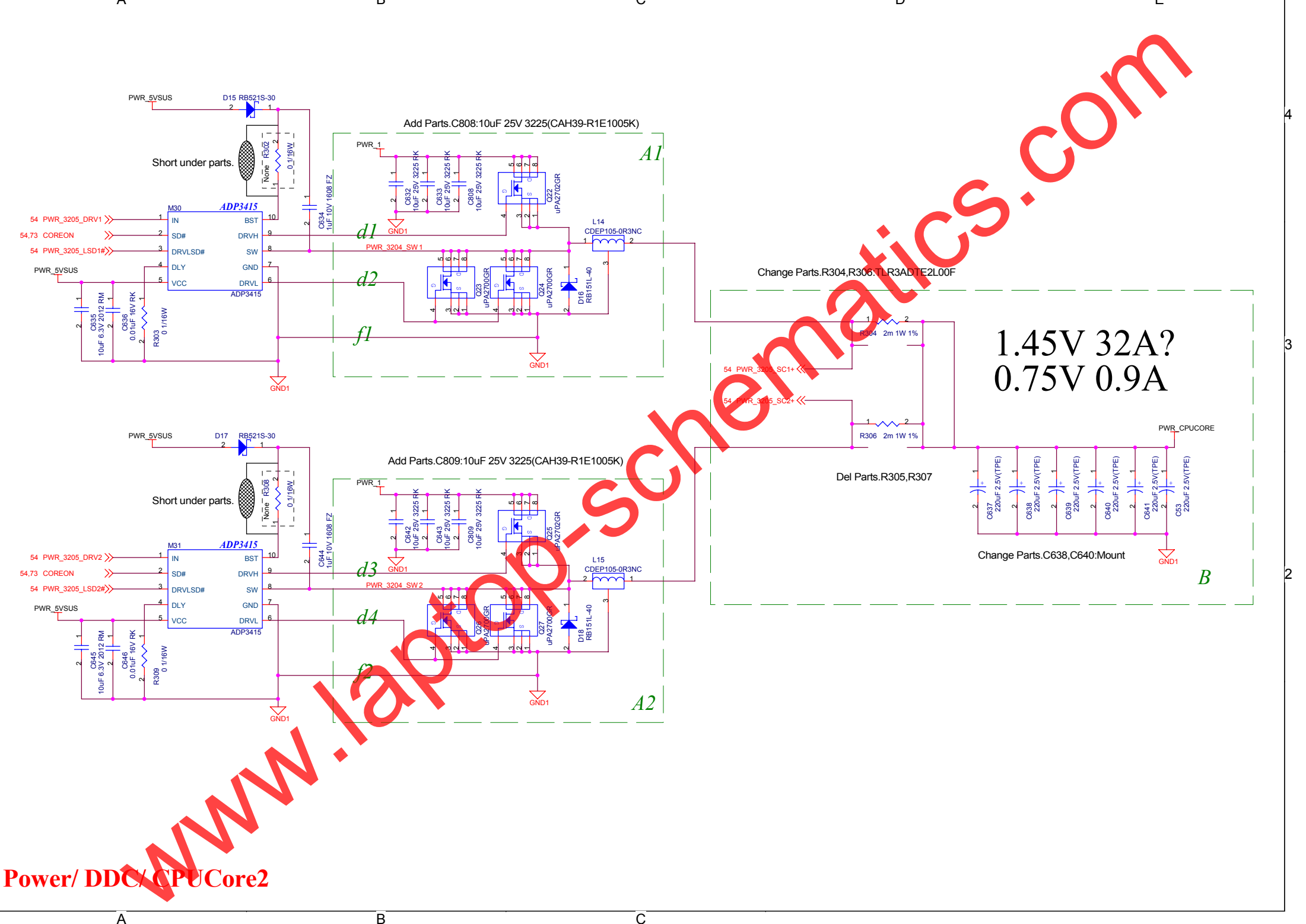
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LDO_PMU	62
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Battery	64
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PowerManagement	
LUNA1	67
LUNA2	68
AmMeter	69
VolMeter	70
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Etc	72
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DDC ; DC to DC Convector







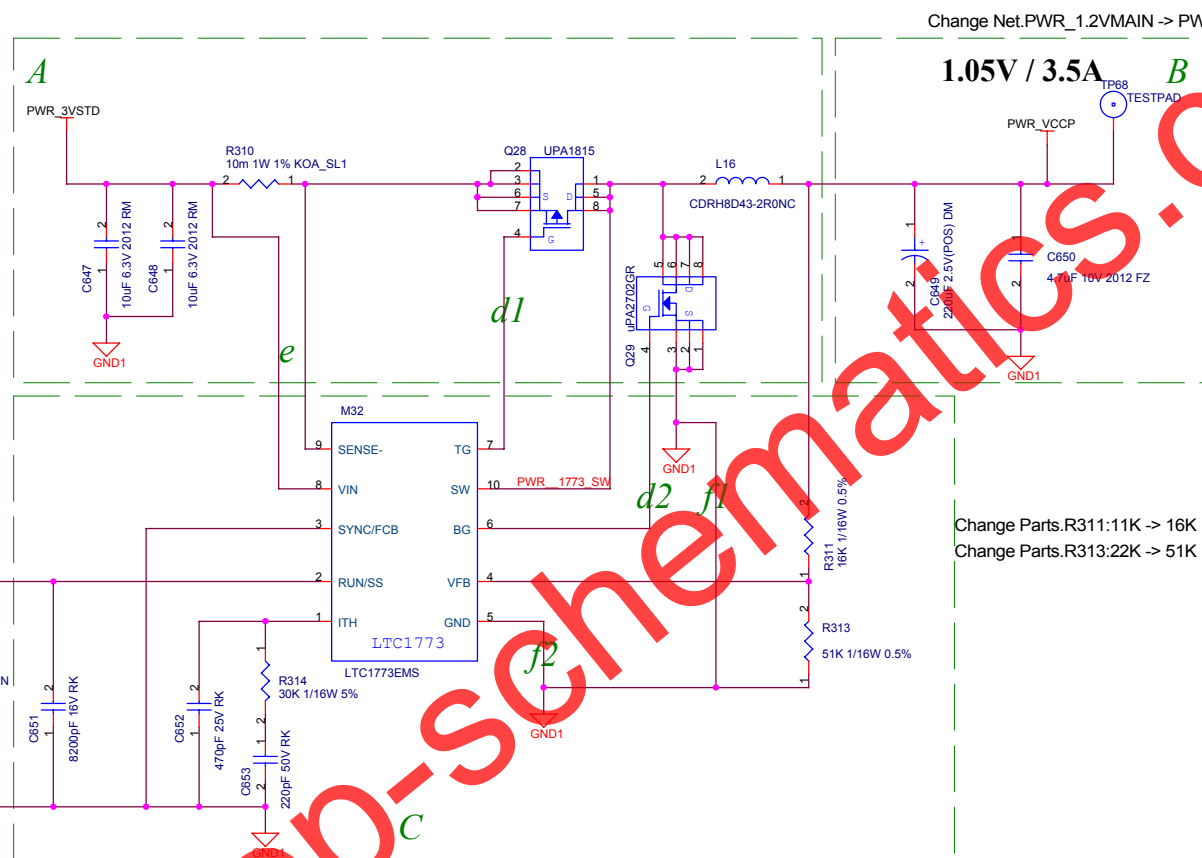


Change Net.SUSB# -> DLY\_SUSB#

Change Net.PWR\_1.2VMAIN -> PWR\_VCCP

1.05V / 3.5A

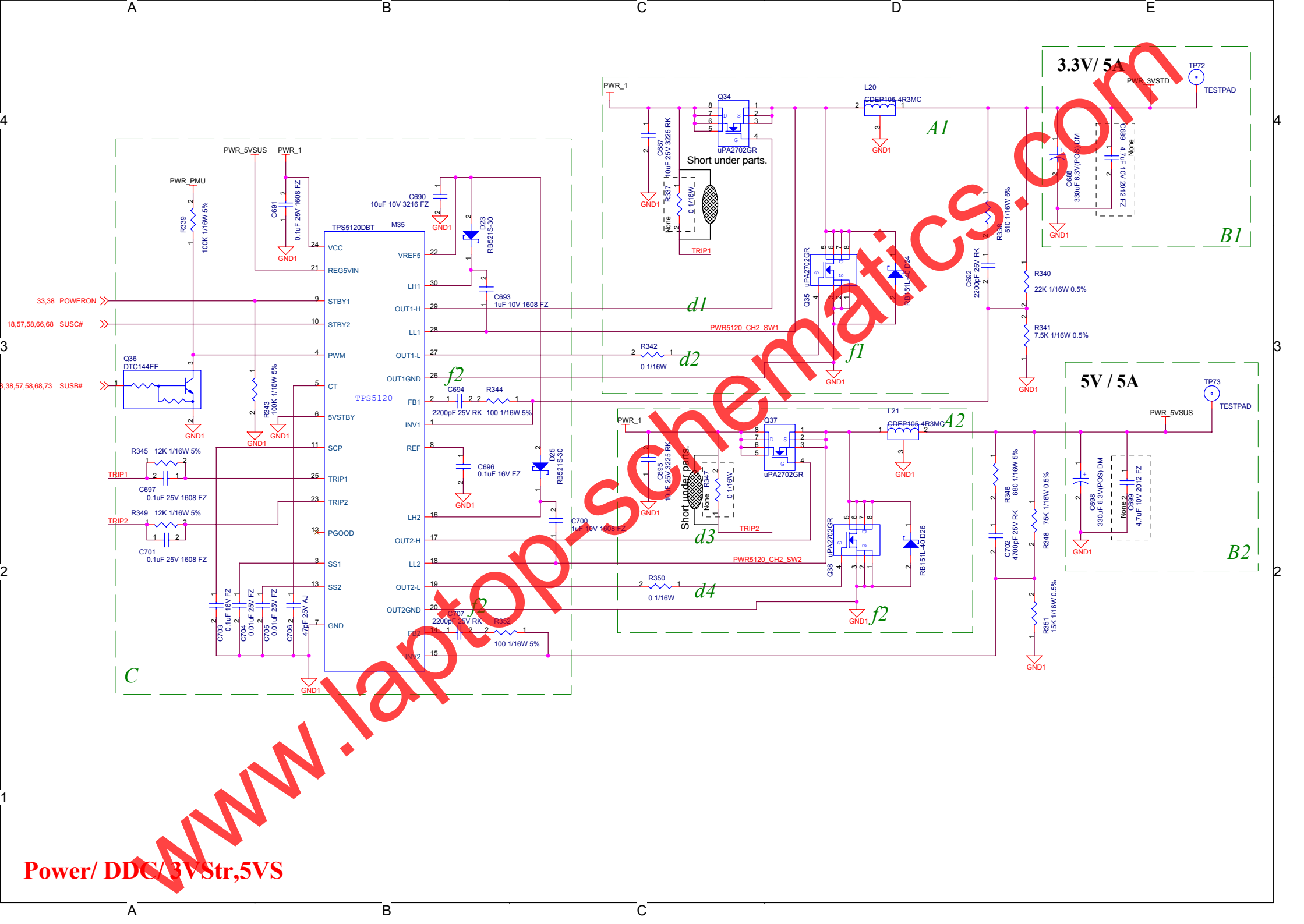
66,73 DLY\_SUSB#



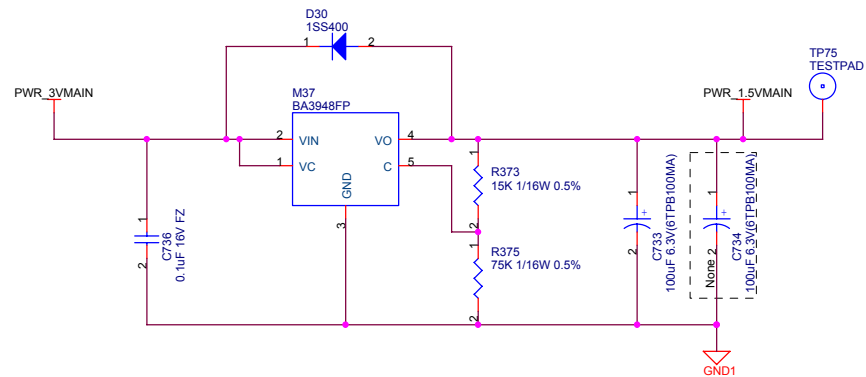
Change Parts.R311:11K -> 16K 1/16W 0.5%(CAA34-K1J1602D)  
Change Parts.R313:22K -> 51K 1/16W 0.5%(CAA34-K1J5102D)



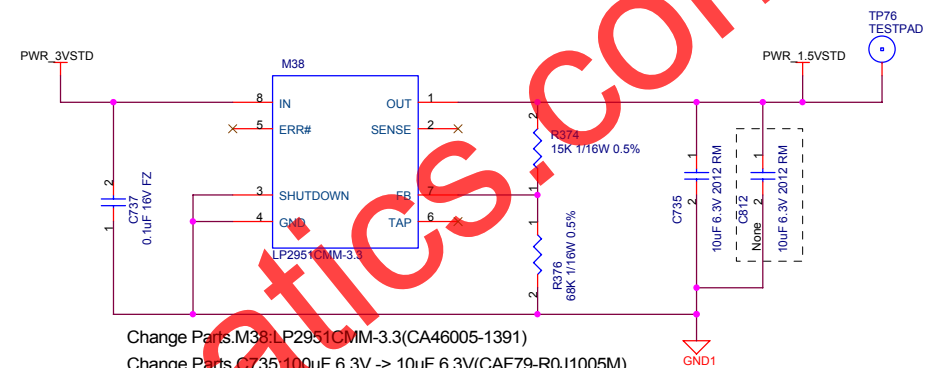






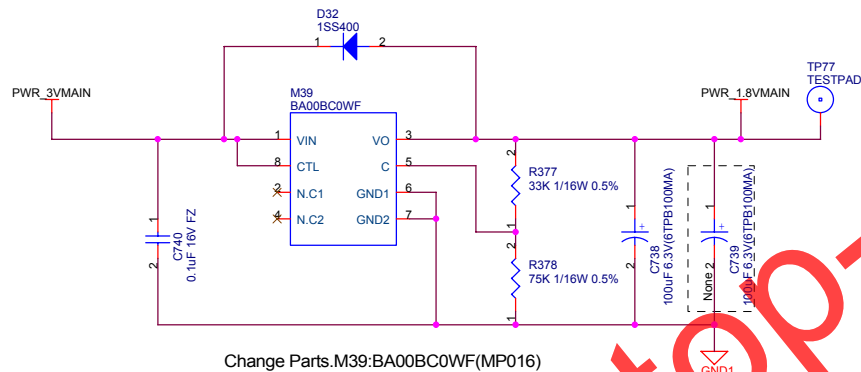


1.5V 0.74A



1.5V 0.08A

Change Parts.M38:LP2951CMM-3.3(CA46005-1391)  
Change Parts.C735:100uF 6.3V -> 10uF 6.3V(CAF79-R0J1005M)  
Add Parts.C812:10uF 6.3V(CAF79-R0J1005M)Reserve  
Change Parts.R376:75K -> 68K 1/16W 0.5%(CAA34-K1J6802D)

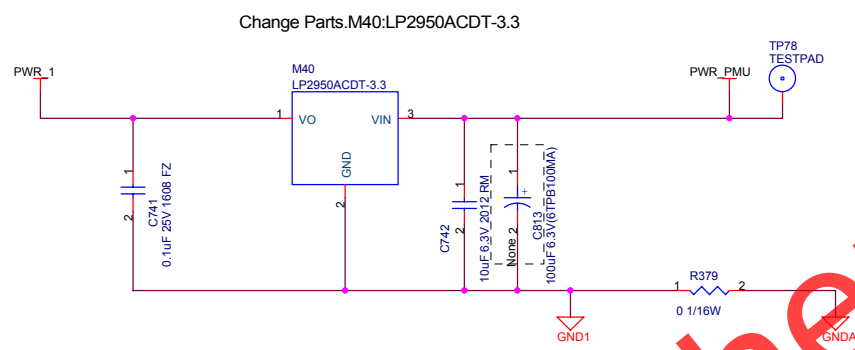


1.8V 0.3A

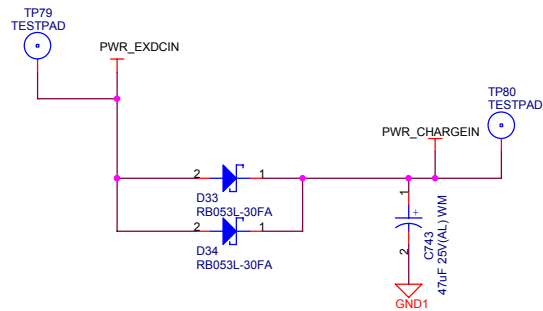
Change Parts.M39:BA00BC0WF(MP016)



Power/ LDO/ PMU

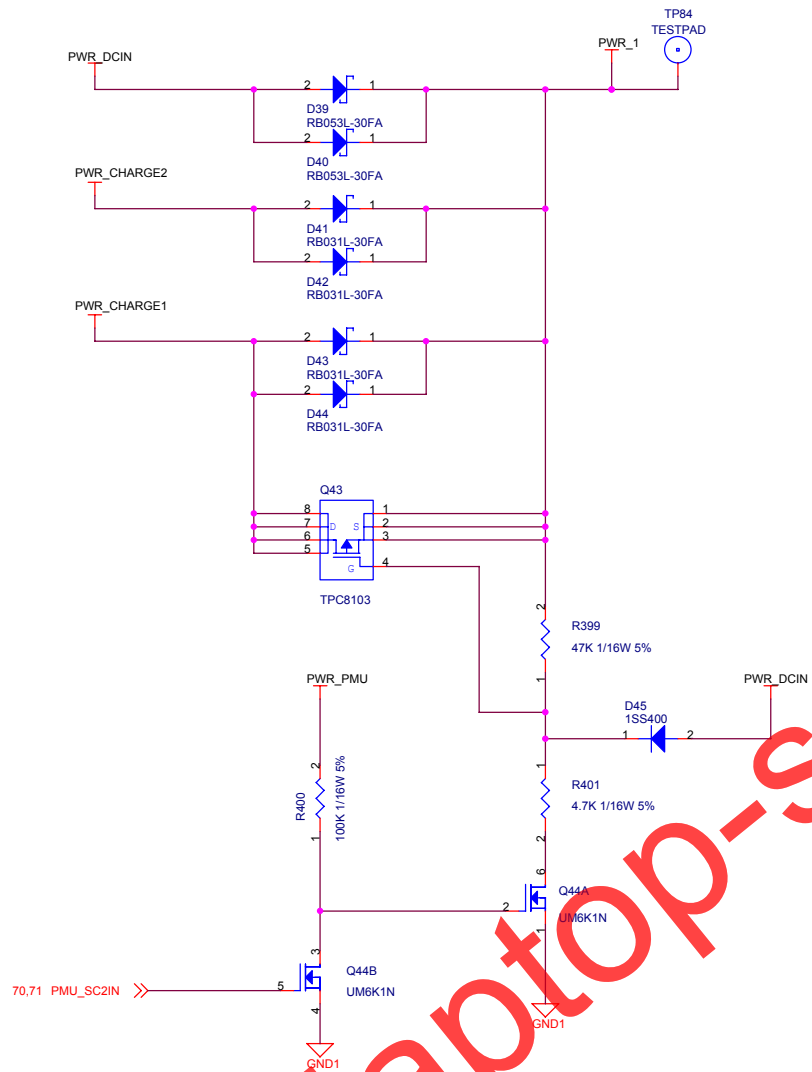


3.3V 50mA

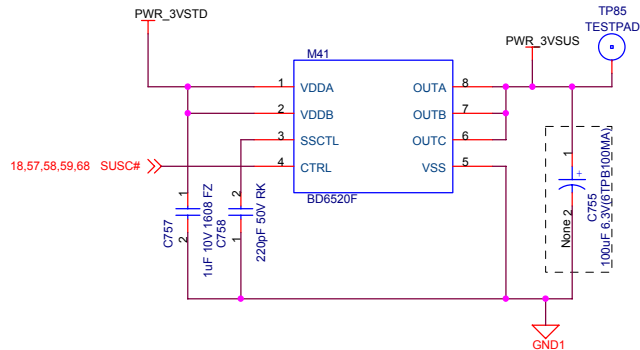


Power/ Node/ DCIn

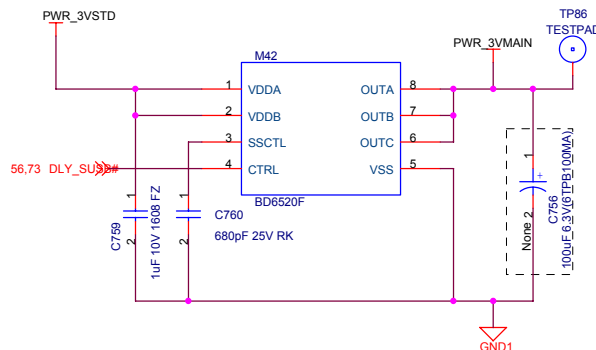




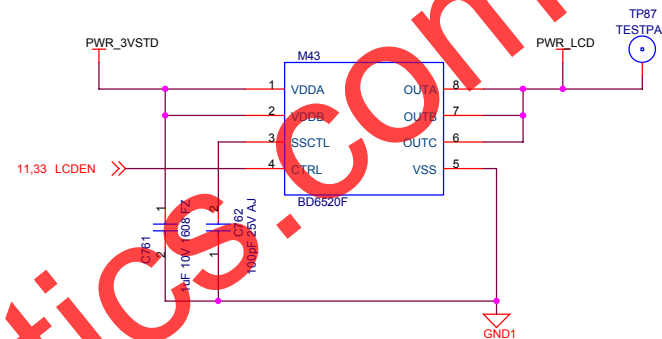
3.3V 2.0A



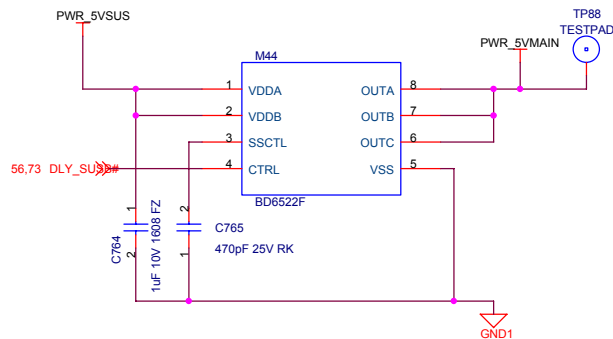
3.3V 2.0A



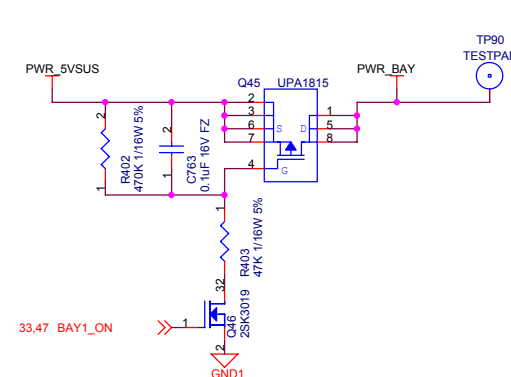
3.3V 1.0A



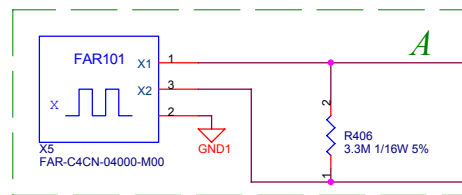
5.0V 2.0A



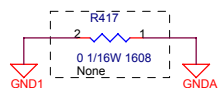
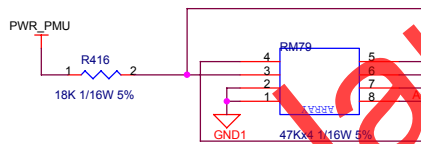
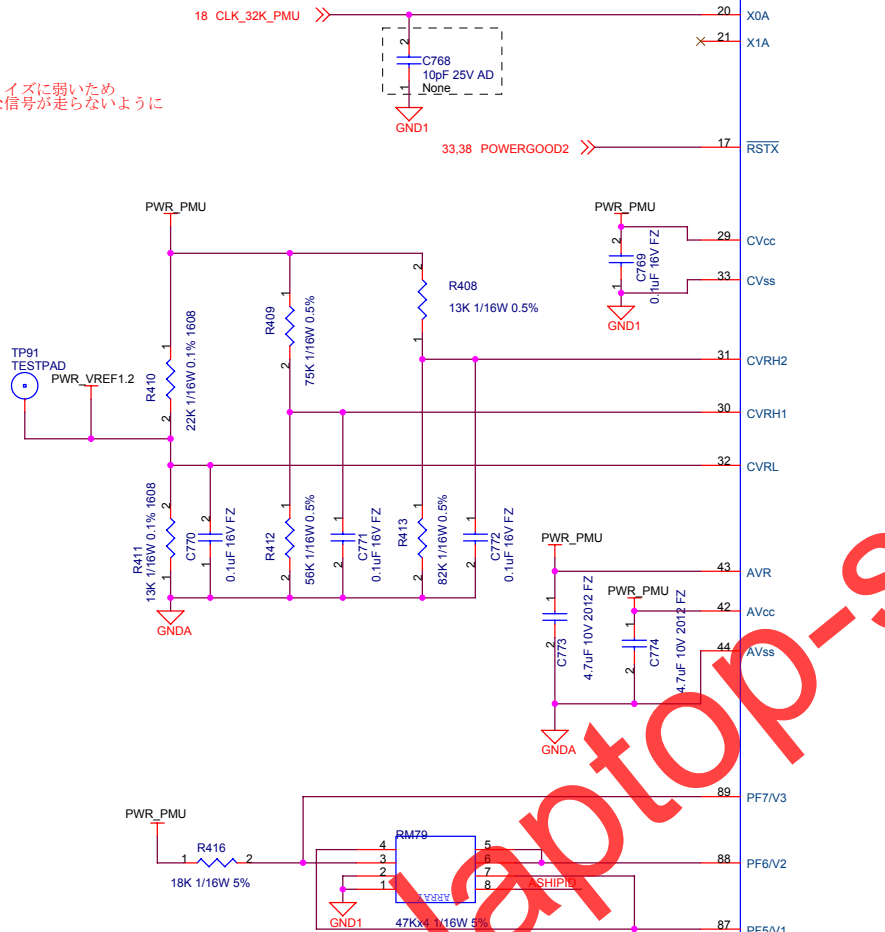
5.0V 2.0A



Power/ Node/ Swicth



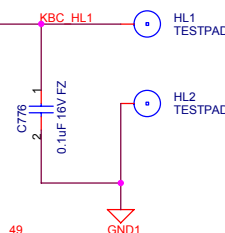
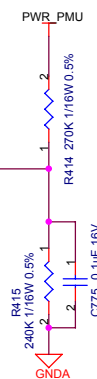
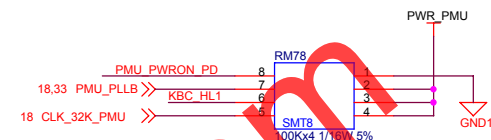
本クロックはノイズに弱い  
ため上下層に高速な信号が走らないように配線する。と



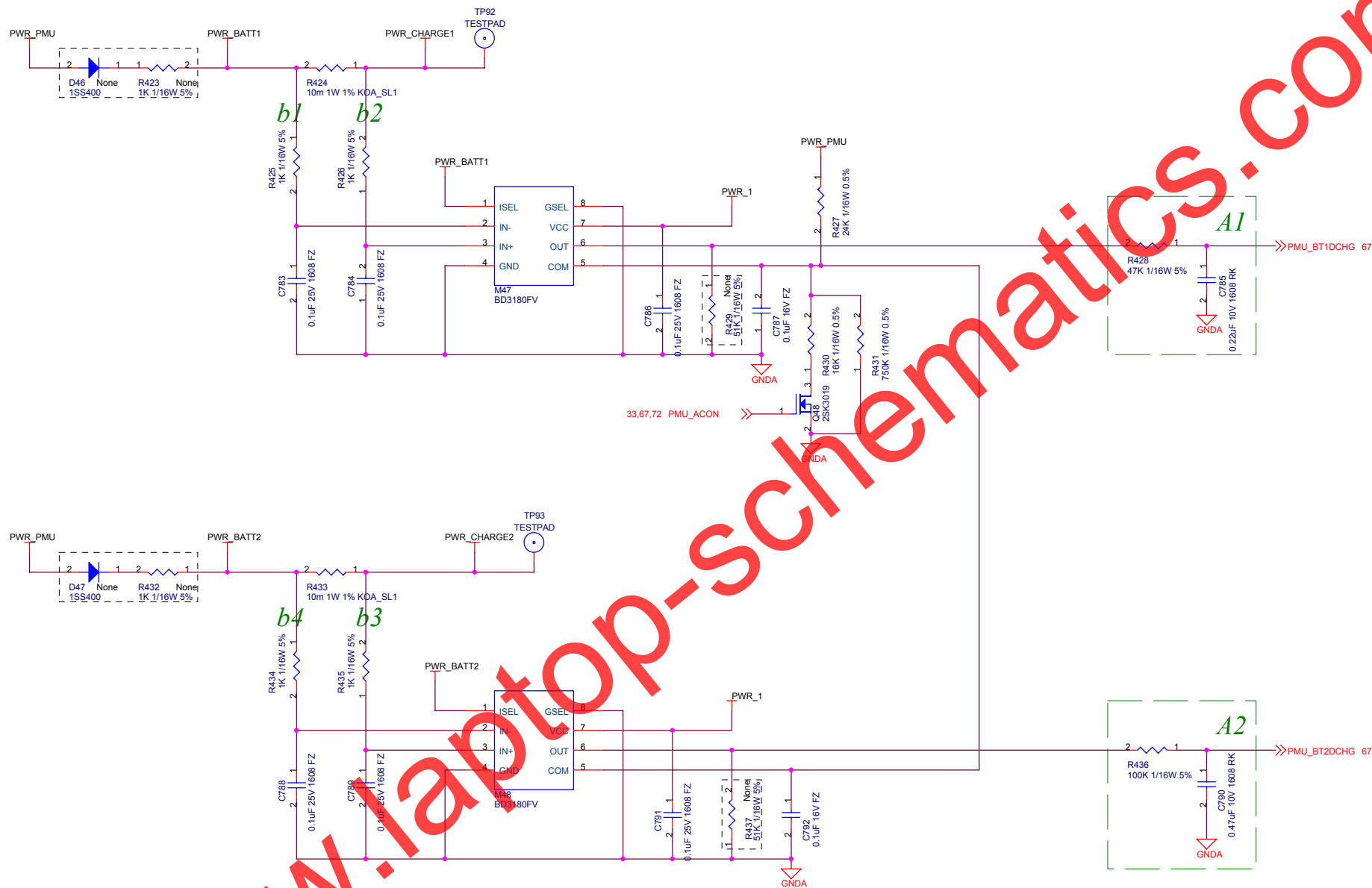
## Power/ PMU/ LUNA1



61pinはシリアル充電の時のみ使用するのか？ 要確認

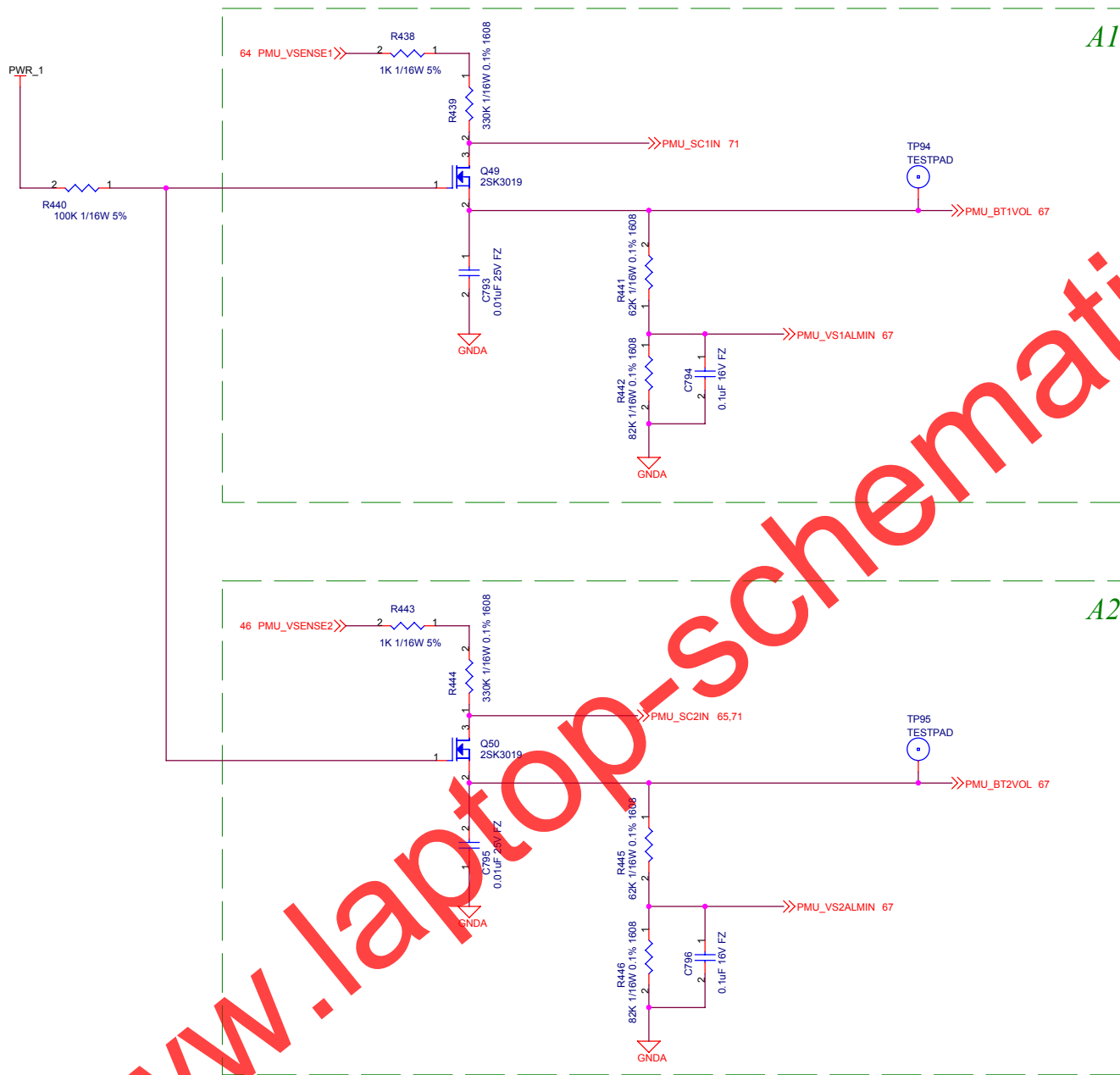


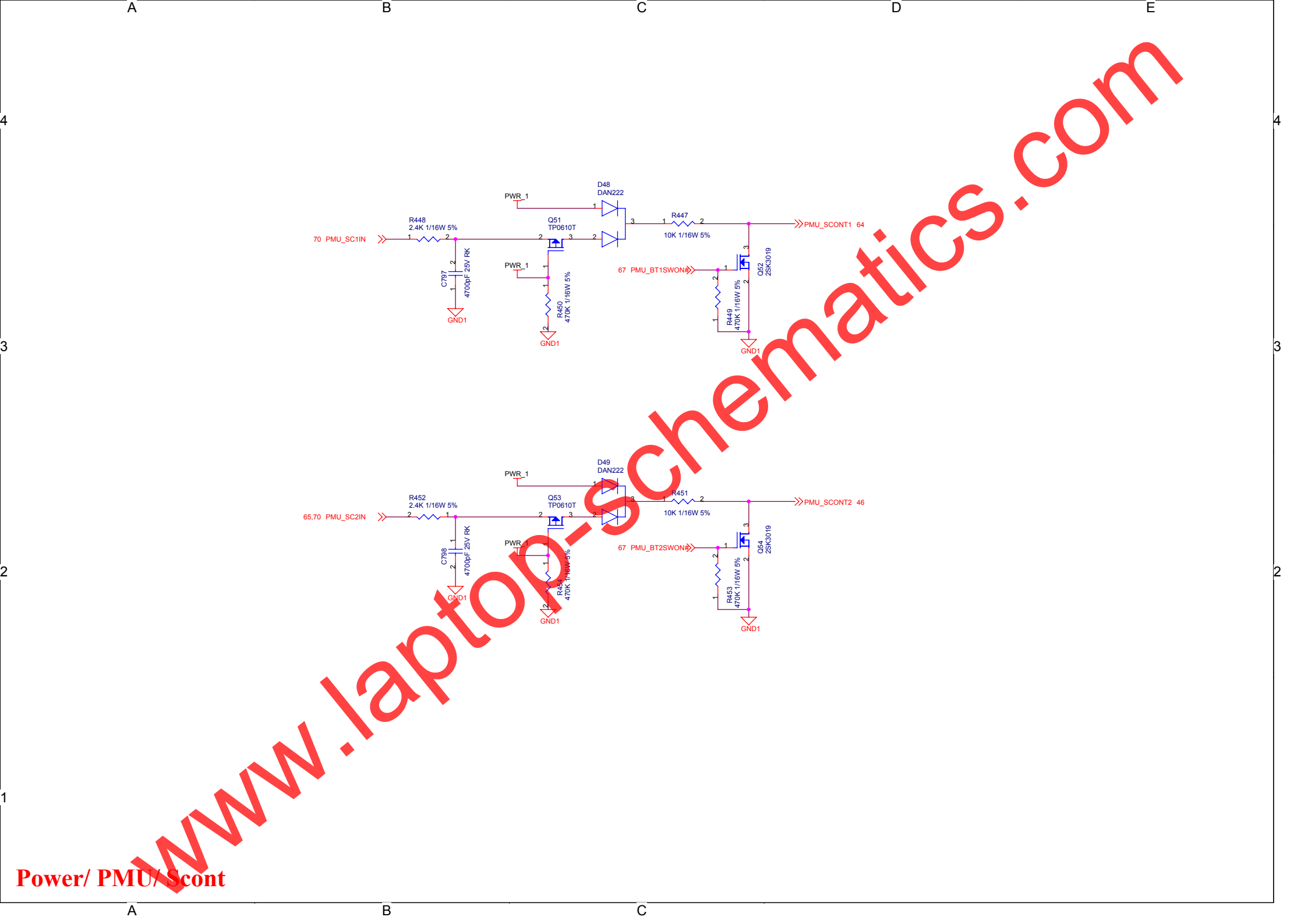




Power/ PMU/ AmMeter







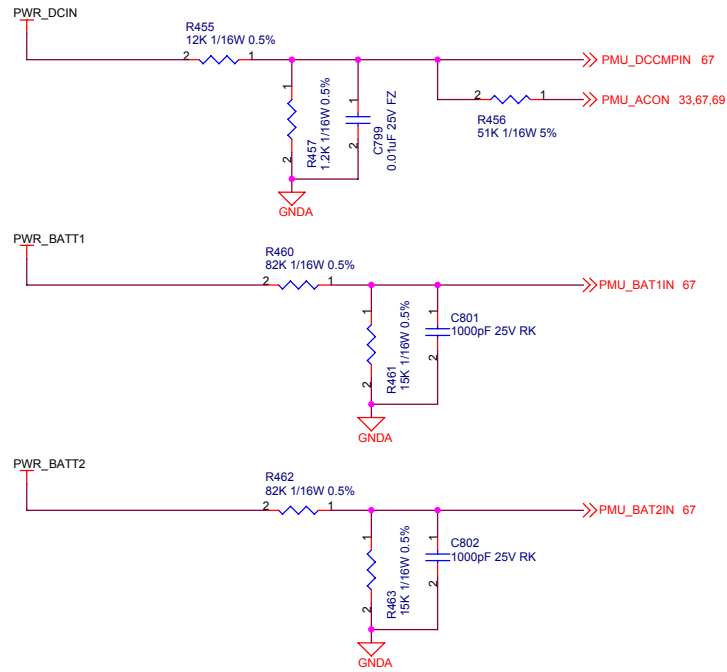
A

B

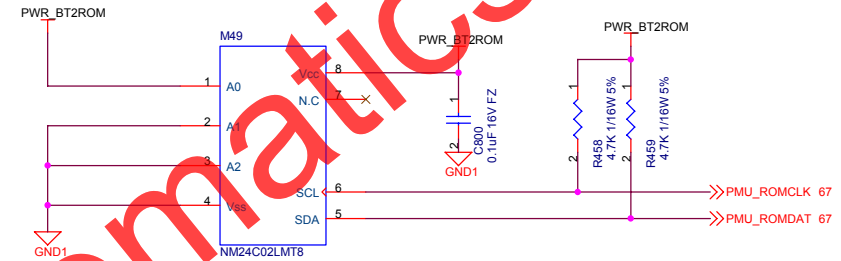
C

D

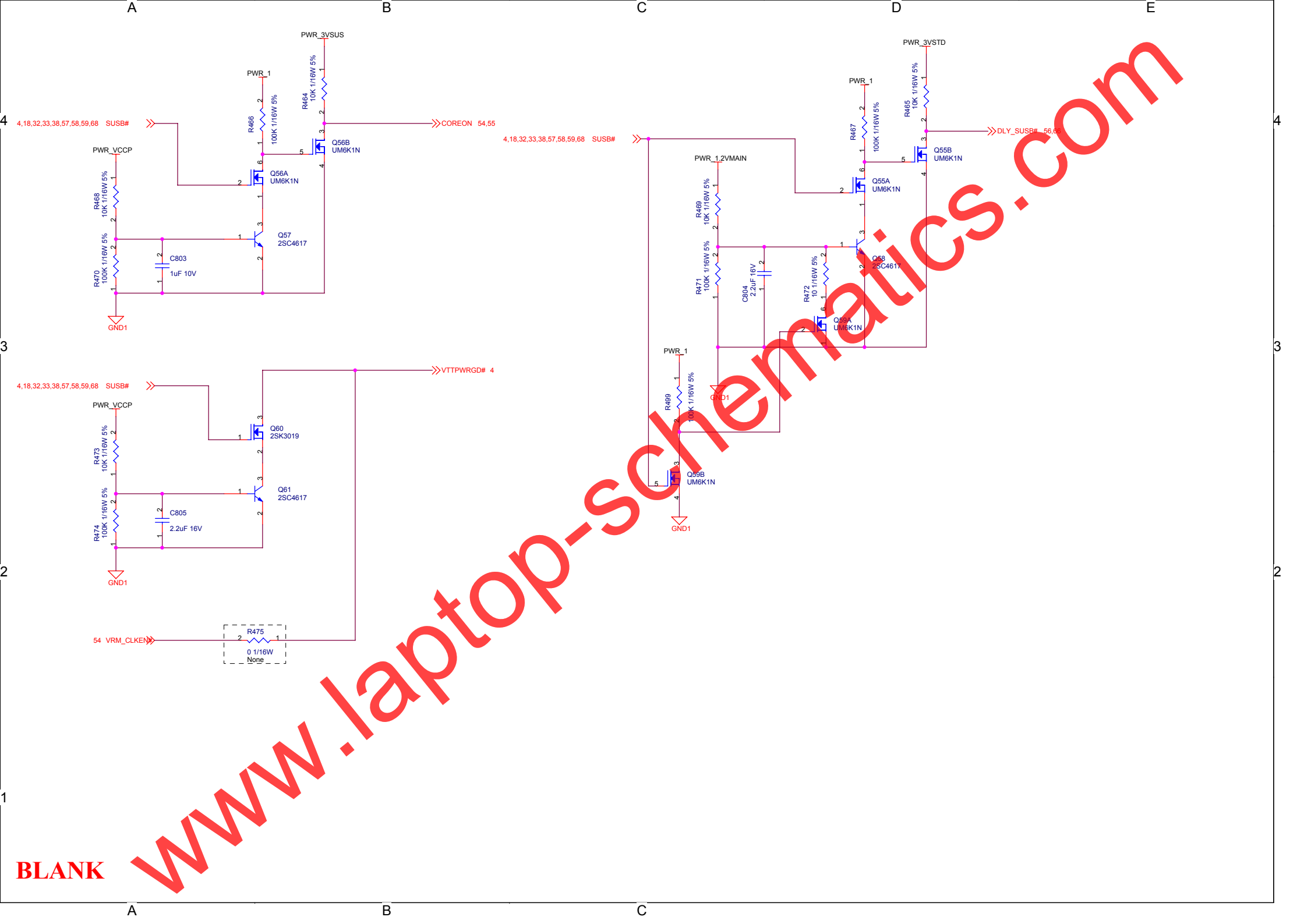
E



A



B



# Block diagram

