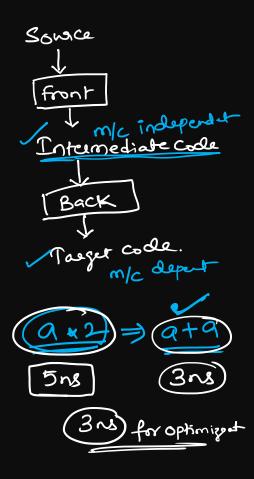
Code Optimization

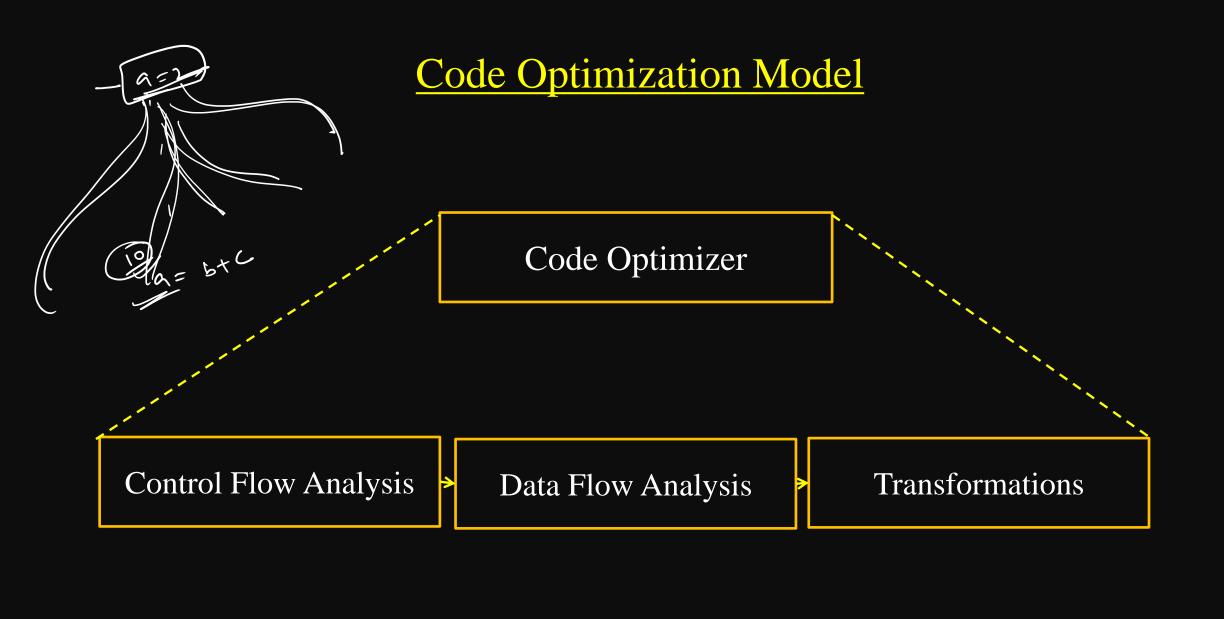
Code optimization phase is an optional phase in the phases of a compiler, which is either before or after the code generation phase.

The code optimization is required to produce an efficient target code, this improvement is achieved by program transformations that are traditionally called optimizations

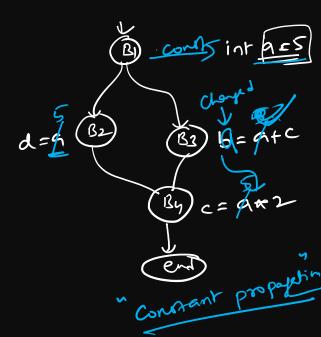
The transformations provided by an optimizing compiler should have the following properties.

- 1. The transformation must not change the meaning of the original source program.
- 2. The Optimizing technique should increase the speed of program by a considerable amount.
- 3. The transformation technique must justify the effort.



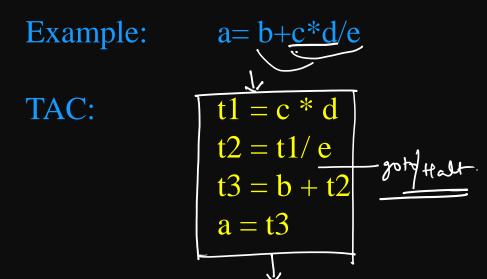


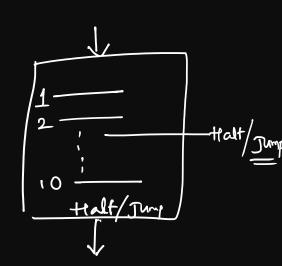
- 1. Control flow analysis: It determines the control flow structure of a program and builds a control flow graph.
- 2. Data flow analysis: It determines the flow of scalar values, that is flow analysis propagates data flow information along a flow graph.
- 3. Transformation: Transformation helps in improving the code without changing the meaning or functionality.



Basic Blocks

A Basic Block is a sequence of consecutive statements in which flow of control enters at the beginning and leaves at the end without halt or possibly of branching except at the end.





Procedure to identify the Basic Blocks

Given a Three Address Code, first identify the leader statement and group the leader statement with the statements up to the next leader.

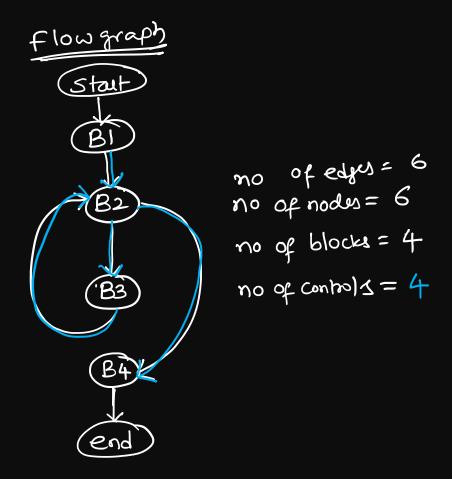
To identify the leader use the following rules:

- 1. First statement in the program is a leader
- 2. Any statement that is the <u>target of a conditional</u> or unconditional statement is a leader.
- 3. Any statement that immediately follows a conditional or unconditional statement is a leader.

Example

Identify the basic blocks for the following code fragment

```
(1) Leader \rightarrow 1. i=0
main()
                                                  BI
                          (2) Leady -> 3.
  int i=0, n=10;
                                         if <u>i>t</u>1 goto <u>11</u>
  int a[n];
                          (3) Leader \rightarrow 5. t2=i*i
  while (i \le (n-1))
                                     6. t3=4*i
                                     7. a[t3]=t2
    a[i]=i*i;
                                                      B3
                                     8. t4=i+1
    i=i+1;
                                     9. i=t4
                                     10. goto 3
  return
                   (3) (2) Leader -> 11. return
```



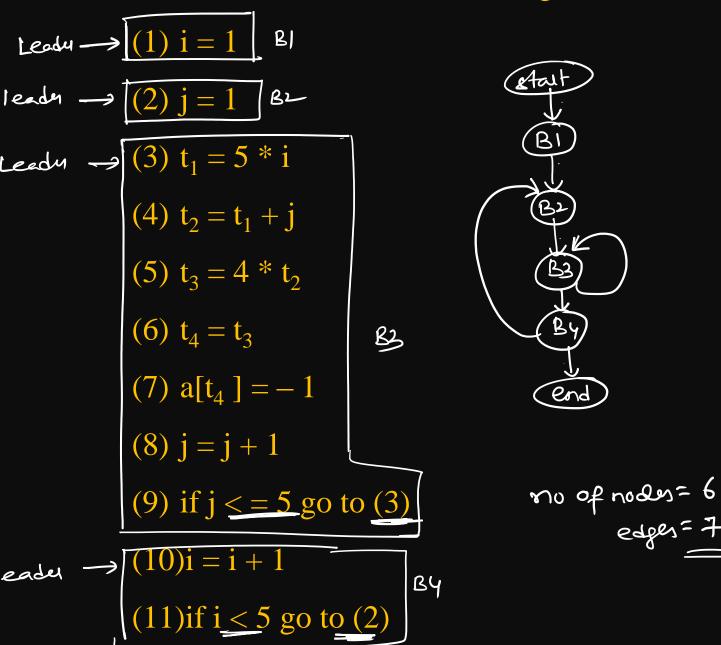
Flow Graph

A graphical representation of three address code is called flow graph.

The nodes in the flow graph represent a single basic block and the edges represent the flow of control.

These flow graphs are useful in performing the <u>control flow</u> and dataflow analysis and to apply local or global optimization.

Q. Consider the intermediate code given below.



The number of <u>nodes</u> and <u>edges</u> in the control-flow graph constructed for the above code, respectively, are

(GATE-15- SET2)

- (a) 5 and 7
- (b) 6 and 7
- (c) 5 and 5
- (d) 7 and 8

Q. Consider the following grammar:

 \rightarrow stmt \rightarrow if expr then expr else expr; stmt | o expr → term relop term | term term → id | number $id \rightarrow a \mid b \mid c$ number \rightarrow [0-9] where relop is relational operator (e.g., , ...), o refers to the empty statement, and if, then, else are terminals.

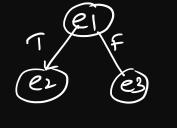
Consider a program P following the above grammar containing ten if erminals.

The number of control flow paths in P is $\frac{102-4}{2}$.

For example, the program

if e_1 then e_2 else e_3 has 2 control flow paths, $e_1 \rightarrow e_2$ and $e_1 \rightarrow e_3$.

(GATE-17-SET1)



2 Contrib
$$e_1 \rightarrow e_2$$
 $e_1 \rightarrow e_3$

Classification of Code Optimization

Code Optimization

Machine Dependent

- It considers the role properties

 Such as instruction format,

 adversing modes,

 Register set
- -> It is performed on Parget code

Machine Independent

- It does not consider the m/c properties.
 - 4 It depends on program logic
 - 1) It is performed on intermediate code.

Scope of Optimization

1. <u>Local Scope</u>: When a local optimization has to be performed its scope is limited to certain specific block of statements

2. Global Scope: When a global optimization has to be performed, its scope is throughout the program. It is performed using dataflow analysis.

- code motion

-> strength reduction.

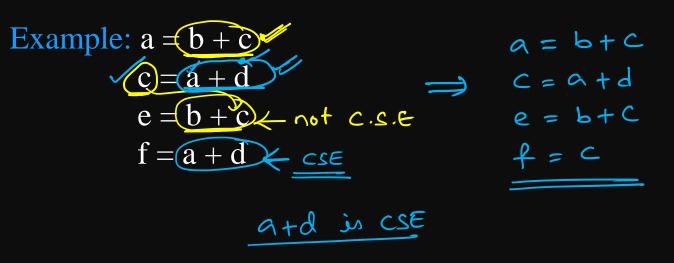
Local Optimization

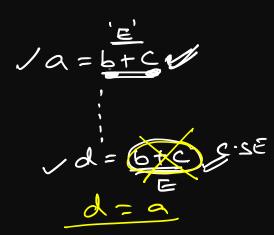
- 1. Common sub expression elimination
- 2. Copy propagation
- 3. Dead code elimination
- 4. Constant Propagation
- 5. Constant folding
- 6. Strength Reduction

Common sub expression elimination

An expression E is called a common sub expression if it was previously computed, and the values of variables in E have not changed since the previous computation.

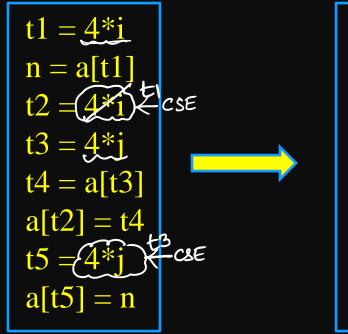
In common sub expression elimination we have to avoid recomputing of the expression if it's value is already been computed





Common sub expression elimination

Example:

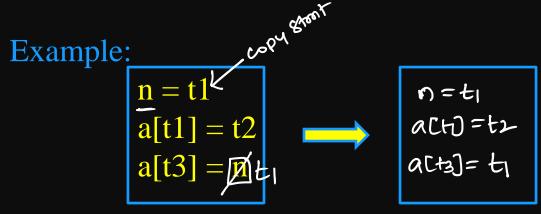


 $t_1 = 4 \times 1$ $n = a[t_1]$ $t_2 = t_1$ $t_3 = 4 \times 1$ $t_4 = a[t_3]$ $a[t_2] = t_4$ $t_5 = t_3$ $a(t_5) = n$

Copy Propagation

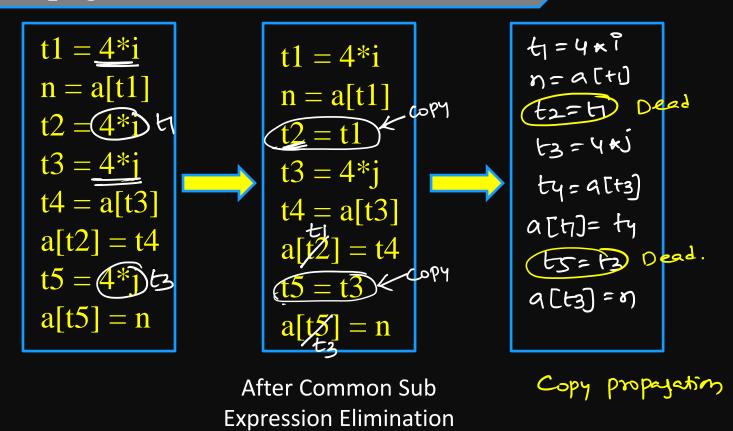
Copy propagation means use of <u>one variable</u> instead of another.

If (x=y) is a statement, which is called <u>copy statement</u>, then copy propagation is a kind of transformation in which use (y for x) wherever possible after the copy statement (x=y).



$$x=y$$
 pead
 $a=b+xy$

Copy Propagation

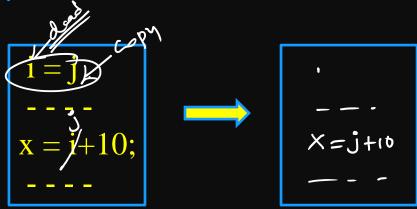


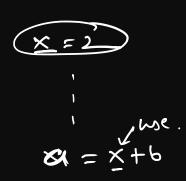
Dead code Elimination

A variable is said to be live in the program if its value can be used subsequently otherwise it is said to be dead at that point.

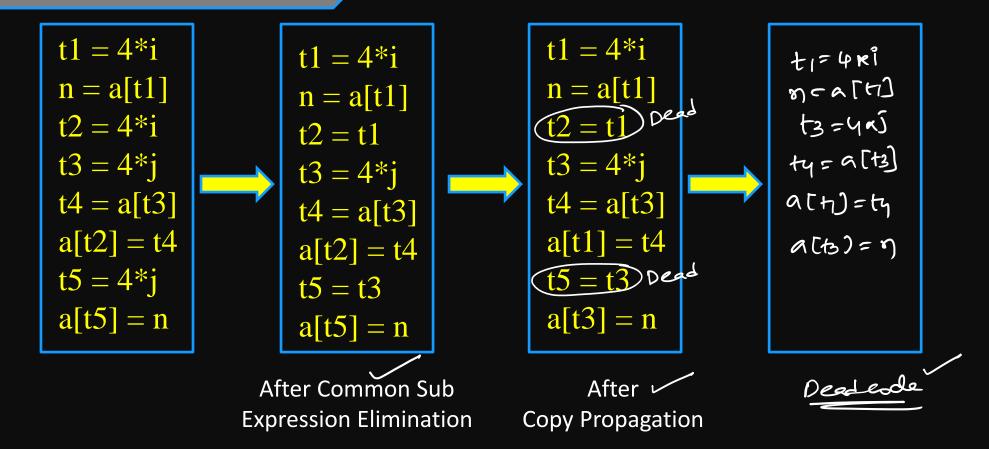
The dead code is basically an useless code. An Optimization can be performed by eliminating such a dead code.







Dead Code Elimination



Constant Propagation

It can be defined as the process of replacing the constant value of variables in the expression.

Constants assigned to a variable can be propagated through the flow graph and can be replaced when the variable is used.

Constint
$$a=2$$
;
$$P = a+b$$

$$2$$

const int
$$a = 30$$
;
 $b = 20 - a/2$;
 $c = b * (30/a + 2) - a$;

Const int
$$a = 30$$
;
 $b = 20 - 30/2$ $5 = 5$
 $c = b * (30/30 + 2) - 30$

Constant Folding

It is a technique in which the constant expressions are calculated during compilation.

Replacing expressions consisting of constants with their final value at compilation time rather than doing the calculation at run time.

const int
$$a = 30$$
; const int $a = 30$; constant propagation constant folding.

Strength Reduction

Strength reduction is used to replace the expensive operation by the cheaper one .

Example:
$$a \times 2 \Rightarrow a + a$$

$$a/2 \Rightarrow a \times 0.5$$

$$a \times 4 \Rightarrow a \times 2^2 \Rightarrow a < 2$$

Loop Optimization

Loop optimization is a technique in which code optimization is performed on inner loops.

- 1. Code Motion
- 2. Induction variables and strength reduction
- 3. Loop Unrolling
- 4. Loop Jamming

Code Motion

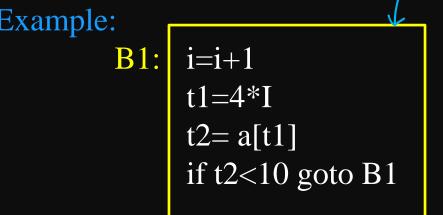
Code motion is a technique which moves the code outside the loop.

If there is some expression in the <u>loop</u> whose result remains unchanged even after executing the loop several times, then such an expression should be placed just before the loop.

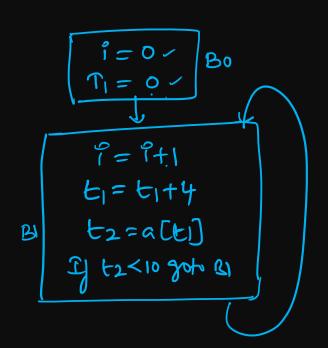
Induction variables and reduction in strength

A variable x is called an induction variable of loop, if the value of variable gets changed every time, it is either incremented or decremented by some constant.





 $\hat{i} = 0$



Loop Unrolling

In this method we duplicate the body of the loop several times, inorder to decrease the number of times loop condition is tested and number of jumps.

for
$$(i=1, i \times 100; i+1)$$
 $ACIJ = 10;$
 A

Note: this approach requires that, the number of iterations to be known at compile time.

Loop Jamming / Loop Combining '! Loop Fusion

In this method several loops are merged to one loop.

It is another technique which attempts to reduce loop overhead when two adjacent loops would iterate the same number of times, their bodies can be combined as long as they make no reference to each others data.

Data Flow Analysis

In Dataflow analysis the analysis is made on the flow of data. That is it determines the information regarding the definition and use of the data in the program. Using this kind of analysis optimization can be done.

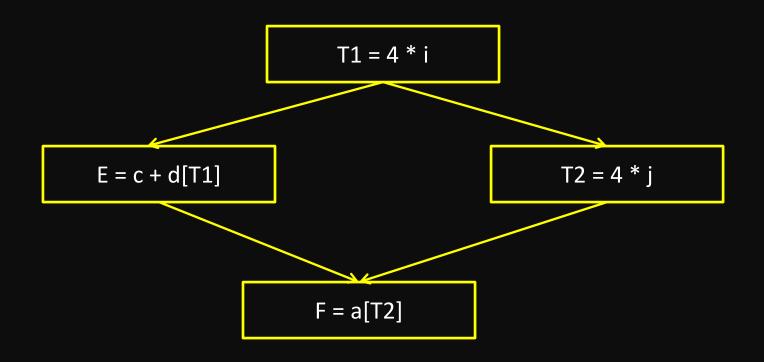
The Dataflow analysis is basically a process in which the values are computed using dataflow properties. The dataflow property represents the certain information regarding usefulness of the data items for the purpose of optimization.

These dataflow properties are,

- 1. Available expressions
- 2. Reaching definitions
- 3. Live variables
- 4. Busy Expressions

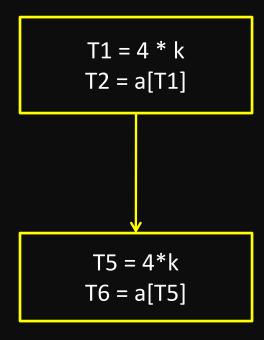
Available Expression

An expression x+y is available at point P if every path from the node to P evaluates x+y and after the last such evaluation prior to reaching P, there are no subsequent assignments to X or Y.



Available Expression

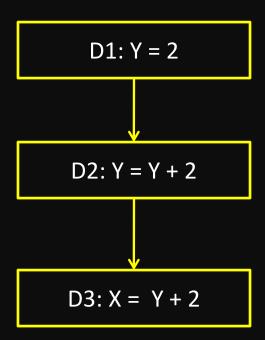
Advantage: The use of available expression is to eliminate Common Sub Expressions.



Reaching Definition

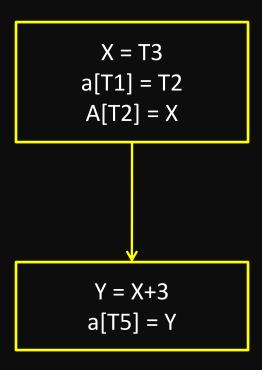
A definition 'D' reaches at point P if there is a path from D to P along which D is not killed.

A definition 'D' of variable x is killed when there is a redefinition of x.



Reaching Definition

Advantage: The Reaching definitions are used in constant and variable propagation.



Live Variables

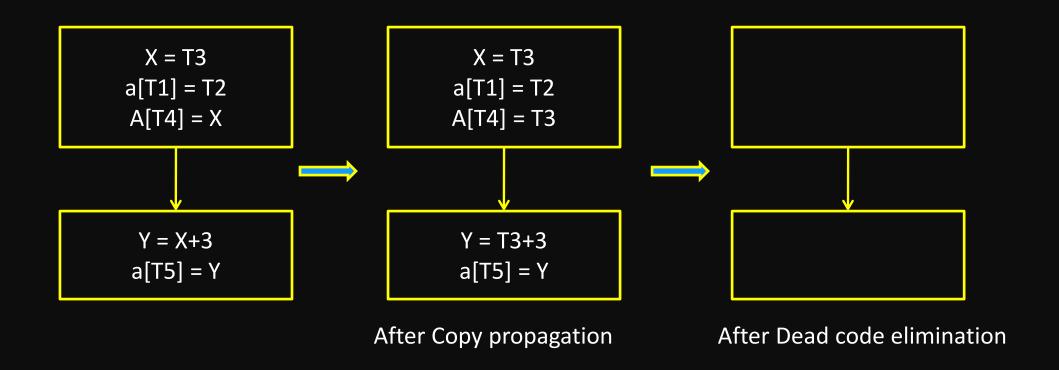
A Variable 'X' is live at some point 'P' if there is some path from 'P' to the exit, along which the value of 'X' is used before it is redefined, otherwise it is said to be dead at that point.

Live Variables

Advantage: Live variables are useful for dead code elimination.

If variable X is not live after an assignment X=....

Then the assignment is redefined and can be deleted as dead code.



Live Variables

Advantage: Live variables are useful in register allocation.

If variable X is live in basic block B, it is potential candidate for register allocation.

Busy Expression

An expression 'E' is said to be a busy expression along some path Pi - - - Pj if and only if an evaluation of E exists along some path Pi - - - Pj and no definition of any operand exists before its evaluation along the path.

$$i = 0$$
 $T1 = max - 1$
 $i = i + 1$
If $T1 \le 10$ goto B1

Advantage: Busy Expressions are useful in performing code movement optimization.

Conclusion

By performing Data flow analysis we get available expressions, reaching definitions, live variables and busy expressions and these are useful for applying various global common sub expression elimination, copy propagation, Dead code elimination and code movement optimizations.

Q. Which one of the following is **FALSE**?

$$(GATE - 14 - SET1)$$

- (a) A basic block is a sequence of instructions where control enters the sequence at the beginning and exists at the end.
- (b) Available expression analysis can be used for common subexpression elimination.
- (c) Live variable analysis can be used for dead code elimination.
- (d) $x = 4 * 5 \Rightarrow x = 20$ is an example of common sub-expression elimination

```
for (i = 0; i < n; i ++)
            for (j = 0; j < n; j ++)
                    if (i % 2)
                            X + = (4 * j + 5 \times i);
                             Y + = (7 + 4 * j);
```

Which one of the following is **FALSE**?

(GATE - 06)

- (a) The code contains loop-invariant computation.
- (b) There is scope of common sub expression elimination in this code.
- (c) There is scope of strength reduction in this code.
- (d) There is scope of dead code elimination in this code.

(a) The code contains loop-invariant computation.

```
for (i = 0; i < n; i ++)
           for (j = 0; j < n; j ++)
                    if (i % 2)
                            X + = (4 * j + 5 \times i);
                             Y + = (7 + 4 * j);
```

- (b) There is scope of common sub expression elimination in this code.
- Q. Consider the following C code segment.

```
for (i = 0; i < n; i ++)
           for (j = 0; j < n; j ++)
                    if (i % 2)
                            X + = (4 * j + 5 \times i);
                             Y + = (7 + 4 * j);
```

(c) There is scope of strength reduction in this code.

```
for (i = 0; i < n; i ++)
           for (j = 0; j < n; j ++)
                    if (i % 2)
                            X + = (4 * j + 5 \times i);
                             Y + = (7 + 4 * j);
```

(d) There is scope of dead code elimination in this code

```
for (i = 0; i < n; i ++)
           for (j = 0; j < n; j ++)
                    if (i % 2)
                            X + = (4 * j + 5 \times i);
                            Y + = (7 + 4 * j);
```

```
z = x + 3 + y \rightarrow f1 + y \rightarrow f2;
for (i = 0; i < 200; i = i + 2)
   if (z > i)
      p = p + x + 3;
      q = q + y \rightarrow f1;
    else
      p = p + y \rightarrow f2;
      q = q + x + 3;
```

Assume that the variable y points to a struct (allocated on the heap) containing two fields f1 and f2 and the local variables x, y, z, p, q and i are alloted registers. Common sub-expression elimination (CSE) optimization is applied on the code. The number of addition and dereference operations (of the form $y \to f1$ or $y \to f2$) in the optimized code, respectively, are: (GATE - 21-Set2)

- (a) 403 and 102
- (b) 303 and 102
- (c) 203 and 2
- (d) 303 and 2



















Q. For a statement S in a program, in the context of liveness analysis, the following sets are defined.

USE (S): the set of variables used in S

IN (S): the set of variables that are live at the entry of S

OUT (S): the set of variables that are live at the exit of S

Consider a basic block that consists of two statements

 S_1 followed by S_2 .

Which one of the following statements is correct? (GATE - 21-Set2)

- (a) OUT $(S1) = IN (S1) \cup USE (S1)$ (b) OUT (S1) = IN (S2)
- (c) OUT $(S1) = USE(S1) \cup IN(S2)$ (d) OUT $(S1) = IN(S2) \cup OUT(S2)$