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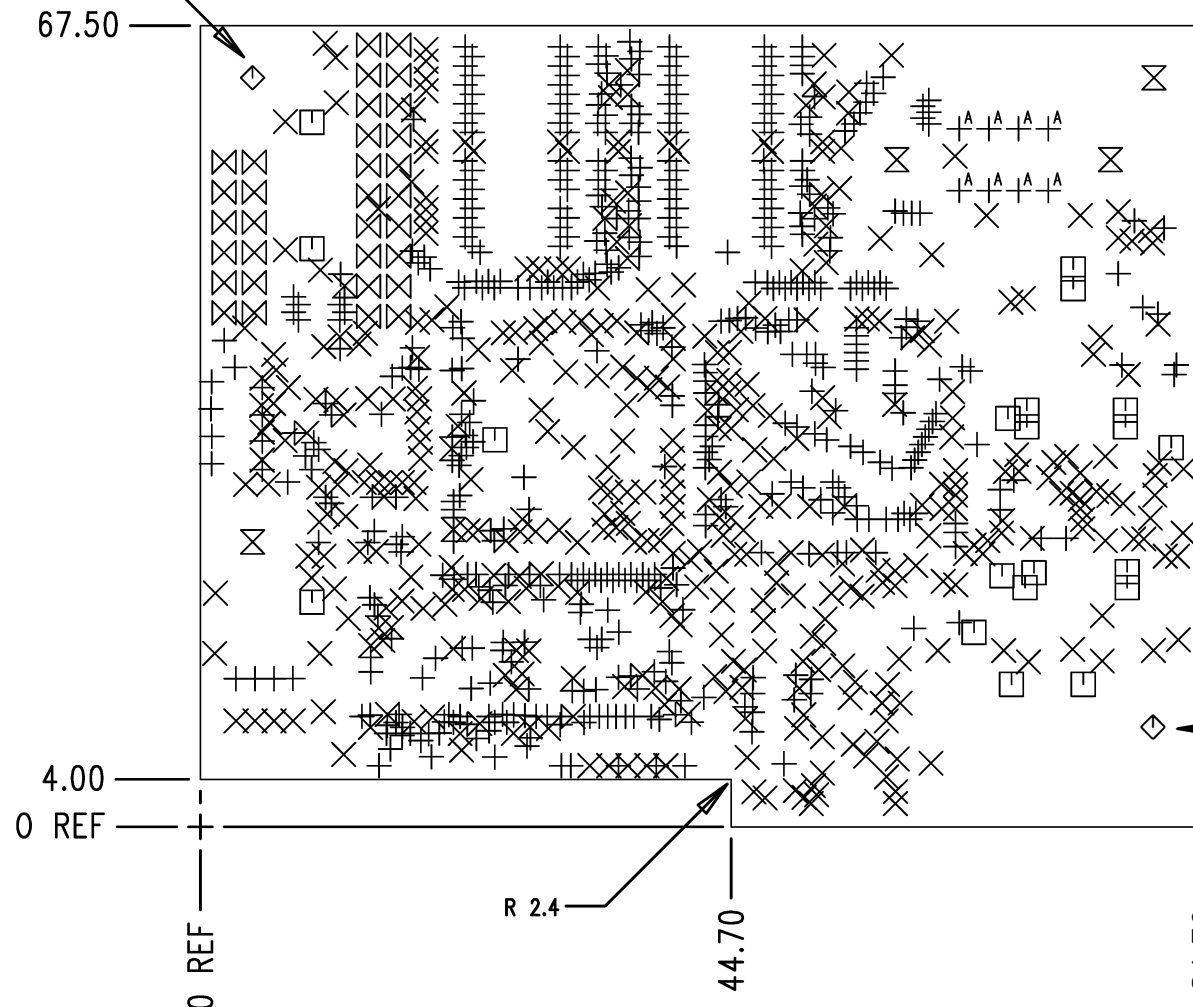
UNLESS OTHERWISE SPECIFIED: BUILD TO THE LATEST REVISION OF IPC-A-600, CLASS 2.

1. BOARD IS VIEWED FROM PRIMARY SIDE (LAYER 1). THE TOTAL NUMBER OF CONDUCTIVE LAYERS IS (8).
 2. MATERIALS:
 - A. LAMINATE AND PREPREG (B-STAGE) TO BE IN ACCORDANCE WITH IPC-4101/24. MATERIAL MUST MEET UL 94V-0 FLAMMABILITY RATING. FR406 MATERIAL WITH A Tg OF 160C OR GREATER IS RECOMMENDED.
 - B. COPPER FOIL TO BE IN ACCORDANCE WITH IPC-MF-150. UNLESS OTHERWISE SPECIFIED, ALL COPPER WEIGHT FOR INNER SIGNAL LAYERS TO BE 1/2 OZ. INNER PLANE LAYERS TO BE 1 OZ. FOR OUTER LAYERS 1.5 OZ. COPPER WEIGHTS ARE TO BE CONSIDERED FLAT.
- LAYER STACK-UP PER DETAIL "A".
3. FABRICATION PROCESS SHALL BE SOLDERMASK OVER BARE COPPER FINISHED PRODUCT TO BE U.L. APPROVED AND CERTIFIED. ALL BOARDS SHALL BE IDENTIFIED WITH VENDOR'S LOGO FOLLOWED BY 94V-0 AND AND DATE CODE IN ETCH ON THE SECONDARY SIDE.
 4. LAYER TO LAYER REGISTRATION OF THE CONDUCTOR PATTERNS SHALL BE WITHIN 0.10mm (0.004") DIA. TRUE POSITION, WITH AN ABSOLUTE MINIMUM OF 0.05mm (0.002") BETWEEN PLATED THRU HOLE WALLS AND ADJACENT CONDUCTIVE SURFACES.
 5. NO FINAL CONDUCTOR WIDTH TO BE LESS THAN 0.13mm (0.005") +/- 0.03mm AND NO CLEARANCE BETWEEN ANY TWO CONDUCTORS SHALL BE LESS THAN 0.13mm (0.005") +/- 0.03.
 6. COPPER PLATE:
 - A. HOLES - COPPER PLATING ON WALLS OF HOLES SHALL BE 20 um (0.0008") AVERAGE MINIMUM, NO VALUE LESS THAN 18 um (0.0007").
 7. SOLDERMASK: BOTH SIDES USING LIQUID PHOTO IMAGEABLE PROCESS PER IPC-SM-840, TYPE B, CLASS II. COLOR: GREEN, MATTE FINISH.
 9. ALL EXPOSED CONDUCTIVE PATTERN AREAS NOT COVERED WITH SOLDER MASK OR OTHER PLATING SHALL BE HOT AIR SOLDER LEVELED USING SN63A TIN-LEAD SOLDER PER ANSI/J-STD-001.
 10. SILKSCREEN LEGEND OVER SOLDERMASK ON BOTH SIDES USING NON-CONDUCTIVE WHITE INK. OVERLAP ONTO TENTED HOLES IS ALLOWED. SILKSCREEN LEGEND TO PCB REGISTRATION +/-0.25mm (0.010").
 11. DATUM HOLE TO EDGE TOLERANCE SHALL NOT VARY MORE THAN +/-0.25mm (0.010").
 12. ALL TOLERANCES ARE NON-ACCUMULATIVE.
 13. NO SOLDER MASK BLEEDING OR RESIDUE IS ALLOWED IN AREAS DESIGNED TO BE FREE OF SOLDERMASK. VIAS MAY BE COVERED BY DESIGN ONLY. THERE IS TO BE NO MASK MATERIAL ON GOLD FINGER CONNECTORS, OR ON SURFACE MOUNT CONTACTS. PRESENCE OF SOLDER MASK RESIDUE, SCRATCHES, AND/OR CONTAMINATION WHICH IMPAIR CONTACT PERFORMANCE ARE MAJOR DEFECTS AND CAUSE FOR LOT REJECTION.
 14. ALL BOARDS SHALL BE 100% ELECTRICALLY TESTED FOR OPENS / SHORTS. APPLY TEST STAMP IN NON-LEGEND AREA ON SECONDARY SIDE.
 15. PRIOR TO BOARD FABRICATION, COMPARE GERBER DATA TO SUPPLIED IPC-D-356A NET LIST. REPORT ALL DISCREPANCIES.
 16. ANY MODIFICATIONS TO THE GERBER DATA OR DEVIATION FROM THESE NOTES MUST BE APPROVED IN WRITING.

17. ALL MATERIALS AND COATINGS MUST COMPLY WITH EU DIRECTIVE 2000/53/EC (ELV DIRECTIVE).

18. THIS COMPONENT HAS BEEN DESIGNED TO COMPLY WITH A REGULATORY REQUIREMENT. CONTACT TESLA TYPE APPROVAL BEFORE UNDERTAKING DESIGN MODIFICATIONS. A COMPONENT WHICH EMBODIES ALL REQUIREMENTS STATED ON THIS DRAWING WILL COMPLY WITH THE REGULATORY REQUIREMENT.

3.5 +0.08/-0.0mm DIA TOOLING HOLE



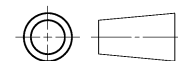
SIZE	QTY	SYM	PLATED	TOL
0.26	519	⊕	YES	+/-0.08
0.3	425	⊗	YES	+/-0.08
0.5	20	□	YES	+/-0.08
1	8	⊕ ^A	YES	+/-0.08
1.016	32	⊗	YES	+/-0.08
3.5	2	◇	NO	+/-0.08
3.5	4	⊗	YES	+/-0.08

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UNLESS OTHERWISE SPECIFIED:
DIMENSIONS ARE IN MILLIMETERS AND
AND APPLY AFTER PLATING.
ALL MACHINED SURFACES AMERICAN
STANDARD FINISH. 16-32/

TOLERANCES ARE:

ANGLES	± 0.5
FRACTIONS	± ---
X DECIMAL	± 1
X.X DECIMAL	± 0.3
X.XX DECIMAL	± 0.10



Tesla Motors

Inc.

F

ITEM NAME: PCB FABRICATION
VEHICLE DISPLAY CONTROLLER

ITEM NO.	DESCRIPTION	QTY	UNIT	PRICE	TOTAL
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FNG APPV

DATE: _____

END DATE	---	---	DO NOT SCALE DRAWING
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SHEET	OF
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