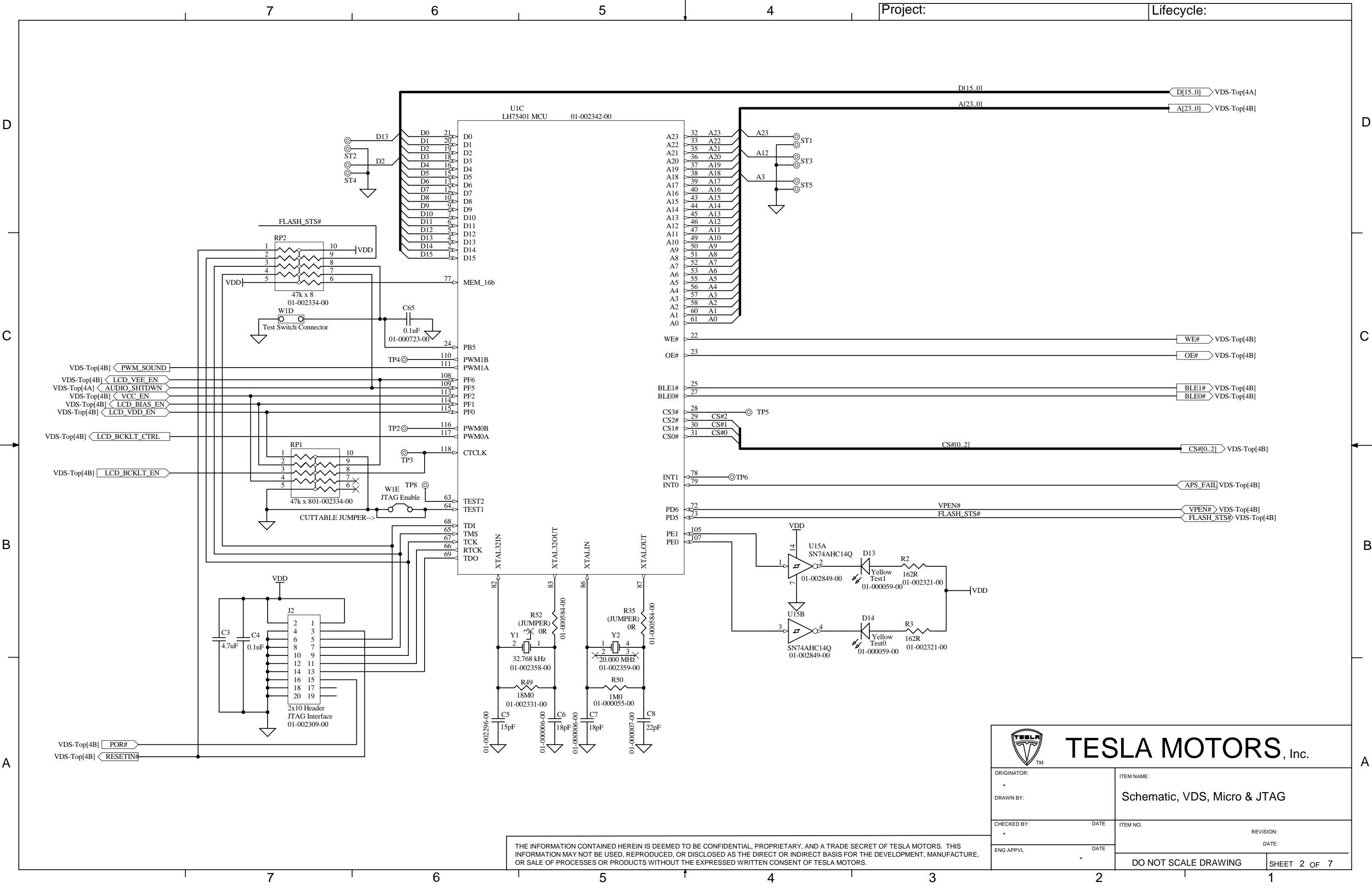



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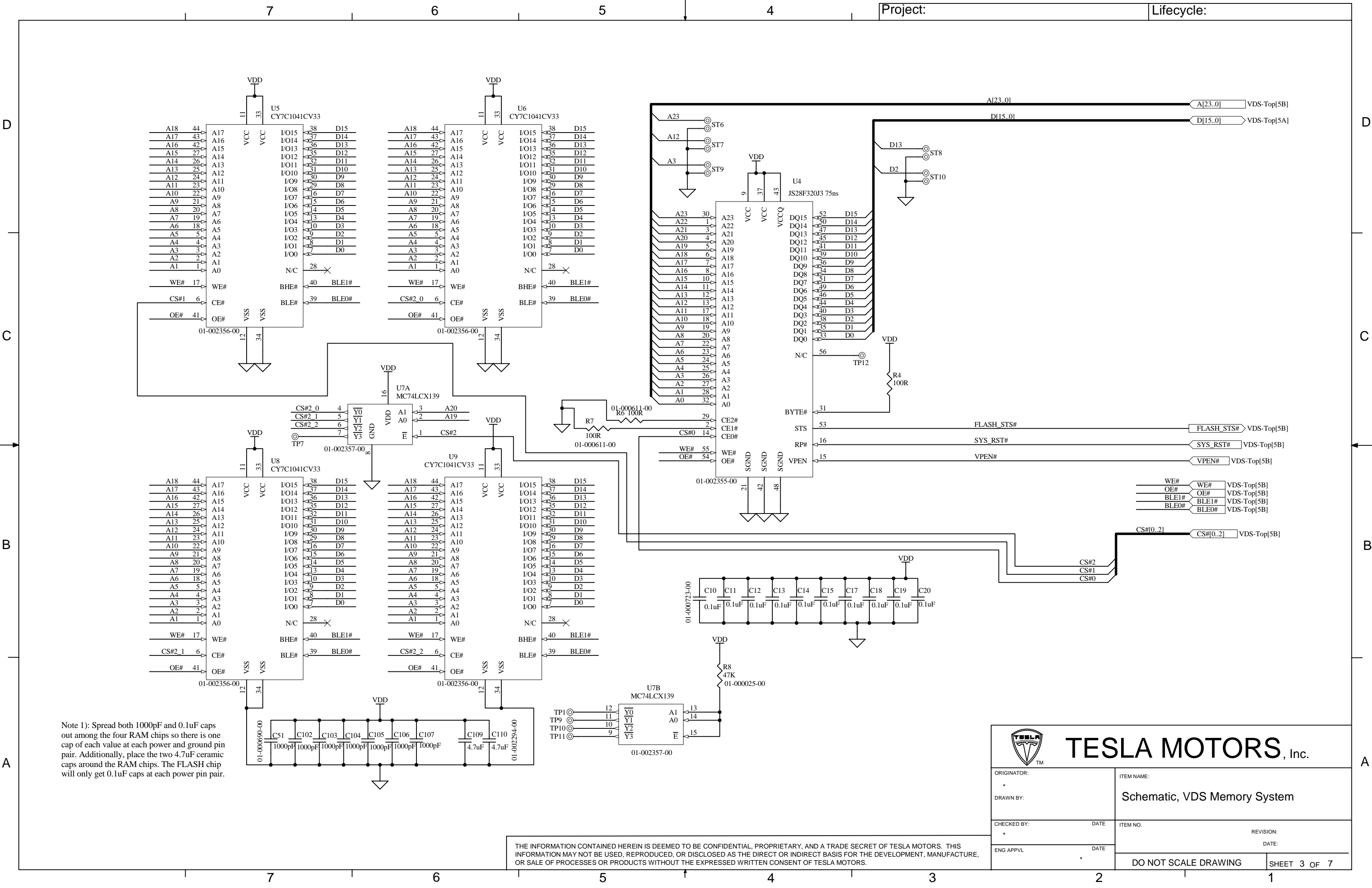
ORIGINATOR: *	ITEM NAME: Schematic, Top, Vehicle Display System, (VDS)		
DRAWN BY:	DATE	ITEM NO.	REVISION:
CHECKED BY:	DATE	DATE:	
ENG APPVL	DATE	DATE:	
DO NOT SCALE DRAWING		SHEET 1 OF 7	


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ORIGINATOR: *		ITEM NAME: Schematic, VDS, Micro & JTAG	
DRAWN BY: *		ITEM NO. REVISION: DATE:	
CHECKED BY: *		DATE:	
ENG APPVL *		DATE:	
DO NOT SCALE DRAWING		SHEET 2 OF 7	

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ORIGINATOR: *	ITEM NAME: Schematic, VDS Memory System		
DRAWN BY: *	CHECKED BY: *	DATE	ITEM NO.
ENG APPVL *	DATE		REVISION: DATE:
DO NOT SCALE DRAWING			SHEET 3 OF 7

D

D

C

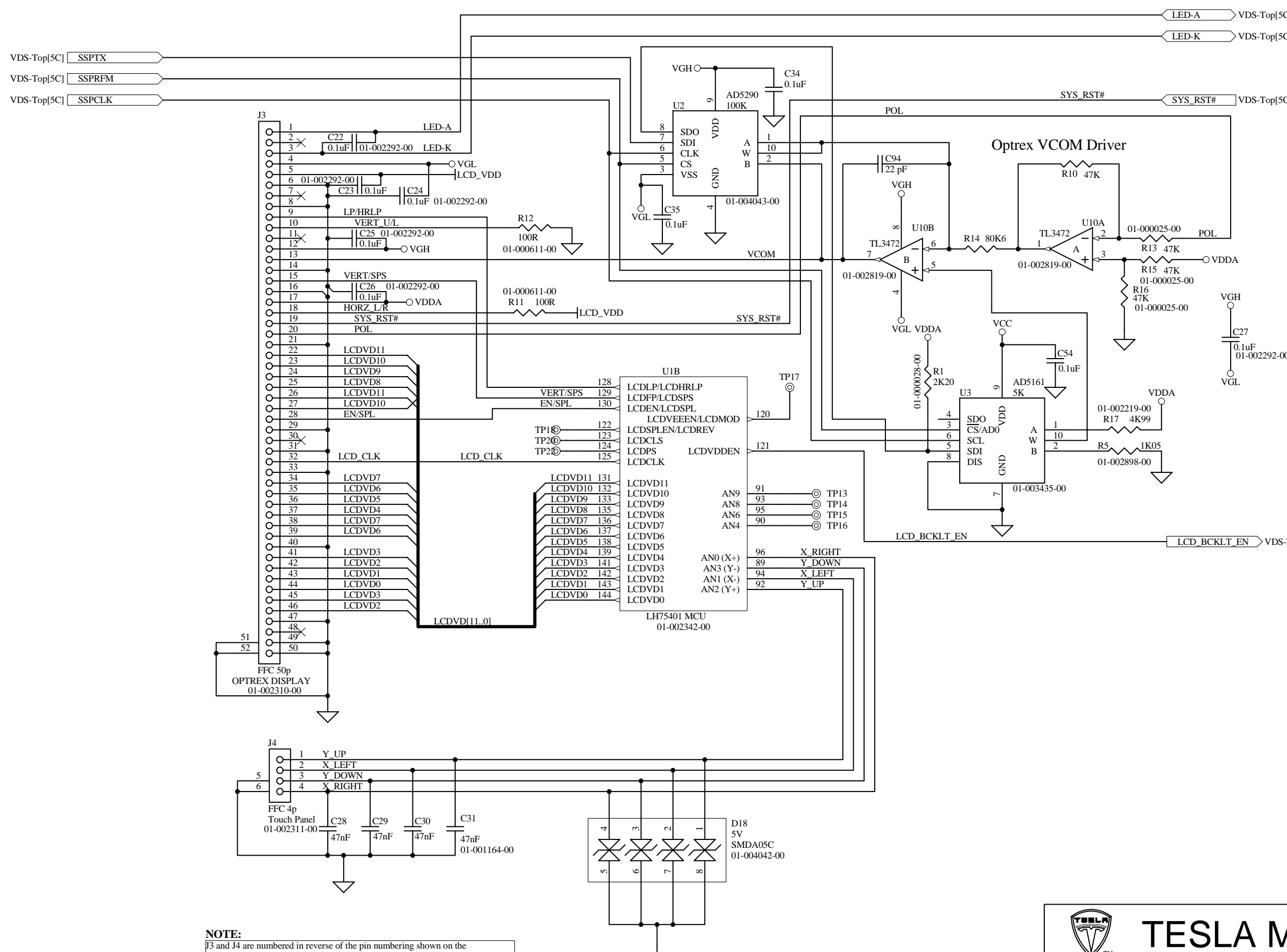
C

B

B


A

A



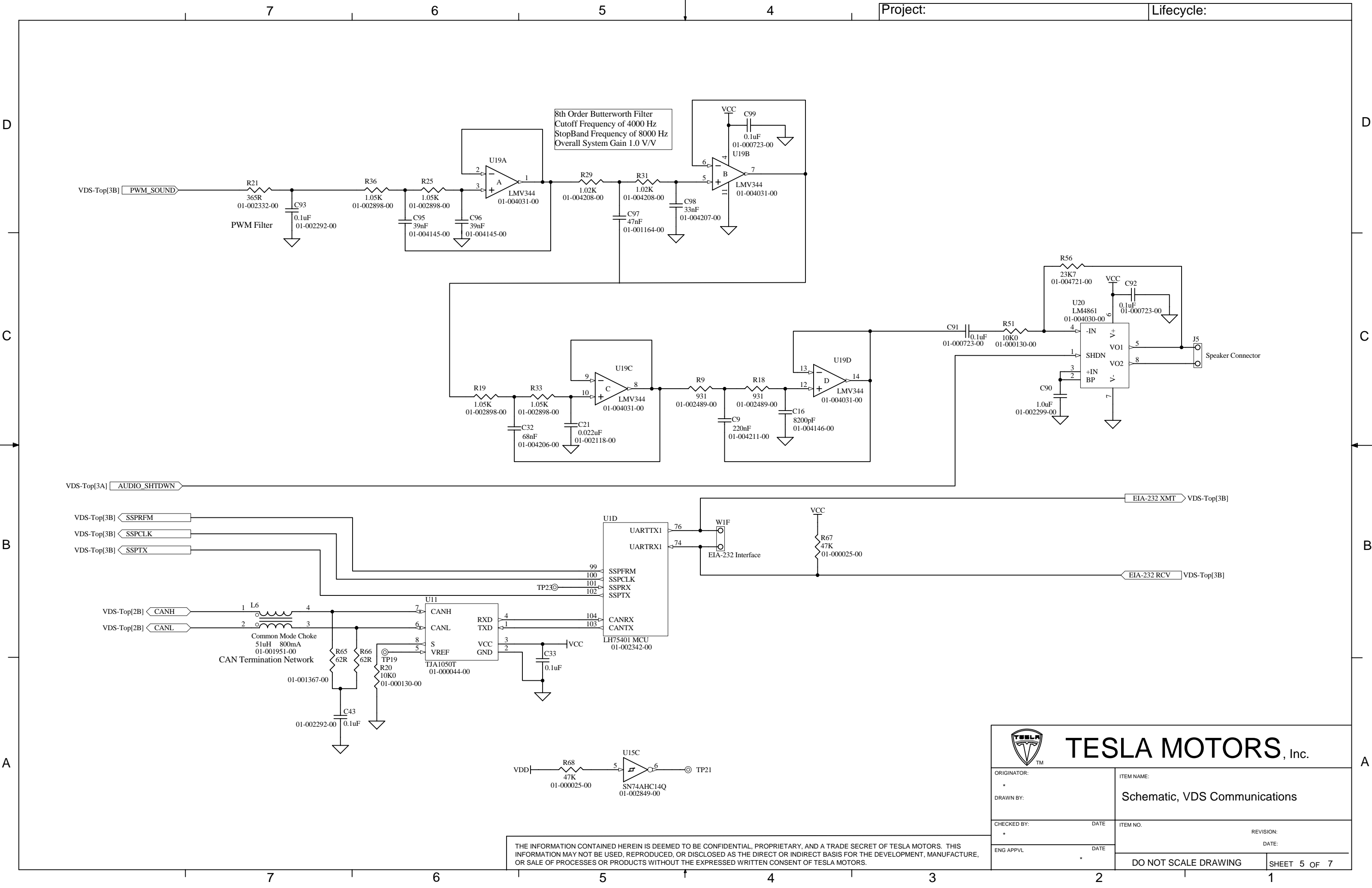
**NOTE:**  
J3 and J4 are numbered in reverse of the pin numbering shown on the connector. Where pin #1 is shown on the connector will be pin 50 on J3 and pin 4 on J4.  
The numbering shown for pin 1 on the board is correct and should match the numbering of the LCD interface cable.

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ORIGINATOR: *	ITEM NAME: Schematic, VDS LCD Interface		
DRAWN BY: *	DATE	ITEM NO.	REVISION:
CHECKED BY: *	DATE	DATE:	
ENG APPVL *	DATE	DO NOT SCALE DRAWING	
		SHEET 4 OF 7	



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ORIGINATOR:  
\*  
DRAWN BY:  
CHECKED BY:  
ENG APPVL

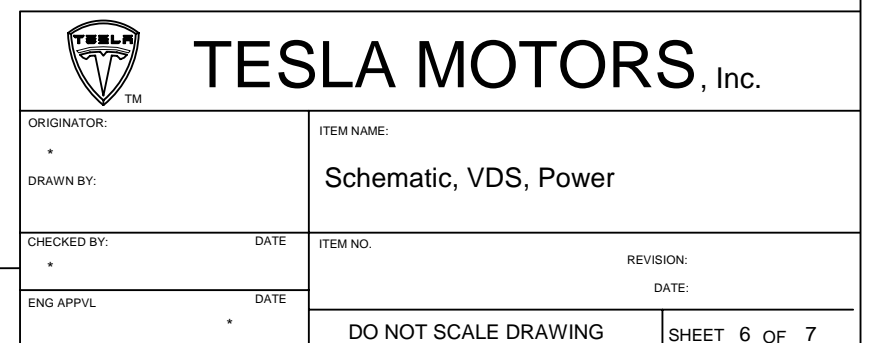
DATE  
\*  
DATE

ITEM NAME:  
Schematic, VDS Communications

ITEM NO.  
REVISION:  
DATE:

DO NOT SCALE DRAWING

SHEET 5 OF 7



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