
STM32
CubeMX

1. Description

1.1. Project

Project Name	0_GETTING_STARTED
Board Name	NUCLEO-F091RC
Generated with:	STM32CubeMX 6.1.0
Date	04/07/2021

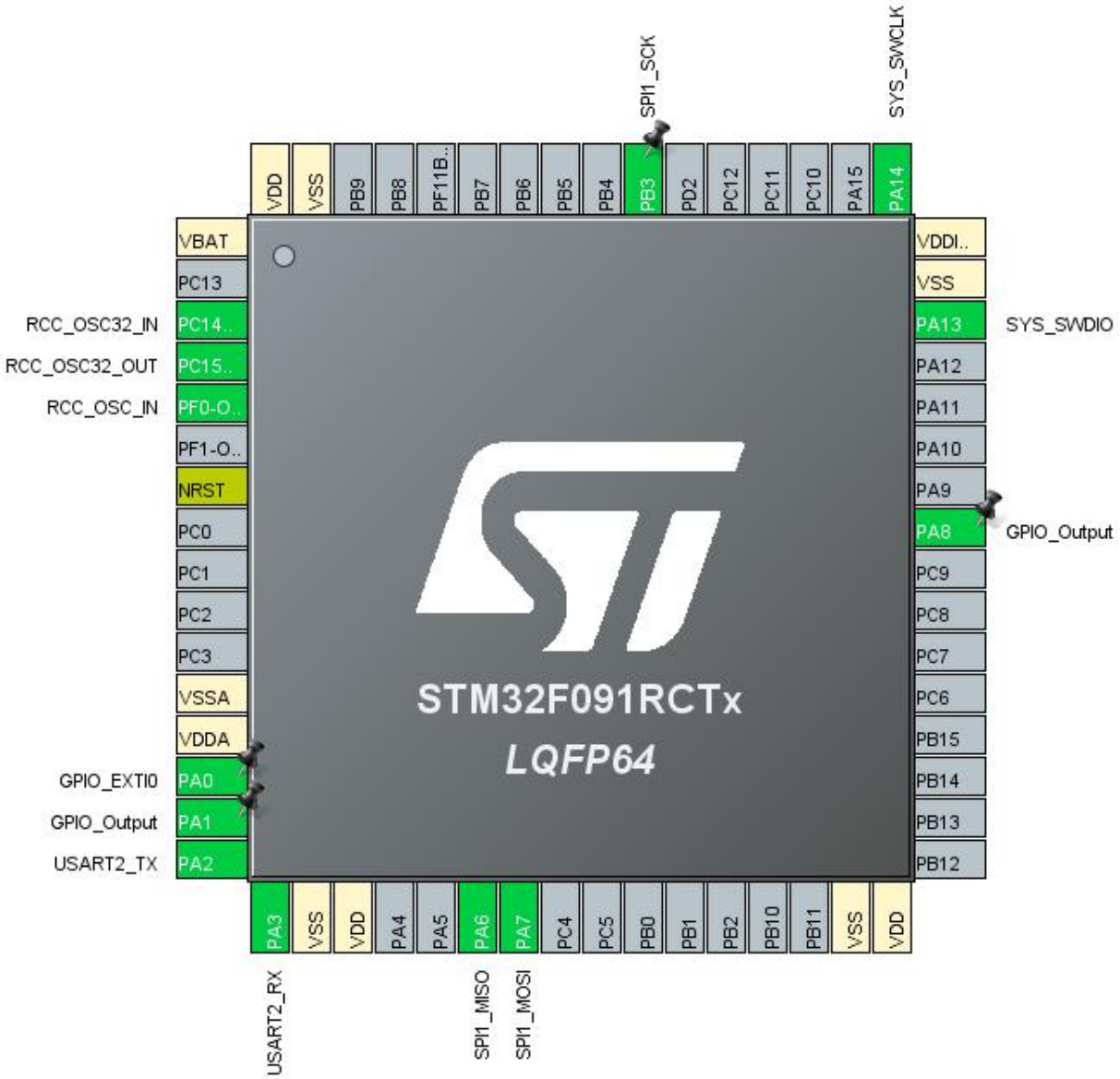
1.2. MCU

MCU Series	STM32F0
MCU Line	STM32F0x1
MCU name	STM32F091RCTx
MCU Package	LQFP64
MCU Pin number	64

1.3. Core(s) information

Core(s)	Arm Cortex-M0

2. Pinout Configuration

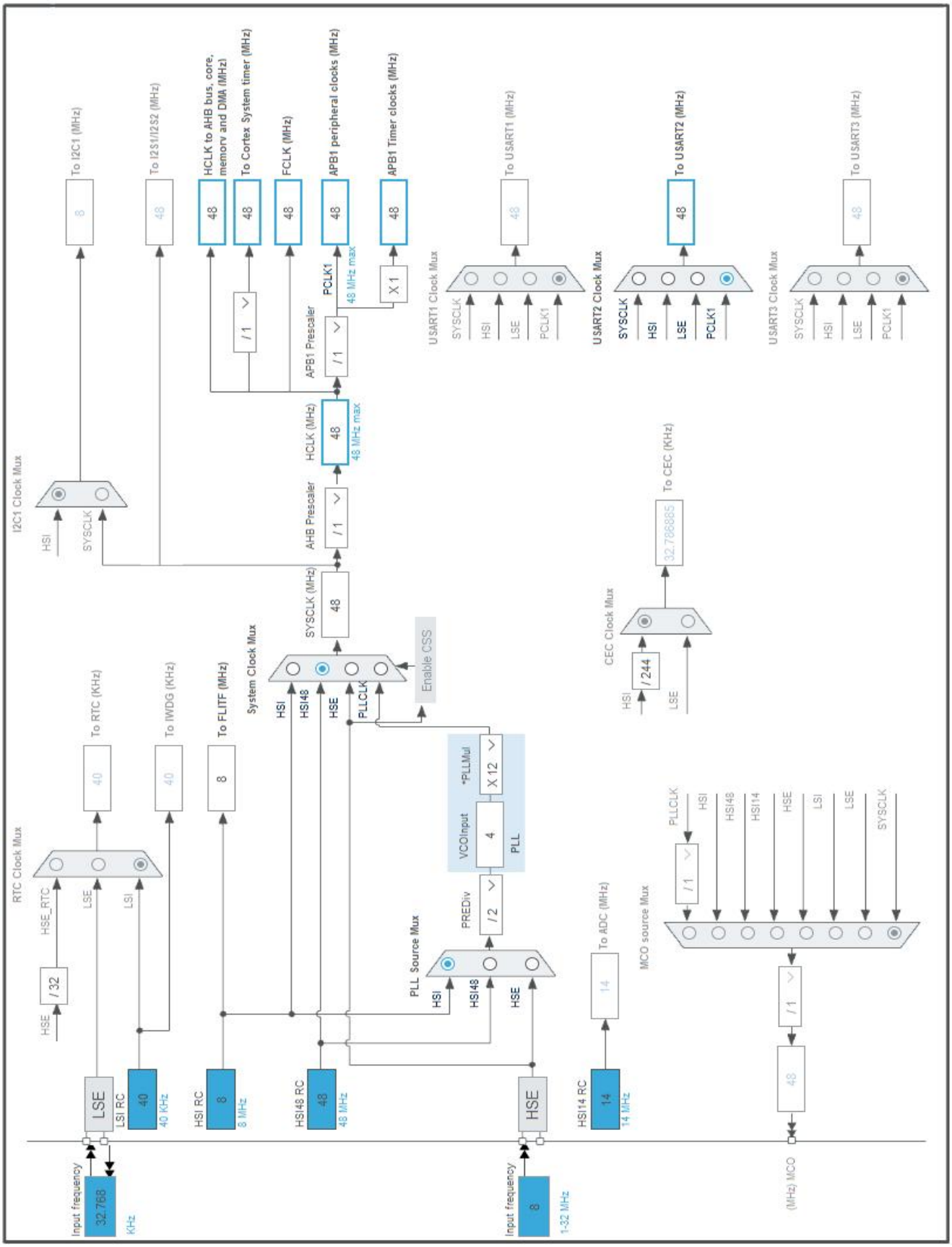


3. Pins Configuration

Pin Number LQFP64	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
3	PC14OSC32_IN	I/O	RCC_OSC32_IN	
4	PC15OSC32_OUT	I/O	RCC_OSC32_OUT	
5	PF0-OSC_IN	I/O	RCC_OSC_IN	
7	NRST	Reset		
12	VSSA	Power		
13	VDDA	Power		
14	PA0	I/O	GPIO_EXTI0	
15	PA1 *	I/O	GPIO_Output	
16	PA2	I/O	USART2_TX	
17	PA3	I/O	USART2_RX	
18	VSS	Power		
19	VDD	Power		
22	PA6	I/O	SPI1_MISO	
23	PA7	I/O	SPI1_MOSI	
31	VSS	Power		
32	VDD	Power		
41	PA8 *	I/O	GPIO_Output	
46	PA13	I/O	SYS_SWDIO	
47	VSS	Power		
48	VDDIO2	Power		
49	PA14	I/O	SYS_SWCLK	
55	PB3	I/O	SPI1_SCK	
63	VSS	Power		
64	VDD	Power		

* The pin is affected with an I/O function

4. Clock Tree Configuration



5. Software Project

5.1. Project Settings

Name	Value
Project Name	0_GETTING_STARTED
Project Folder	D:\UBUNTU-VBOX-SHARED-
Toolchain / IDE	EWARM V8.32
Firmware Package Name and Version	STM32Cube FW_F0 V1.11.2
Application Structure	Advanced
Generate Under Root	No
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No
Enable Full Assert	No

5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	MX_GPIO_Init	GPIO
2	SystemClock_Config	RCC
3	MX_USART2_UART_Init	USART2

6. Power Consumption Calculator report

6.1. Microcontroller Selection

Series	STM32F0
Line	STM32F0x1
MCU	STM32F091RCTx
Datasheet	DS10312_Rev4

6.2. Parameter Selection

Temperature	25
Vdd	3.6

6.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

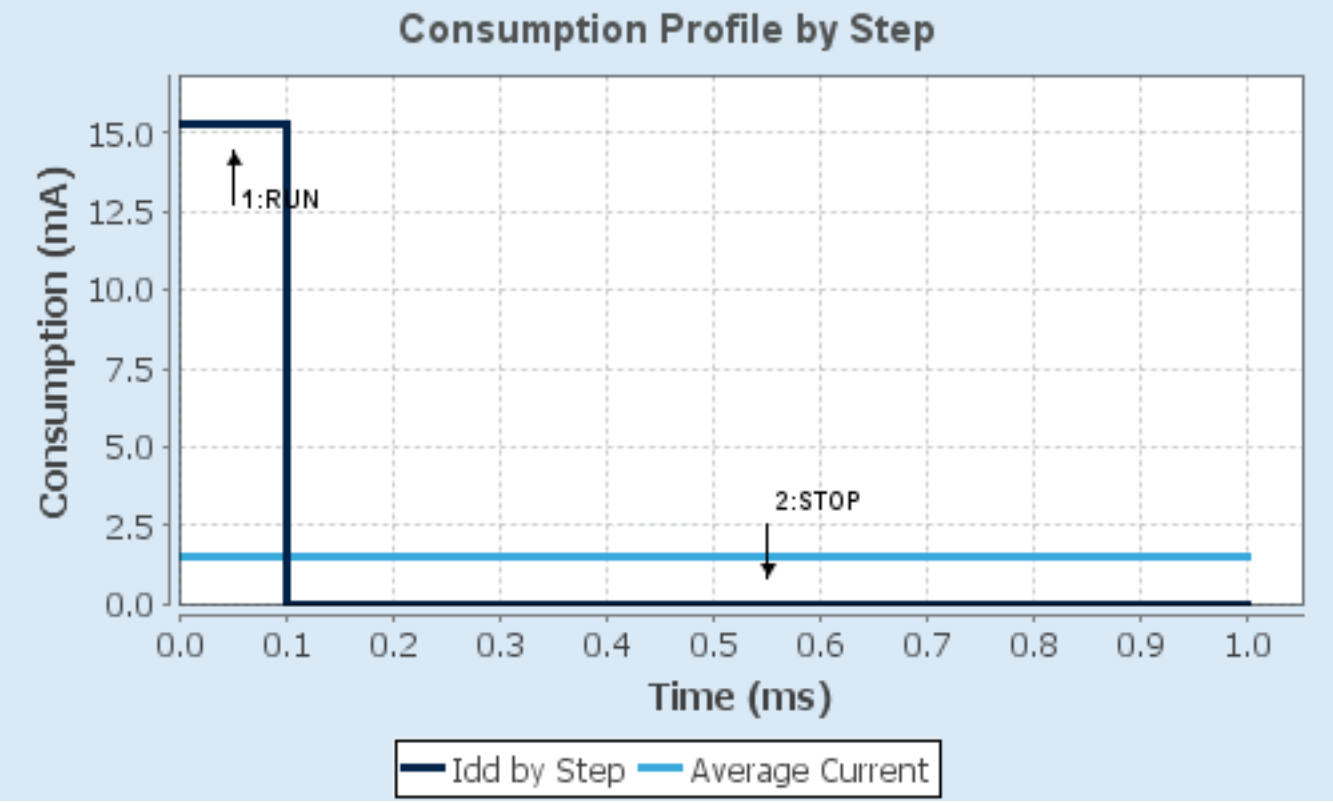
6.4. Sequence

Step	Step1	Step2
Mode	RUN	STOP
Vdd	3.6	3.6
Voltage Source	Battery	Battery
Range	No Scale	No Scale
Fetch Type	FLASH	n/a
CPU Frequency	48 MHz	0 Hz
Clock Configuration	HSE PLL	Regulator_LP
Clock Source Frequency	8 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	15.27 mA	7 µA
Duration	0.1 ms	0.9 ms
DMIPS	0.0	0.0
Ta Max	102.58	105
Category	In DS Table	In DS Table

6.5. Results

Sequence Time	1 ms	Average Current	1.53 mA
Battery Life	3 months, 22 hours	Average DMIPS	0.0 DMIPS

6.6. Chart


Consumption Profile by Step
Consumption (mA)
15.0
12.5
10.0
7.5
5.0
2.5
0.0
1:RUN
2:STOP
0.0
0.1
0.2
0.3
0.4
0.5
0.6
0.7
0.8
0.9
1.0
Time (ms)
Idd by Step
Average Current

7. Peripherals and Middlewares Configuration

7.1. RCC

High Speed Clock (HSE): BYPASS Clock Source

Low Speed Clock (LSE) : Crystal/Ceramic Resonator

7.1.1. Parameter Settings:

System Parameters:

VDD voltage (V)	3.3
Prefetch Buffer	Enabled
Flash Latency(WS)	1 WS (2 CPU cycle)

RCC Parameters:

HSE Startup Timout Value (ms)	100
LSE Startup Timout Value (ms)	5000

7.2. SPI1

Mode: Full-Duplex Master

7.2.1. Parameter Settings:

Basic Parameters:

Frame Format	Motorola
Data Size	4 Bits
First Bit	MSB First

Clock Parameters:

Prescaler (for Baud Rate)	**16 ***
Baud Rate	**3.0 MBits/s ***
Clock Polarity (CPOL)	Low
Clock Phase (CPHA)	1 Edge

Advanced Parameters:

CRC Calculation	Disabled
NSSP Mode	Enabled
NSS Signal Type	Software

7.3. SYS

mode: Debug Serial Wire

Timebase Source: SysTick

7.4. USART2

Mode: Asynchronous

7.4.1. Parameter Settings:

Basic Parameters:

Baud Rate	115200
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1

Advanced Parameters:

Data Direction	Receive and Transmit
Over Sampling	16 Samples
Single Sample	Disable

Advanced Features:

Auto Baudrate	Disable
TX Pin Active Level Inversion	Disable
RX Pin Active Level Inversion	Disable
Data Inversion	Disable
TX and RX Pins Swapping	Disable
Overrun	Enable
DMA on RX Error	Enable
MSB First	Disable

7.5. STMicroelectronics.X-CUBE-BLE1.6.1.0

mode: WirelessJjBlueNRGAaMS

7.5.1. Parameter Settings:

Log & Debug:

BLE1_DEBUG	No debug message (0)
PRINT_CSV_FORMAT	CSV format message print disabled (0)

HCI Basic Parameters:

HCI_READ_PACKET_SIZE	128 Bytes reserved for HCI Read Packet
HCI_MAX_PAYLOAD_SIZE	128 Bytes reserved for HCI Max Payload

Connection Parameters (for expert users):

Scan Interval (SCAN_P)	16384
Scan Window (SCAN_L)	16384

Supervision Timeout (SUPERV_TIMEOUT)	60
Min Connection Period (CONN_P1)	40
Max Connection Period (CONN_P2)	40
Min Connection Length (CONN_L1)	2000
Max Connection Length (CONN_L2)	2000
Advertising Type (ADV_DATA_TYPE)	Connectable Undirected Advertising (ADV_IND)
Min Advertising Interval (ADV_INTERV_MIN)	2048
Max Advertising Interval (ADV_INTERV_MAX)	4096
Min Connection Event Interval (L2CAP_INTERV_MIN)	9
Max Connection Event Interval (L2CAP_INTERV_MAX)	20
Timeout Multiplier (L2CAP_TIMEOUT_MULTIPLIER)	600

7.5.2. Platform Settings:

Exti Line	PA0
BUS IO driver	SPI1
CS Line	PA1
Reset Line	PA8

*** User modified value**

8. System Configuration

8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
RCC	PC14OSC32_IN	RCC_OSC32_IN	n/a	n/a	n/a	
	PC15OSC32_OUT	RCC_OSC32_OUT	n/a	n/a	n/a	
	PF0-OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
SPI1	PA6	SPI1_MISO	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	
	PA7	SPI1_MOSI	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	
	PB3	SPI1_SCK	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	
SYS	PA13	SYS_SWDIO	n/a	n/a	n/a	
	PA14	SYS_SWCLK	n/a	n/a	n/a	
USART2	PA2	USART2_TX	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	
	PA3	USART2_RX	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	
GPIO	PA0	GPIO_EXTI0	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	
	PA1	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PA8	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	

8.2. DMA configuration

nothing configured in DMA service

8.3. NVIC configuration

8.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
System service call via SWI instruction	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
EXTI line 0 and 1 interrupts	true	0	0
PVD and VDDIO2 supply comparator interrupts through EXTI lines 16 and 31	unused		
Flash global interrupt	unused		
RCC and CRS global interrupts	unused		
SPI1 global interrupt	unused		
USART2 global interrupt / USART2 wake-up interrupt through EXTI line 26	unused		

8.3.2. NVIC Code generation

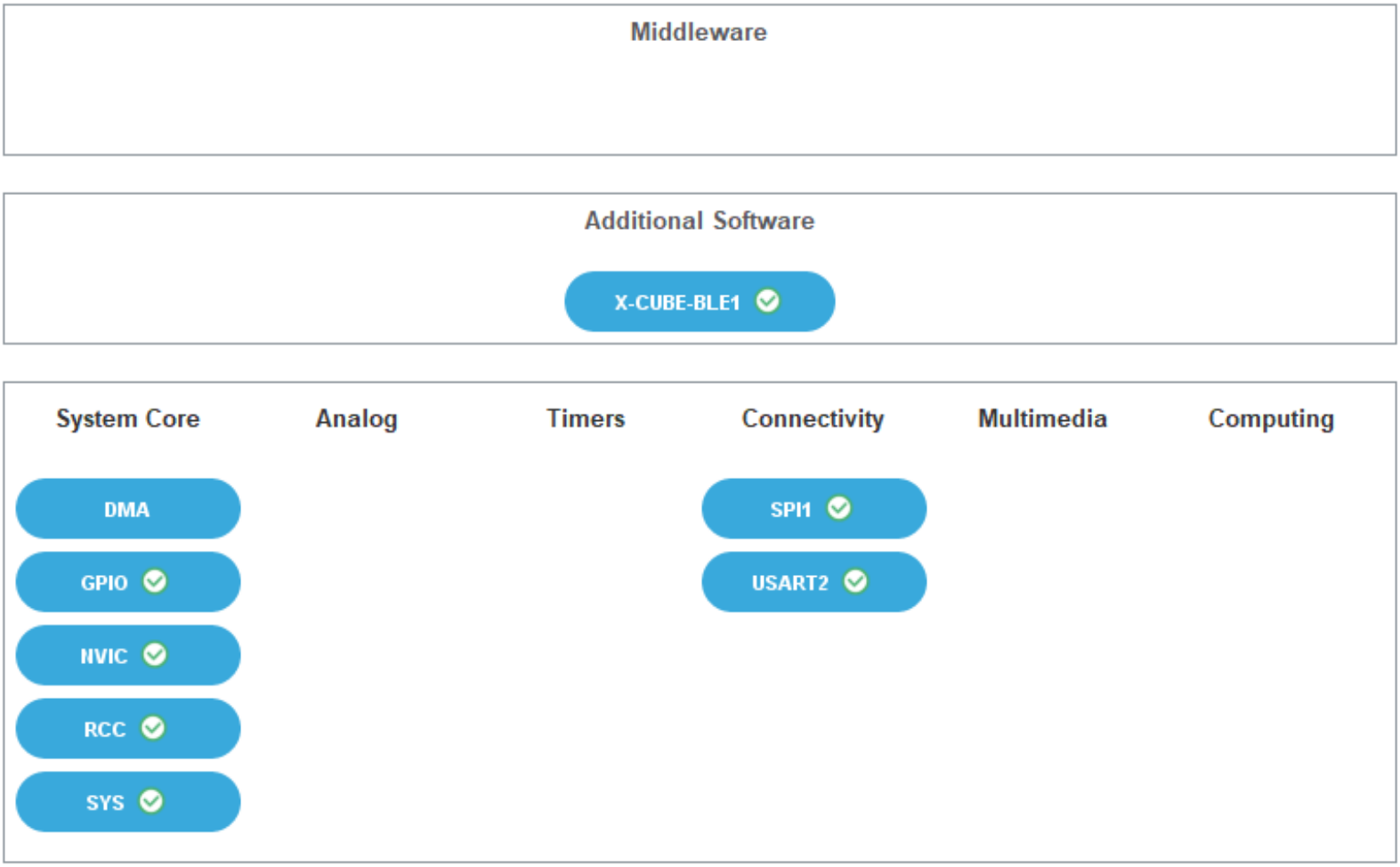
Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
System service call via SWI instruction	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true
EXTI line 0 and 1 interrupts	false	true	true

*** User modified value**

9. System Views

9.1. Category view

9.1.1. Current



10. Software Pack Report

10.1. Software Pack selected

Vendor	Name	Version	Component
STMicroelectronics	X-CUBE-BLE1	6.1.0	Class : Wireless Group : BlueNRG-MS SubGroup : Controller Version : 5.1.0 Class : Wireless Group : BlueNRG-MS SubGroup : HCI_TL Variant : Basic Version : 5.1.0 Class : Wireless Group : BlueNRG-MS SubGroup : HCI_TL_INTERFACE Variant : UserBoard Version : 5.1.0 Class : Wireless Group : BlueNRG-MS SubGroup : Utils Version : 5.1.0

11. Docs & Resources

Type	Link
Datasheet	http://www.st.com/resource/en/datasheet/DM00115237.pdf
Reference manual	http://www.st.com/resource/en/reference_manual/DM00031936.pdf
Programming manual	http://www.st.com/resource/en/programming_manual/DM00051352.pdf
Errata sheet	http://www.st.com/resource/en/errata_sheet/DM00134745.pdf
Application note	http://www.st.com/resource/en/application_note/CD00160362.pdf
Application note	http://www.st.com/resource/en/application_note/CD00167594.pdf
Application note	http://www.st.com/resource/en/application_note/CD00211314.pdf
Application note	http://www.st.com/resource/en/application_note/CD00249778.pdf
Application note	http://www.st.com/resource/en/application_note/CD00259245.pdf
Application note	http://www.st.com/resource/en/application_note/CD00264342.pdf
Application note	http://www.st.com/resource/en/application_note/CD00264379.pdf
Application note	http://www.st.com/resource/en/application_note/DM00024853.pdf
Application note	http://www.st.com/resource/en/application_note/DM00025071.pdf
Application note	http://www.st.com/resource/en/application_note/DM00042534.pdf
Application note	http://www.st.com/resource/en/application_note/DM00051986.pdf
Application note	http://www.st.com/resource/en/application_note/DM00052530.pdf
Application note	http://www.st.com/resource/en/application_note/DM00053084.pdf
Application note	http://www.st.com/resource/en/application_note/DM00072315.pdf
Application note	http://www.st.com/resource/en/application_note/DM00073742.pdf
Application note	http://www.st.com/resource/en/application_note/DM00080497.pdf
Application note	http://www.st.com/resource/en/application_note/DM00085385.pdf
Application note	http://www.st.com/resource/en/application_note/DM00087593.pdf
Application note	http://www.st.com/resource/en/application_note/DM00129215.pdf
Application note	http://www.st.com/resource/en/application_note/DM00145318.pdf
Application note	http://www.st.com/resource/en/application_note/DM00160482.pdf

Application note	http://www.st.com/resource/en/application_note/DM00210690.pdf
Application note	http://www.st.com/resource/en/application_note/DM00220769.pdf
Application note	http://www.st.com/resource/en/application_note/DM00257177.pdf
Application note	http://www.st.com/resource/en/application_note/DM00226326.pdf
Application note	http://www.st.com/resource/en/application_note/DM00236305.pdf
Application note	http://www.st.com/resource/en/application_note/DM00188145.pdf
Application note	http://www.st.com/resource/en/application_note/DM00327191.pdf
Application note	http://www.st.com/resource/en/application_note/DM00355687.pdf
Application note	http://www.st.com/resource/en/application_note/DM00354244.pdf
Application note	http://www.st.com/resource/en/application_note/DM00315319.pdf
Application note	http://www.st.com/resource/en/application_note/DM00380469.pdf
Application note	http://www.st.com/resource/en/application_note/DM00395696.pdf
Application note	http://www.st.com/resource/en/application_note/DM00445657.pdf
Application note	http://www.st.com/resource/en/application_note/DM00493651.pdf
Application note	http://www.st.com/resource/en/application_note/DM00483659.pdf
Application note	http://www.st.com/resource/en/application_note/DM00536349.pdf