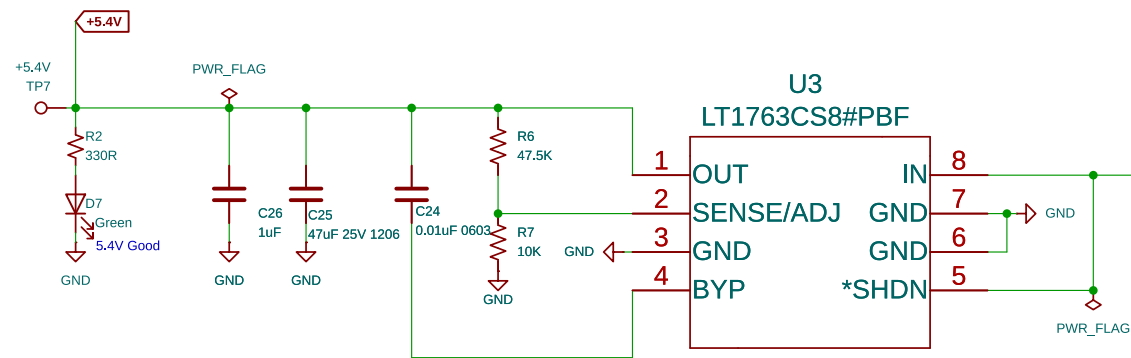
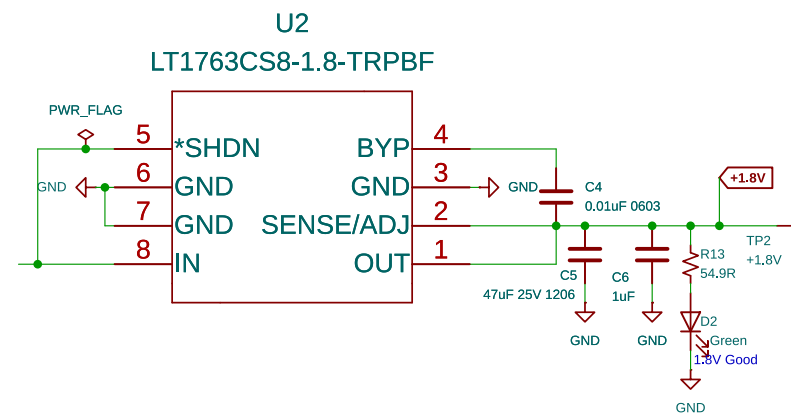


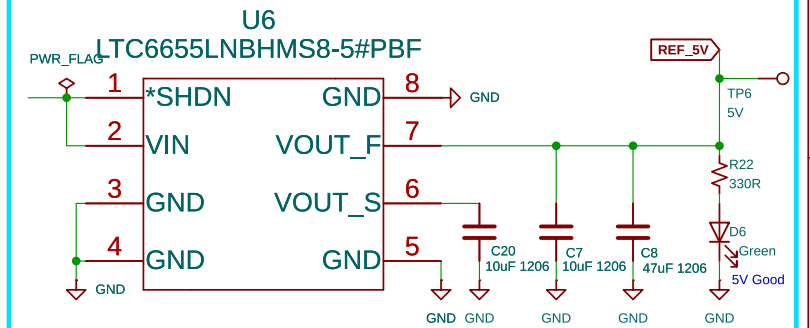
5.4V Analog Positive Rail



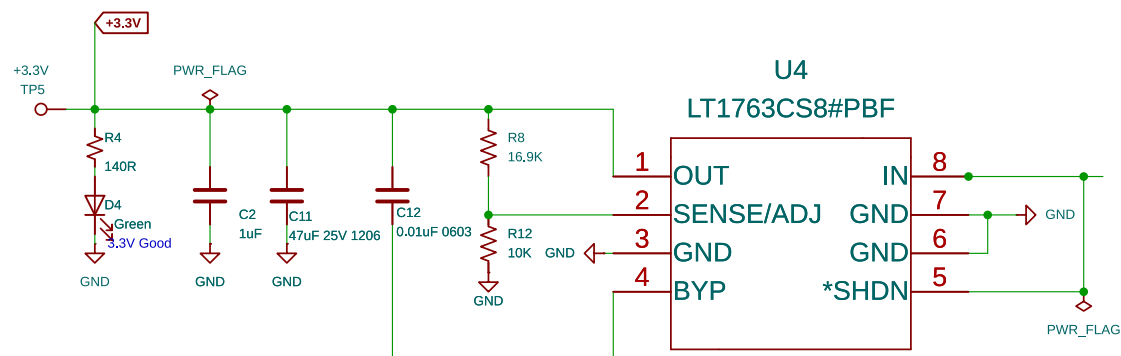
ADC Digital Rail



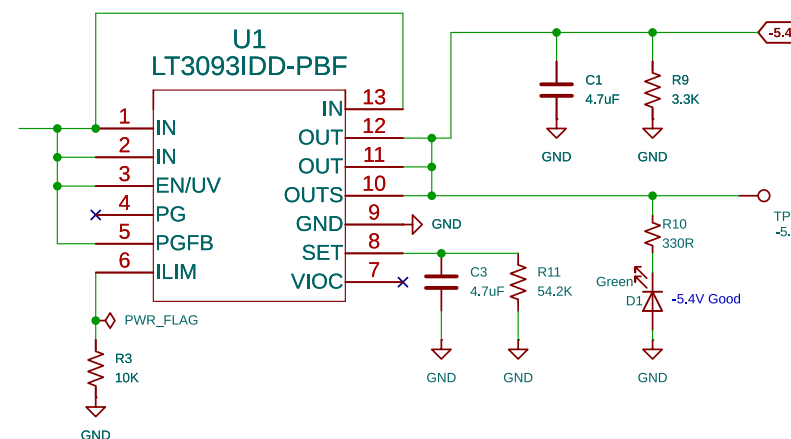
+5V Precision Reference



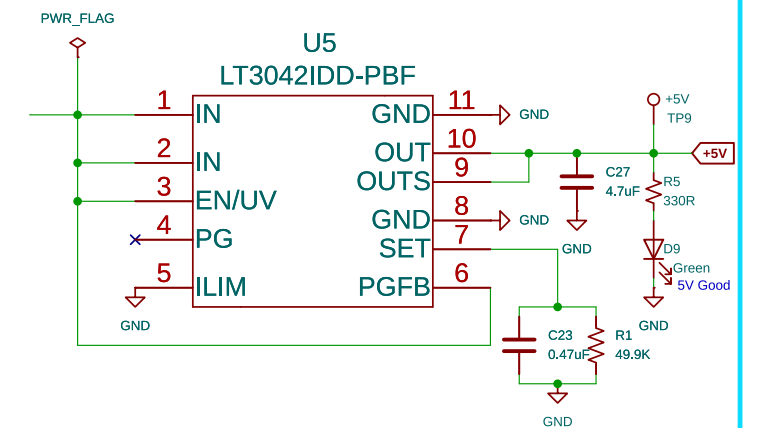
MCU / FPGA Digital Rail



- 5.4V Analog Negative Rail



AD4630 VDD_5V pin



N-Well Labs

Sheet: /Power Management/
File: Power_Management_sch.kicad_sch

Title: NanoPulse - Power Management

Size: A3
KiCad E.D.A. 9.0.3

Date: 2025-07-10

Rev: Rev 1
Id: 2/10