



**DHARMSINH DESAI UNIVERSITY, NADIAD**  
**FACULTY OF TECHNOLOGY**  
**B.TECH. SEMESTER VI [INFORMATION TECHNOLOGY]**  
**SUBJECT: (IT 608) LANGUAGE TRANSLATOR**

<b>Examination</b>	<b>: Third Sessional</b>	<b>Seat No.</b>	<b>:</b>
<b>Date</b>	<b>: 31/03/2018</b>	<b>Day</b>	<b>: Saturday</b>
<b>Time</b>	<b>: 12:00 to 1:15</b>	<b>Max. Marks</b>	<b>: 36</b>

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**INSTRUCTIONS:**

1. Figures to the right indicate maximum marks for that question.
  2. The symbols used carry their usual meanings.
  3. Assume suitable data, if required & mention them clearly.
  4. Draw neat sketches wherever necessary.
  5. Here | is rule separator and ^ stands for NULL.
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**Q.1 Do as directed.**

- (a) If you were to design a compiler, would you provide optimization phase? Why? [2]  
(only key points require)
- (b) Generate intermediate code for given source code. [2]  
 $x = a[i][j] + b[2];$
- (c) Represent three address code using Quadruples and triples for given on [4]  
expression.  
 $a = a + a * (b - c);$
- (d) Define copy & restore and call by name using one unique example output. (No [4]  
marks for theory explanation)

**Q.2 Attempt the following questions.** [12]

- (a) Consider following program written in “lexical scoped non-nested procedural [6]  
language”  

```
int f(int n){
    static int r = 0;
    if (n<=0) return 1;
    if(n>3) {
        r = n;
        return f(n-2)+2; }
    return f(n-1)+r; }
```

What is value of f(5)? Explain your answer showing the snapshots of memory layout at run-time using Activation tree & Records.
- (b) Compare three different storage allocation with proper example in term of pros [6]  
& cons and language support.

**OR**

- (b) Give syntax directed definition to produce three address code for assignments. [6]  
 $S \rightarrow id := E \quad E \rightarrow E1 + E2 \quad E \rightarrow E1 * E2 \quad E \rightarrow -E1 \quad E \rightarrow (E1) \quad E \rightarrow id$

**Q.3 (a) Give assembly code generated by a simple code generator for following [6]  
statement**

$x = (a+b) - ((c+d)-e)$ . Assume the target machine has 2 registers. Also list the issues in the design of code generator

- (b) Generate intermediate code representation for following program fragment. [6]

```
#include <stdio.h>
int gcd(int a, int b) {
    if (a == 0 || b == 0)
        return 0;
    if (a == b)
        return a;
    if (a > b)
        return gcd(a-b, b);
    return gcd(a, b-a); }

int main() {
    int a = 98, b = 56;
    printf("GCD of %d and %d is %d ", a, b, gcd(a, b));
    return 0; }
```

**OR**

- Q.3** (a) Explain Given code optimization technique with proper example [6]

1. Common Sub Expression Elimination
2. Loop Fusion
3. Strength Reduction

- (b) Translate the arithmetic expression  $(a + b * c) + d + (a + b * c) - d + e$  into: [6]
1. syntax tree
  2. DAG (Directed Acyclic Graph) representation
  3. Compare DAG and syntax tree.