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Activity



Branches

Tags

Public repository

| | | | |
|----------------------------------------------------------------------------------------------------------|------------------------------------|-------------------------|-------------------------------------------------------------------------------------|
|  HimaneeshYadala | Update README.md | 50f6792 · 10 months ago |  |
|  Logisim | Delete Logisim/Reception_Desk.circ | 10 months ago | |
|  Snapshots | Uploaded demo video and change... | last year | |
|  Verilog | Uploaded demo video and change... | last year | |
|  Videos | Delete Videos/logisim demo.mp4 | 10 months ago | |
|  .gitignore | Created gitignore | last year | |
|  README.md | Update README.md | 10 months ago | |

README



Automated-Reception-Desk

Our project aims to design an automated reception desk for hospitals. The system has an easy-to-use interface allowing patients to check in and make appointments with doctors based on their needs. It can be integrated with the hospital's software and provide real-time updates on doctor availability for a convenient and satisfying experience.

Team Details:

▼ Details

Semester: 3rd sem B.tech CSE
Section: S2

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Abstract:

▼ Details

Background: An automated system in hospitals for managing appointments can improve the efficiency and accuracy of the scheduling process. It can also reduce the workload of receptionists, allowing them to focus on other tasks, such as managing patient information and assisting patients. One of the key benefits of automating the appointment scheduling process is the ability to match patients with the most appropriate doctor based on their medical needs. This ensures that patients receive the appropriate care from specialized practitioners, ultimately leading to better treatment outcomes. Another benefit of an automated system is the ability to notify patients of their appointment details in a timely manner. This contributes to the overall enhancement of the patient experience, as it fosters a sense of transparency and reliability in the healthcare system. This model can be made using just logic gates, Flip Flops and Counters. This simplistic design also makes the model economical.

Motivation: We wanted to combine two different domains Healthcare and Technology and decided to make a project, which can make the experience of patients and hospital staff better in many ways. With our project we aim to address global health challenges by providing innovative solutions to streamline the process.

Working

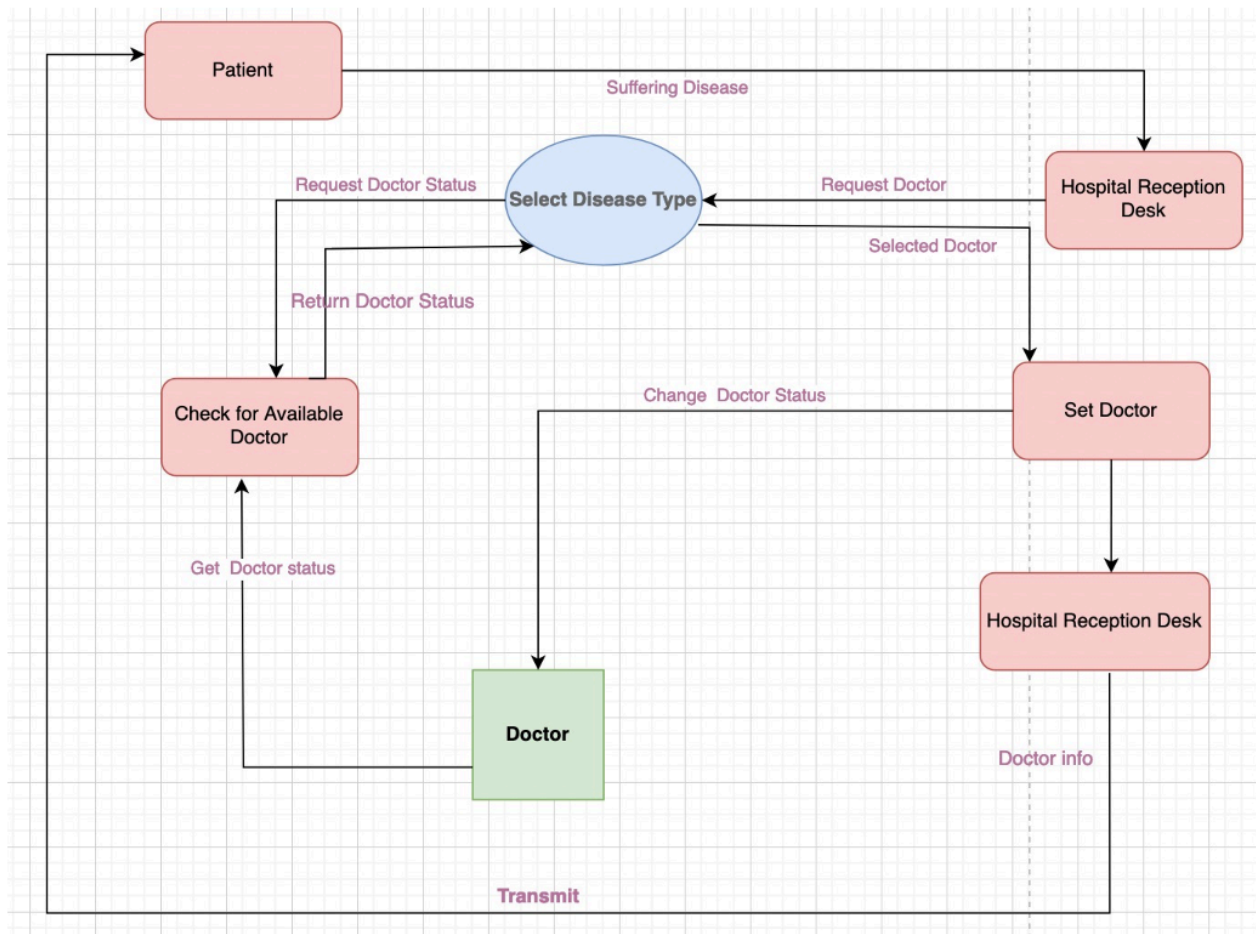
▼ Details

For Query 0, if the input is '0,' the system promptly checks if Doctor A is available. If Doctor A is free, the system assigns the patient to Doctor A and provides a two-bit response to indicate Doctor A's readiness for consultation. However, if Doctor A is currently occupied, the system politely informs the patient about a short wait.

For Query 3, when the input is '3,' the system checks the availability of Doctor B. If Doctor B is ready, the system assigns the patient to Doctor B and provides a two-bit output to confirm Doctor B's readiness for consultation. If Doctor B is not available, the patient is informed about a brief waiting period.

Queries 1 and 2 are slightly different; when the input is '1' or '2,' the system assesses the availability of both Doctor A and Doctor B. If both doctors are free, the system efficiently assigns the patient to the first available doctor and provides a two-bit output specifying which doctor is ready for consultation. If both doctors are currently occupied, the system courteously informs the patient about a short waiting time.

Flowchart:



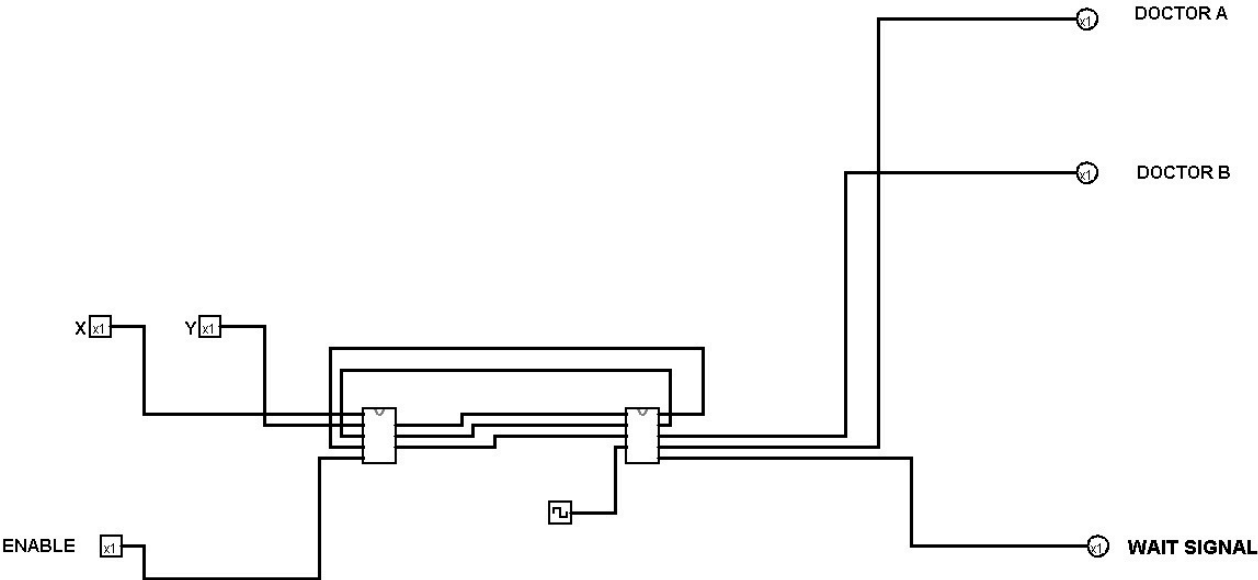
Truth Table:

| A | B | S1(t) | S2(t) | X | Y | S1(t+1) | S2(t+1) |
|---|---|-------|-------|---|---|---------|---------|
| 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 |
| 0 | 0 | 0 | 1 | 1 | 0 | 1 | 1 |
| 0 | 0 | 1 | 0 | 0 | 0 | 1 | 0 |
| 0 | 0 | 1 | 1 | 0 | 0 | 1 | 1 |
| 0 | 1 | 0 | 0 | 1 | 0 | 1 | 0 |
| 0 | 1 | 0 | 1 | 1 | 0 | 1 | 1 |
| 0 | 1 | 1 | 0 | 0 | 1 | 1 | 1 |
| 0 | 1 | 1 | 1 | 0 | 0 | 1 | 1 |
| 1 | 0 | 0 | 0 | 1 | 0 | 1 | 0 |
| 1 | 0 | 0 | 1 | 1 | 0 | 1 | 1 |
| 1 | 0 | 1 | 0 | 0 | 1 | 1 | 1 |
| 1 | 0 | 1 | 1 | 0 | 0 | 1 | 1 |
| 1 | 1 | 0 | 0 | 0 | 1 | 0 | 1 |
| 1 | 1 | 0 | 1 | 0 | 0 | 0 | 1 |
| 1 | 1 | 1 | 0 | 0 | 1 | 1 | 1 |
| 1 | 1 | 1 | 1 | 0 | 0 | 1 | 1 |

Logisim Circuit Diagram:

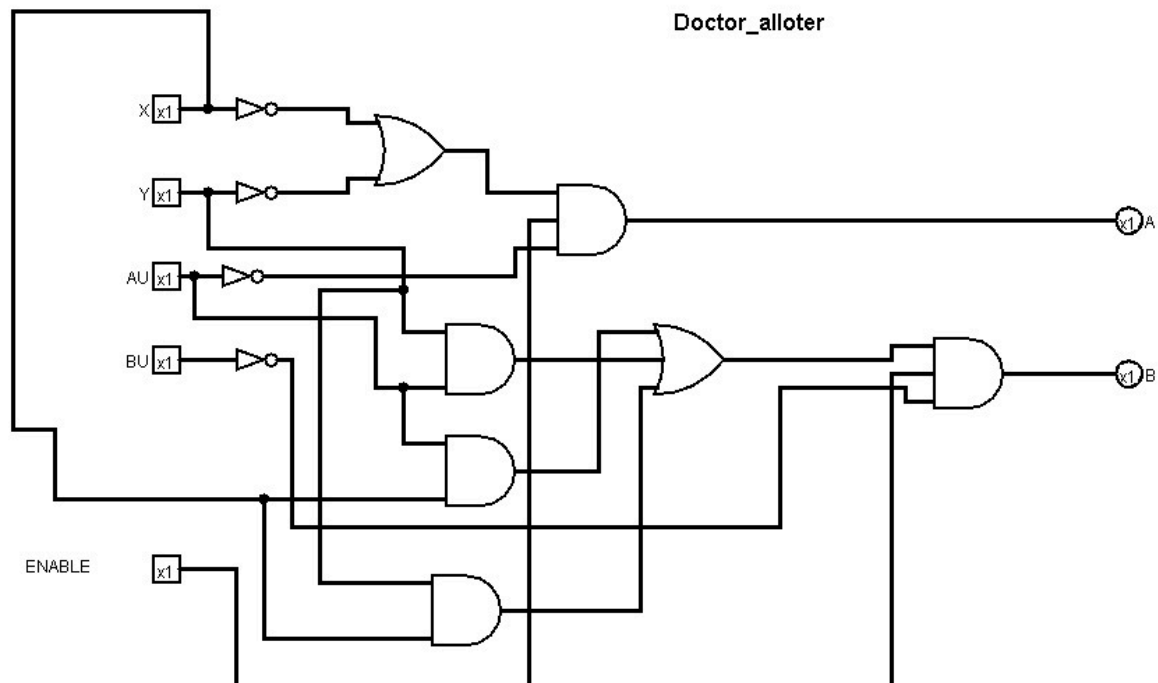
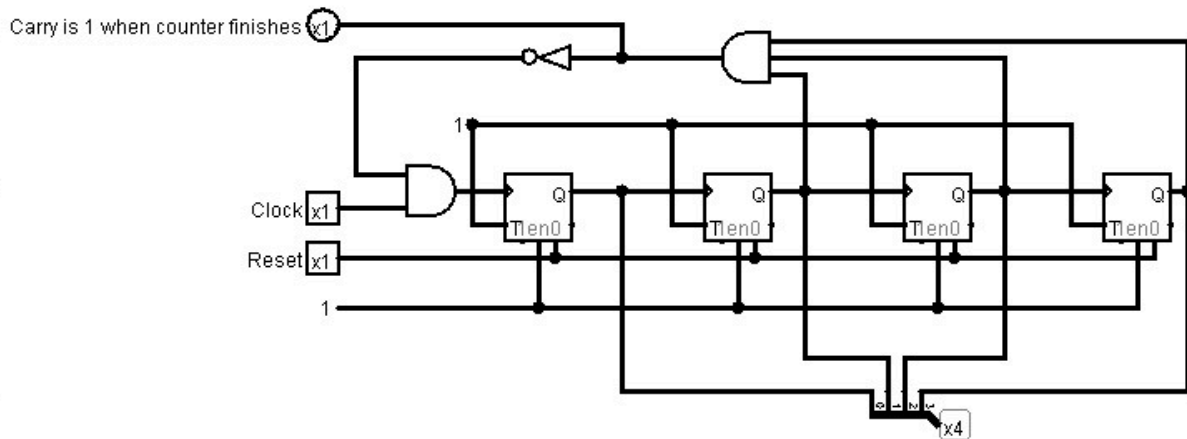
▼ Details

Main Logisim Circuit:



4 bit up counter

A counter circuit that returns 1 and stops counting as soon as it reaches the 0xD



Releases

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Contributors 2



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Languages

● **Verilog** 100.0%