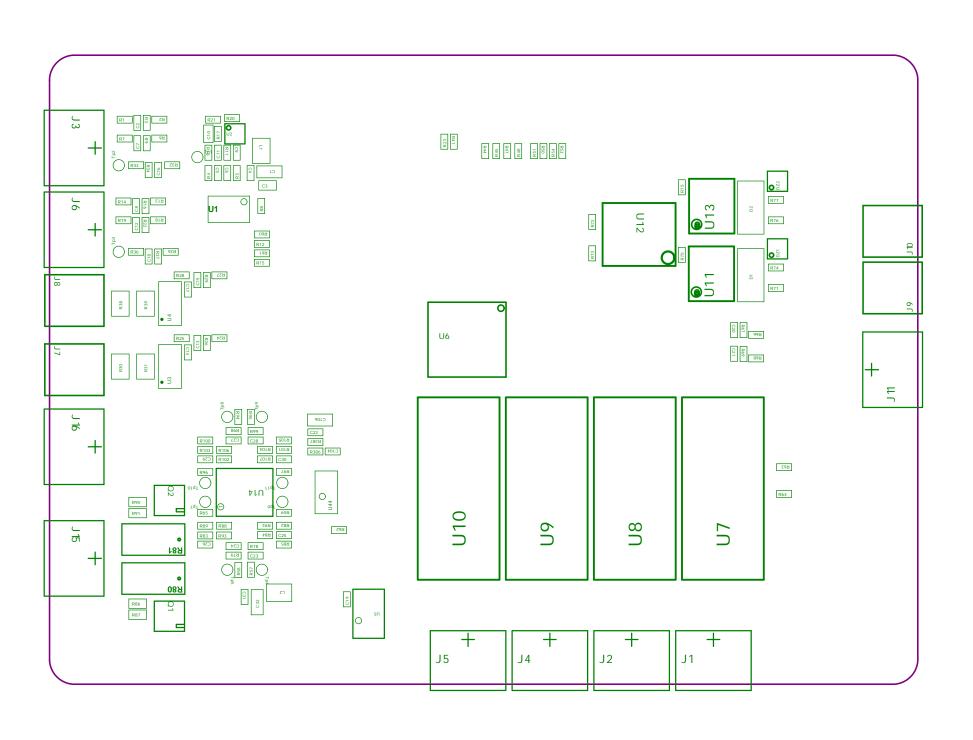


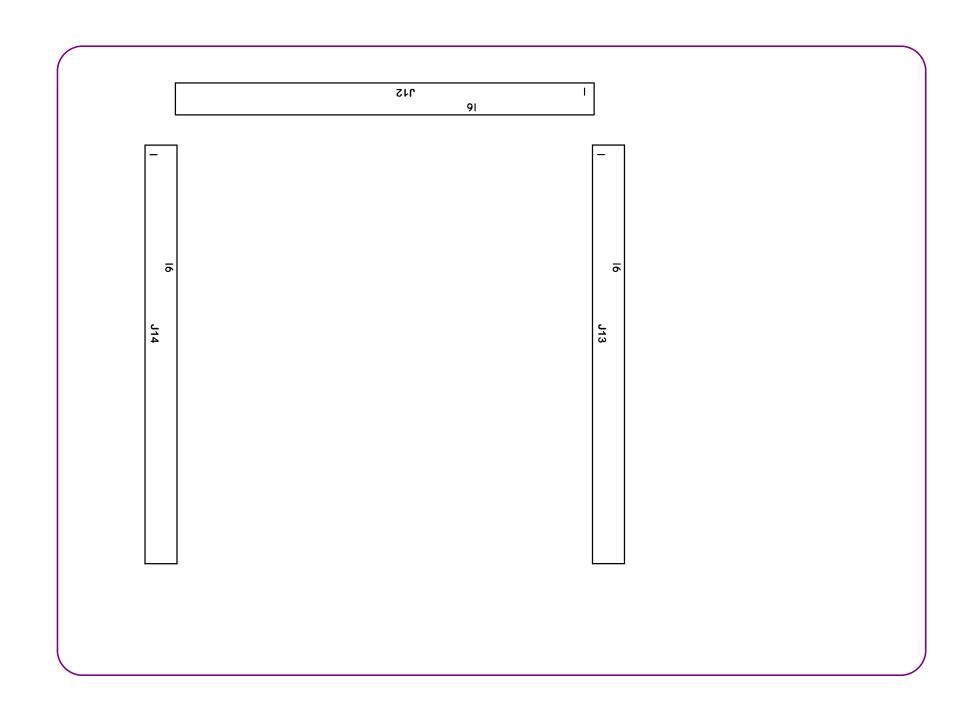
develer
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PCB Not e			
Board Name	IO Shield		
P/N			
Author	Daniele Basile <asterix@develer.co< td=""><td>m&gt;</td></asterix@develer.co<>	m>	
Date	25/08/2019		
Revision	A	А3	



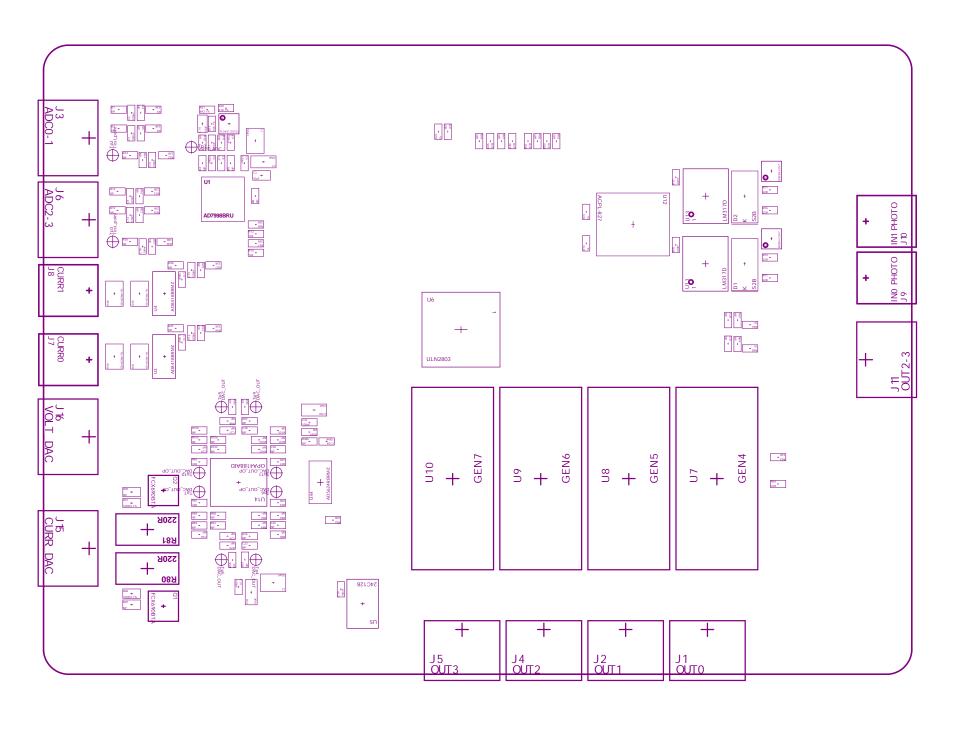


<b>RSB</b> e <b>Mo</b> tye TOP				
Board Name	IO Shield			
P/N	-			
Author	Daniele Basile <asterix@develer.co< td=""><td>m&gt;</td></asterix@develer.co<>	m>		
Date	25/08/2019			
Revision	Α	A3		



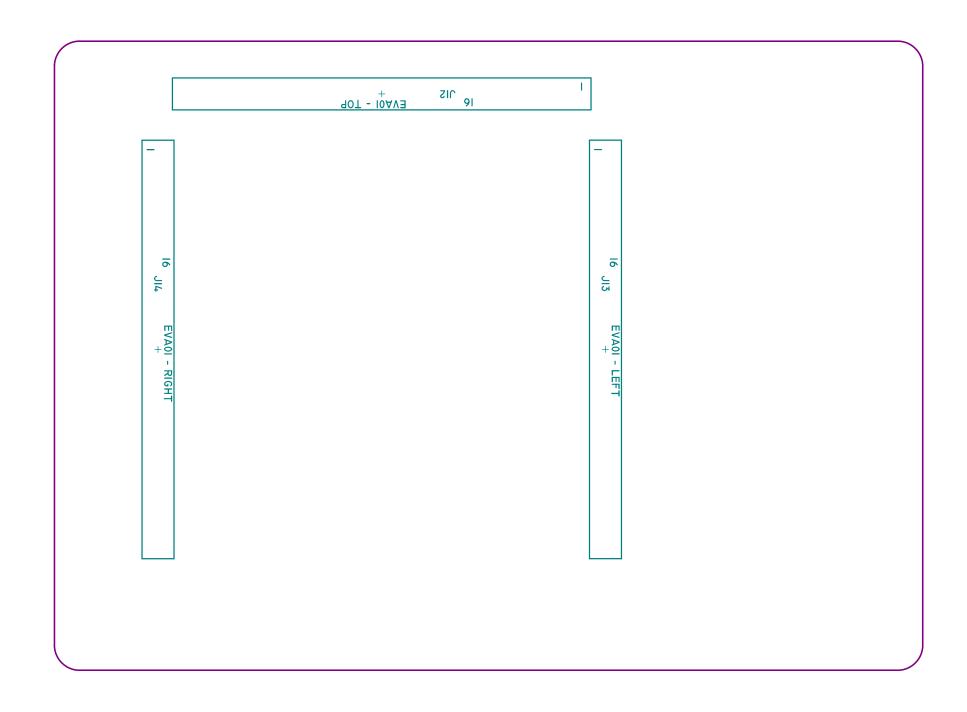


ASSEMBLY BOTTOM				
Board Name	IO Shield			
P/N	_			
Author	Daniele Basile <asterix@develer.com< td=""><td>&gt;</td></asterix@develer.com<>	>		
Date	25/08/2019			
Revision	A	<b>A3</b>		





RGBeNIDDE			
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25/08/2019			
A	А3		
	IO Shield - Daniele Basile <asterix@develer.cc 08="" 2019<="" 25="" td=""></asterix@develer.cc>		





NOTE BOT	TOM	
Board Name	IO Shield	
P/N	_	
Author	Daniele Basile <asterix@develer.com< td=""><td>&gt;</td></asterix@develer.com<>	>
Date	25/08/2019	
Revision	Α	<b>A3</b>

