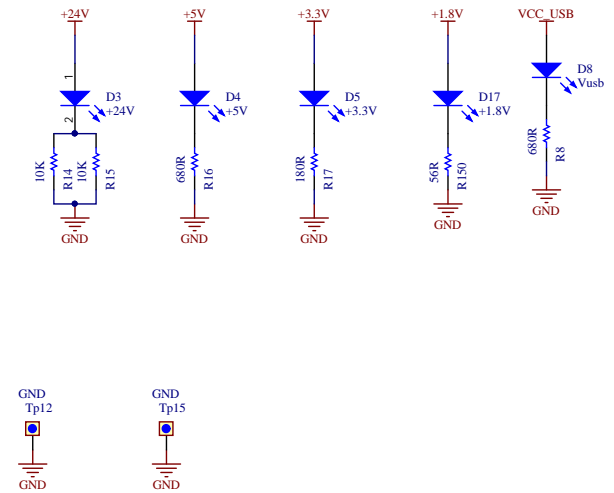
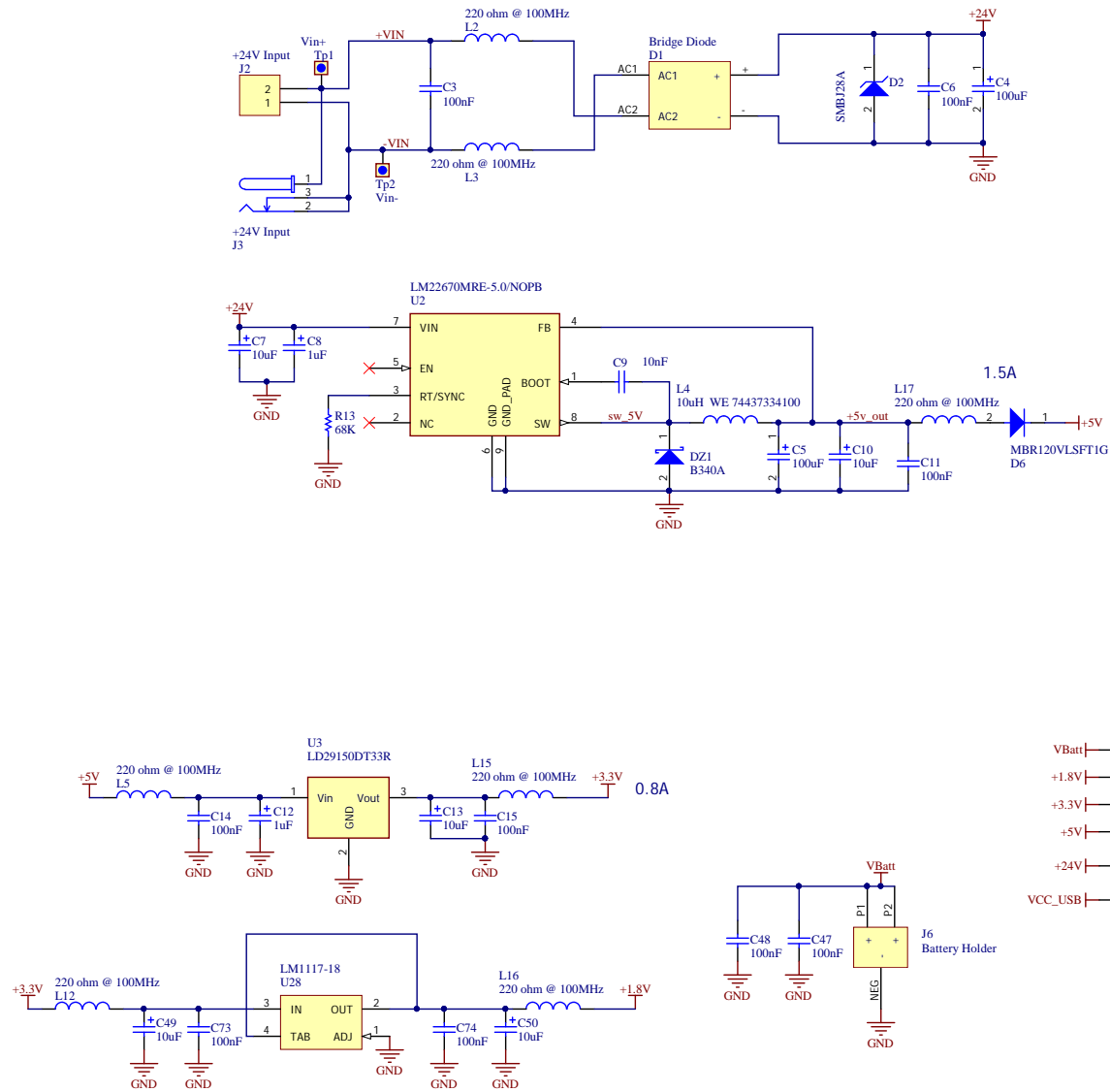




File: sd debug.SchDoc	Date: 09/02/2016	Time: 10:07:50	Sheet 3 of 6
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- VBattp3
- Vbatt
- +1.8Vtp4
- +1.8V
- +3.3Vtp9
- +3.3V
- +5Vtp10
- +5V
- +24Vtp11
- +24V
- VCC\_USBtp16
- Vusb





