```
OPT3101::deviceRegister
+ msb
+ Isb
+ address
+ size
+ OPT3101::deviceRegister()
+ operator=()
+ read()
              +PDN ILLUM DRV
              +sel illum tx0 on
                      tx1
              +TX2 PĪN CONFIG
             +en dyn pd temp
                     sens
                 +pdn global
              +en illum clk
                     gpio
             +i2c num bytes
                     tran1
              +a1 coeff hdr1
                      tx2
              +a0 coeff hdr0
                      tx0
              +tillum calib
                   hdr1 tx2
    OPT3101::registers
    + dig_gpo_sel0
    + dig gpo sel1
    + dig gpo sel2
    + dis ovl gating
    + phase_out
    + phase overflow
    + hdr mode
    + tx channel
    + frame status
    + mod freq
    and 349 more...
```

+ registers()