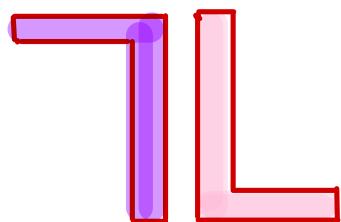


Verifiable Delay Functions

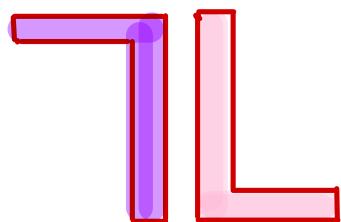
— Archisman Dutta



Verifiable Delay Functions

(or how to harness the power of time)

- Archisman Dutta



Plan for the afternoon

- Timed-release crypto
- Time-lock puzzles
- Verifiable Delay Functions
- Pietrzak's construction

Plan for the afternoon

- Timed-release crypto
- Time-lock puzzles
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Timed-release crypto

“Send information into the future”
(sort of an inverse of Steins;Gate)

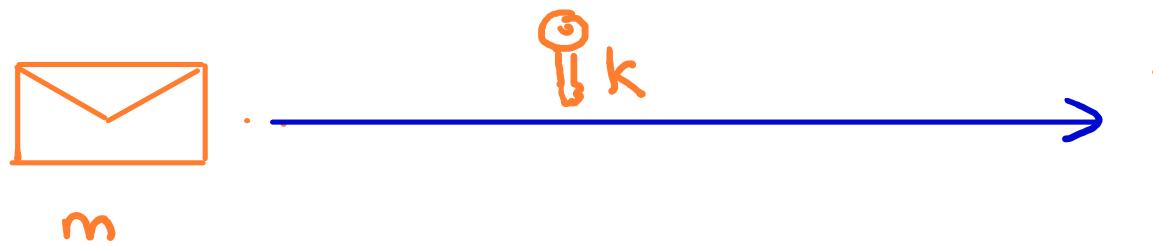
Timed-release crypto



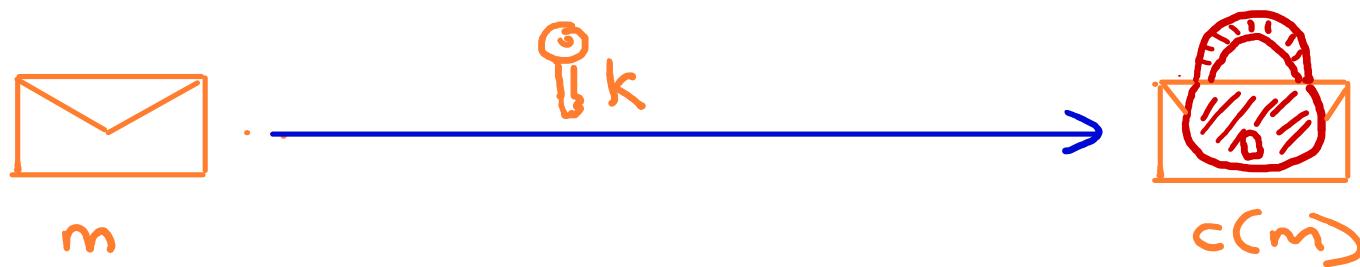
.

m

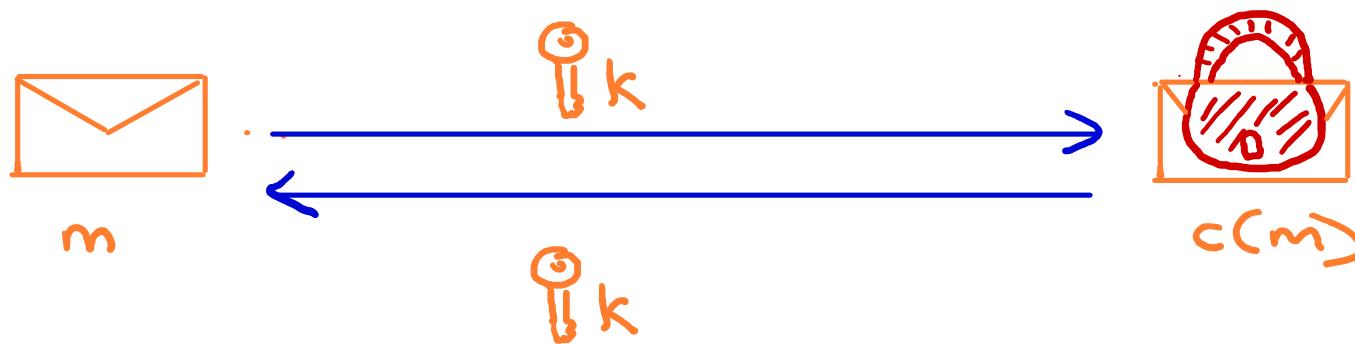
Timed-release crypto



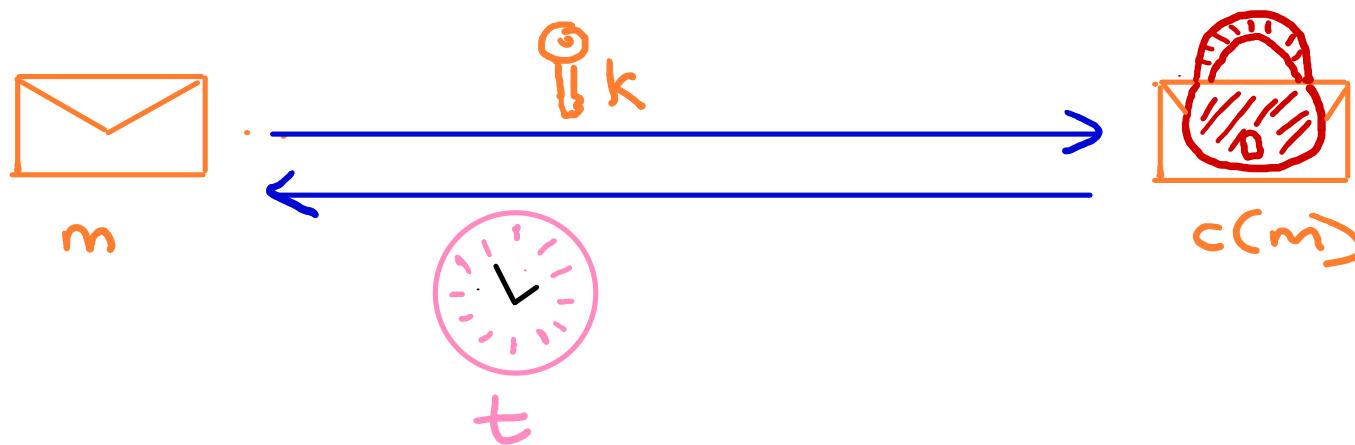
Timed-release crypto



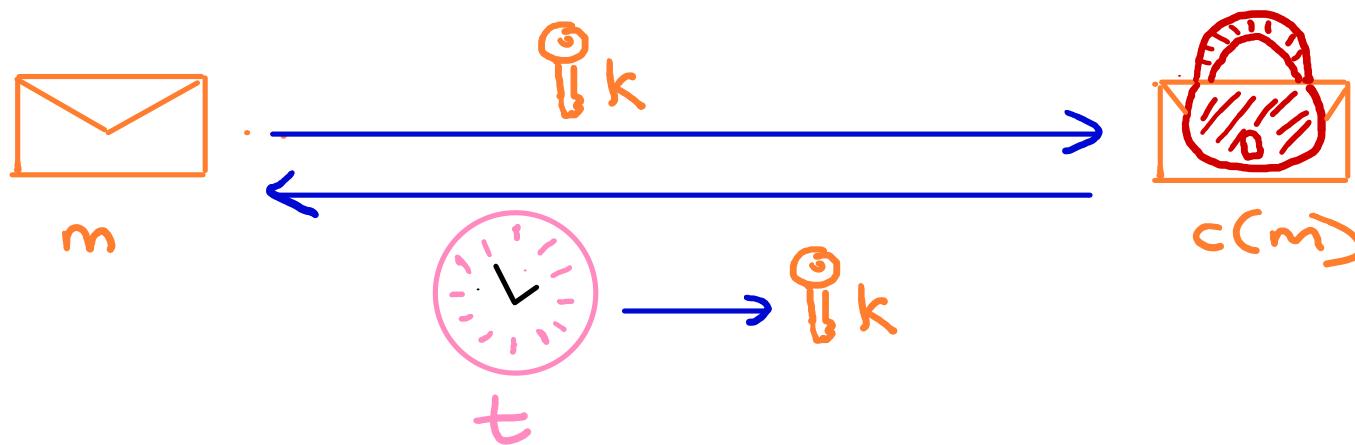
Timed-release crypto



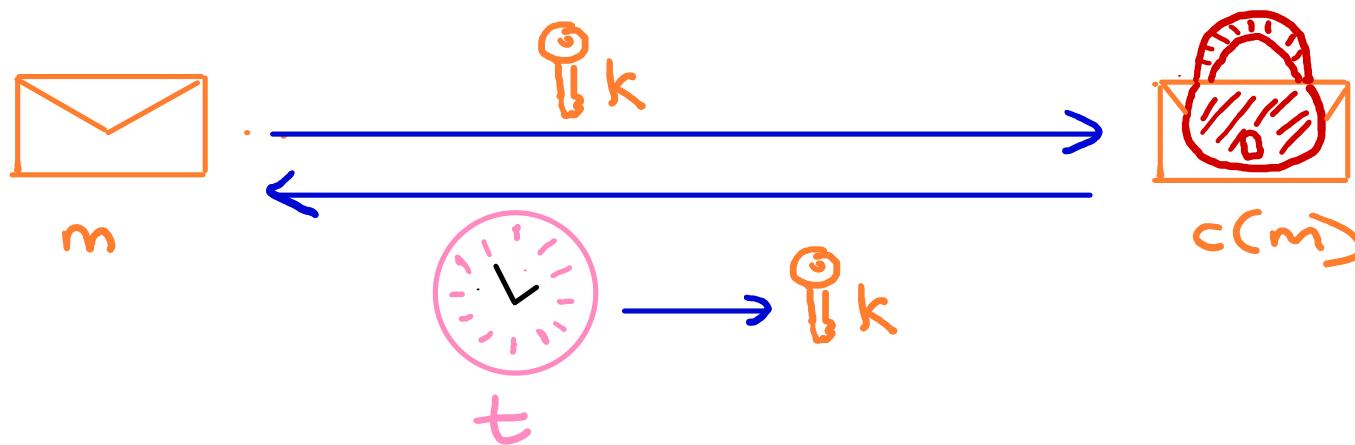
Timed-release crypto



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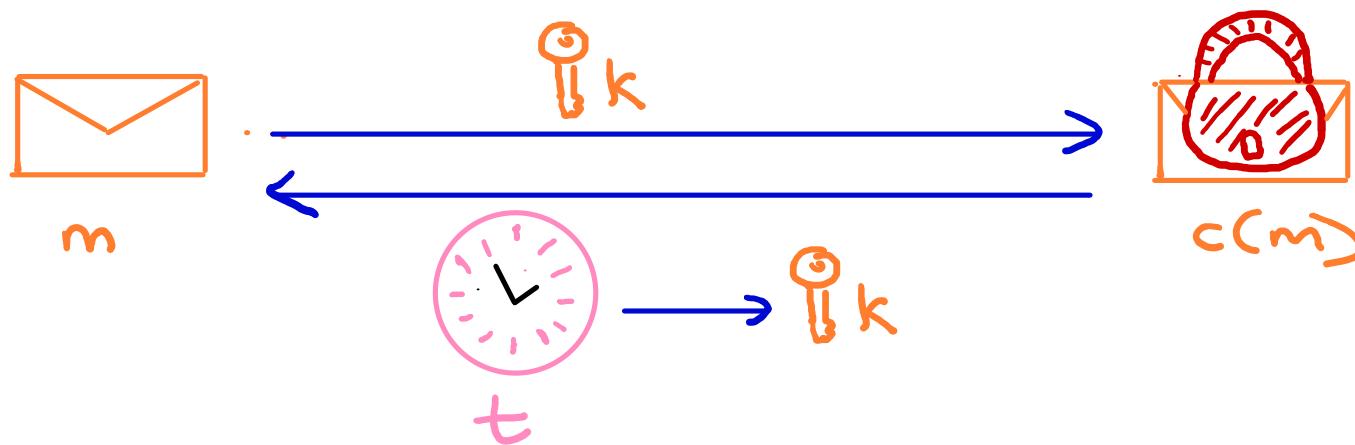


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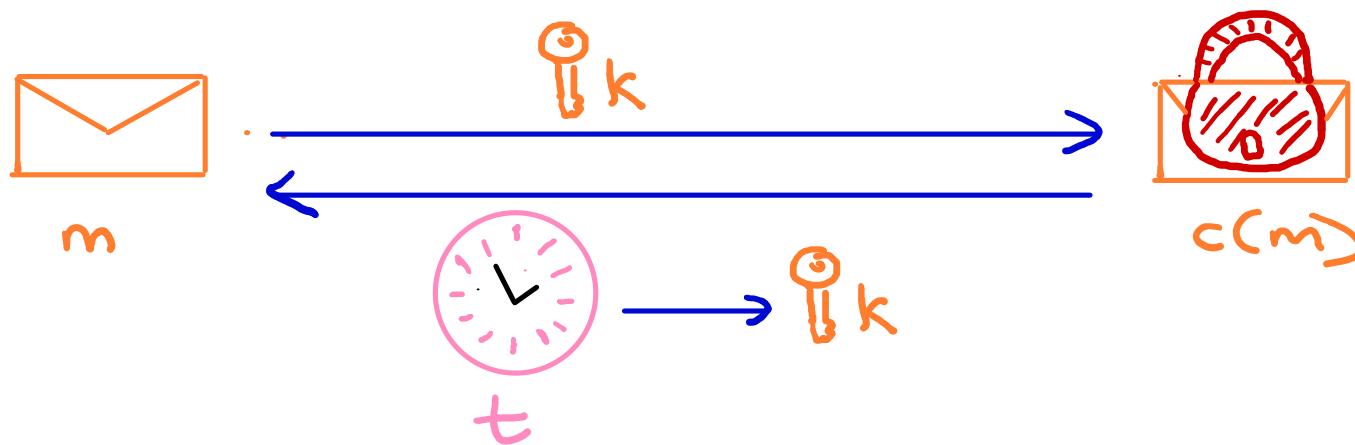
$c(m)$ cannot be decrypted without k

Timed-release crypto



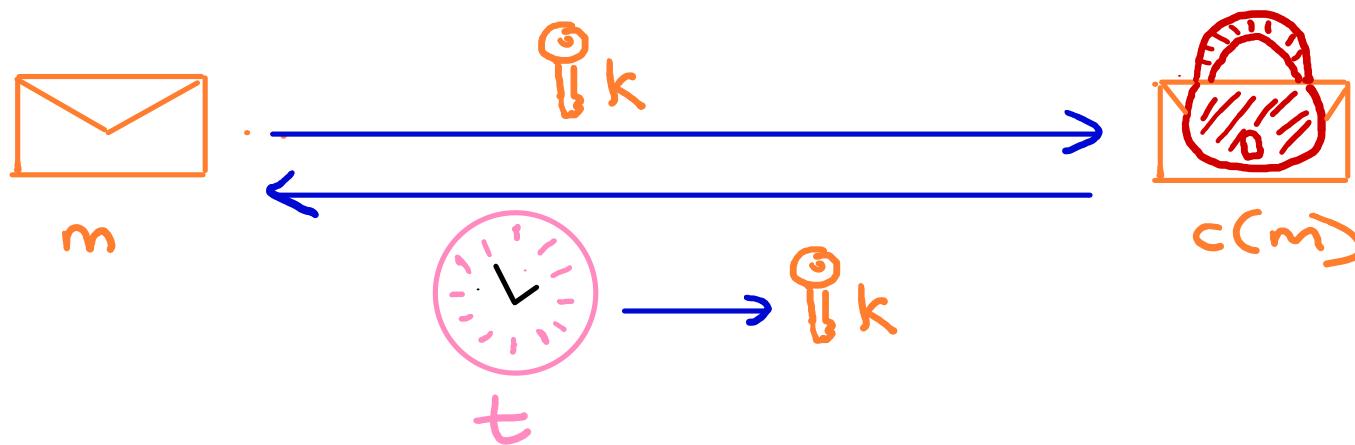
$c(m)$ cannot be decrypted without k
but k is only obtained after time t

Timed-release crypto



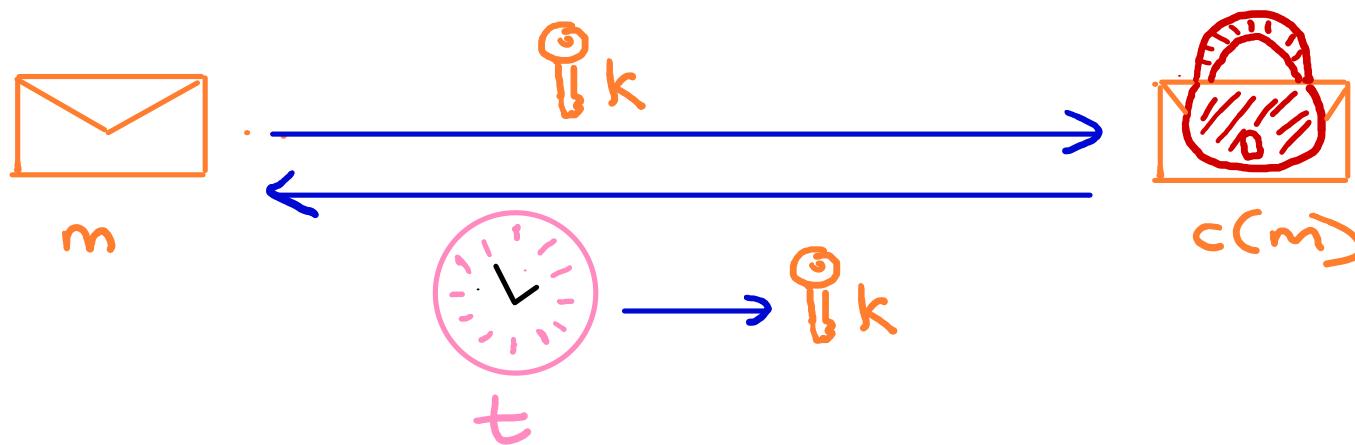
$c(m)$ cannot be decrypted without k
but k is only obtained after time t^*

Timed-release crypto



$c(m)$ cannot be decrypted without k
but k is only obtained after time t^*
 $* t \rightarrow$ wall-clock time (not CPU time)

Timed-release crypto



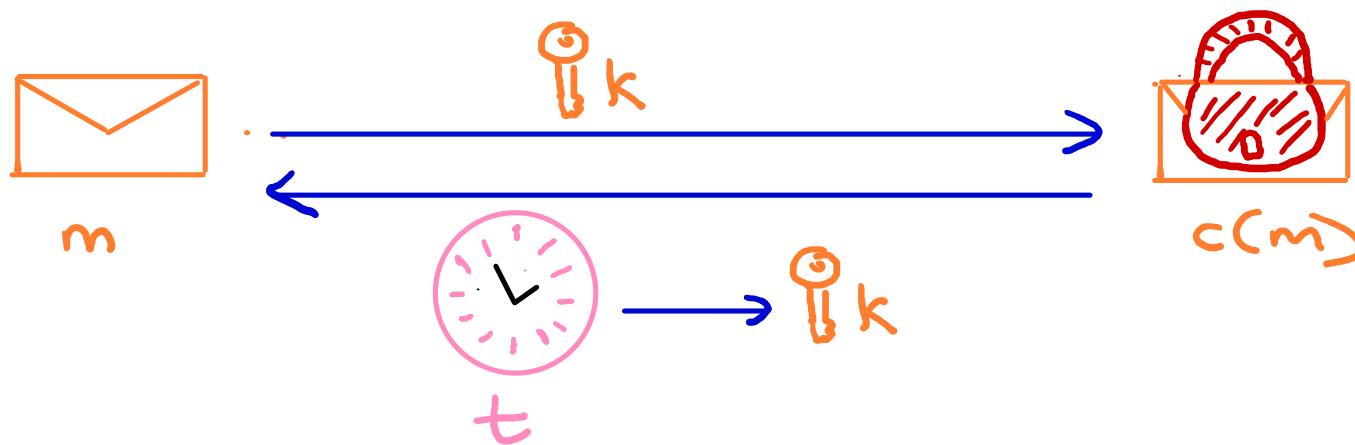
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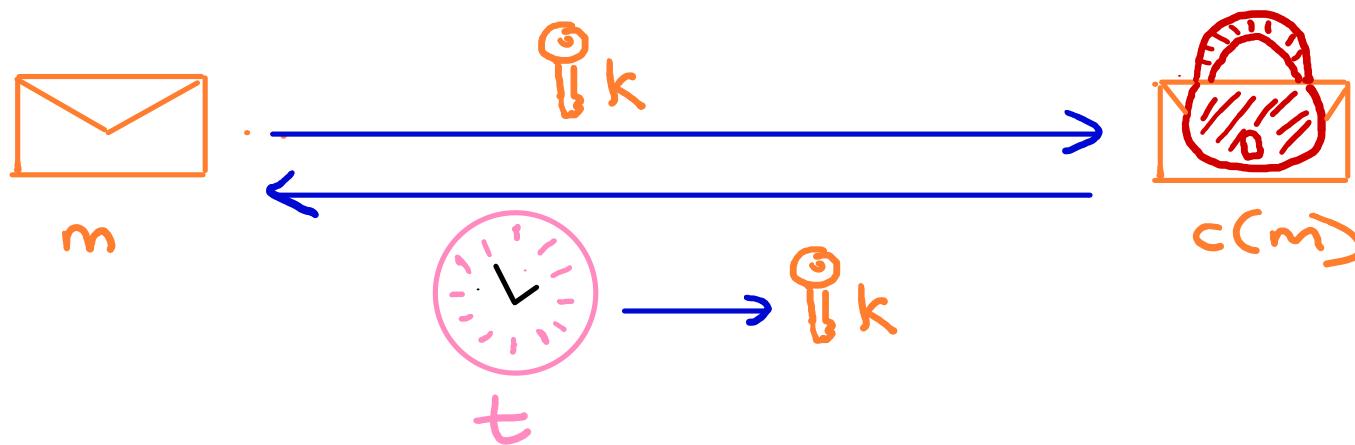
wall-clock time < CPU time for parallel processes

Timed-release crypto



Applications?

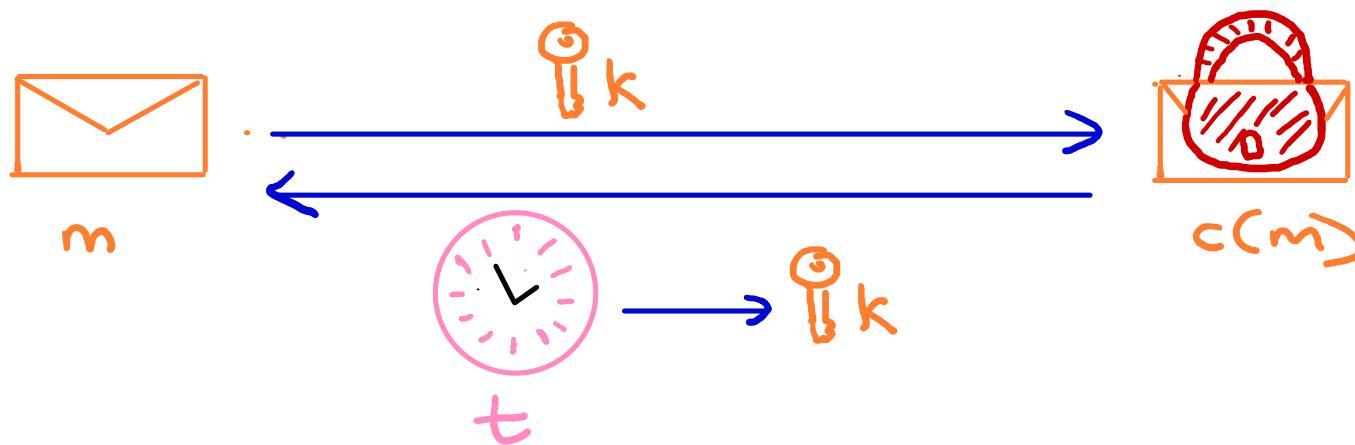
Timed-release crypto



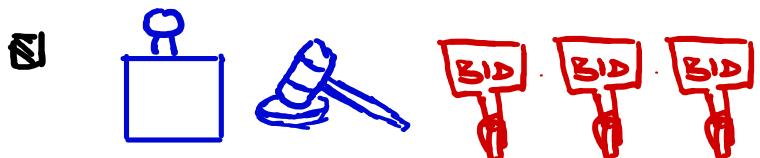
Applications?



Timed-release crypto

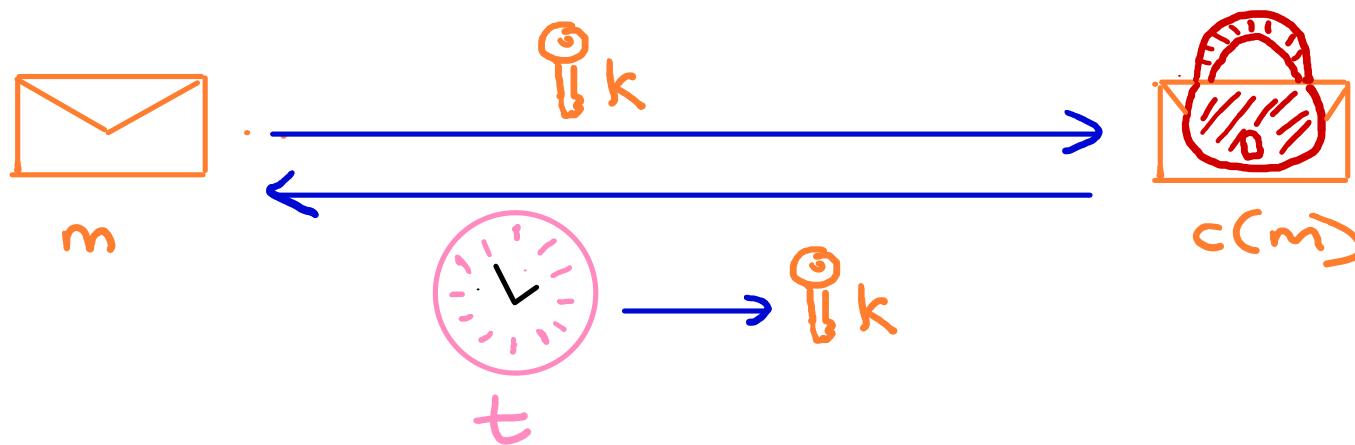


Applications?



Sotheby's

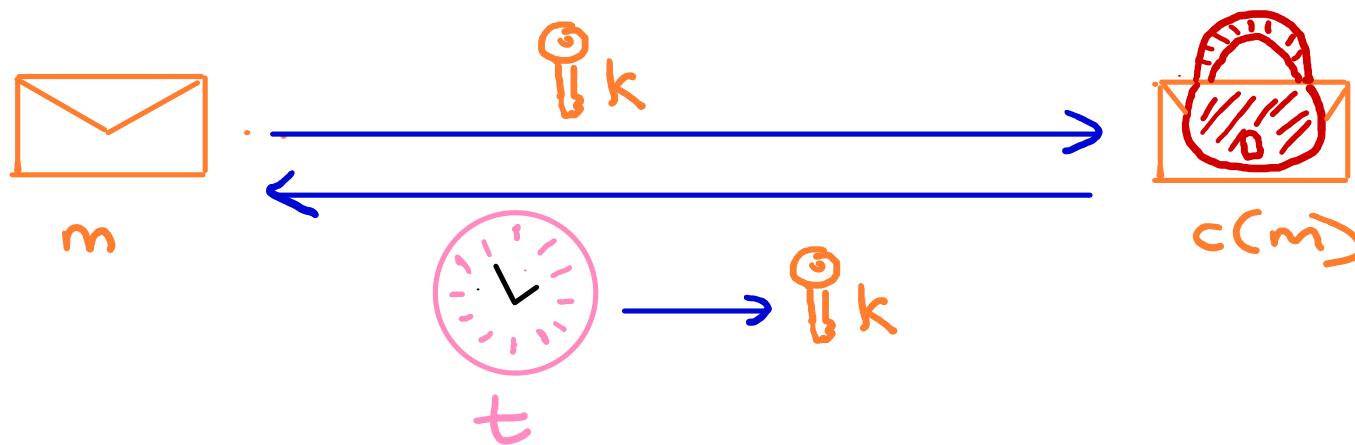
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Applications?

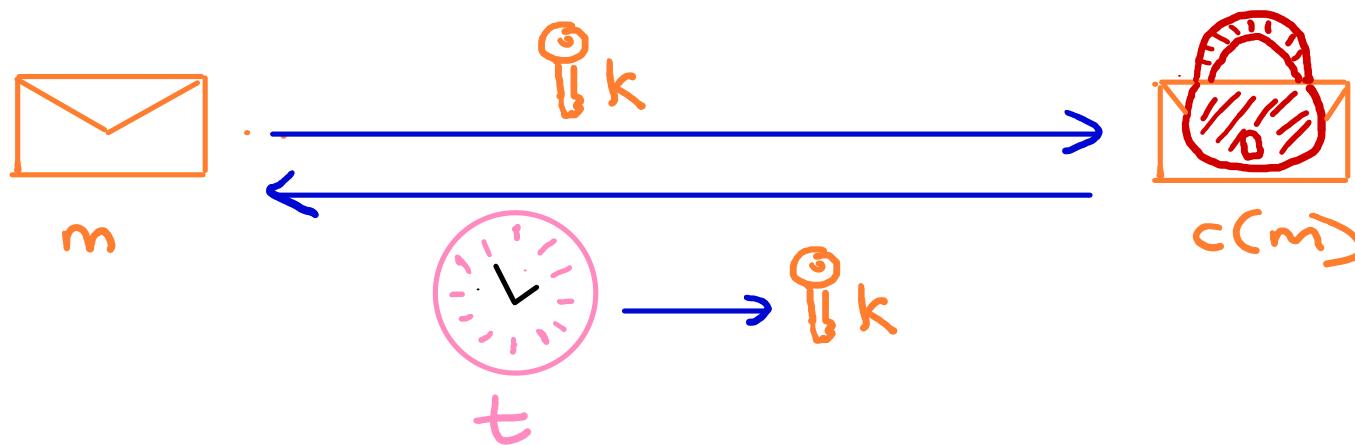


Timed-release crypto

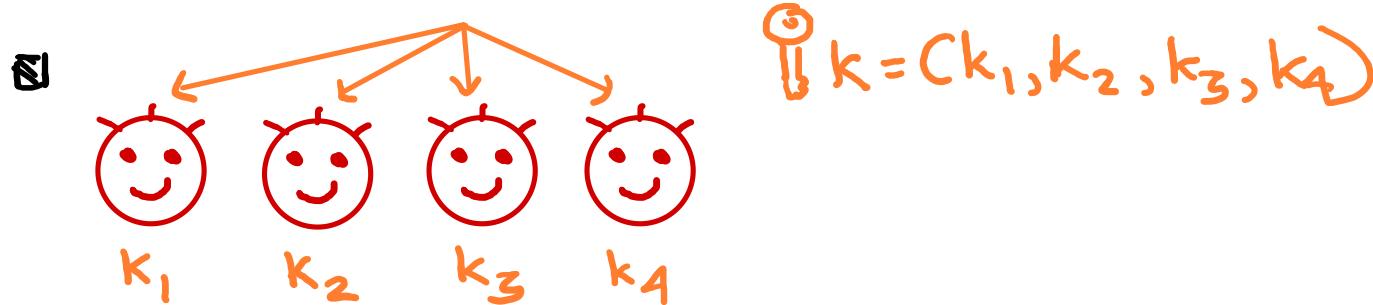


How to implement?

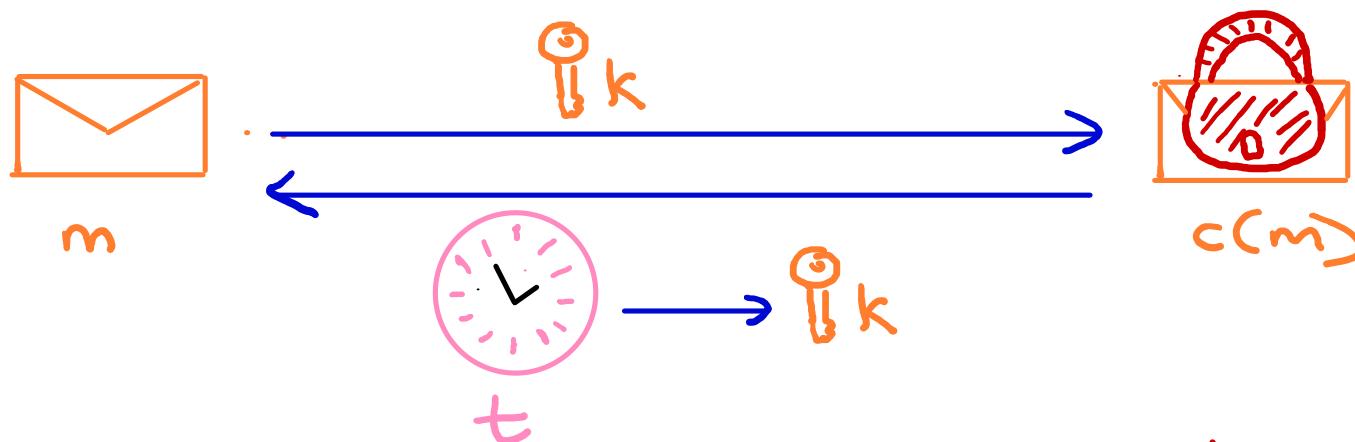
Timed-release crypto



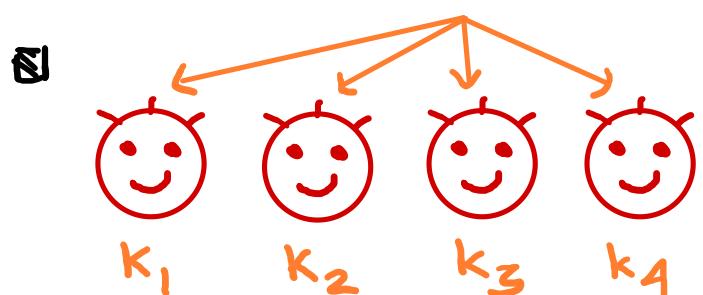
How to implement?



Timed-release crypto



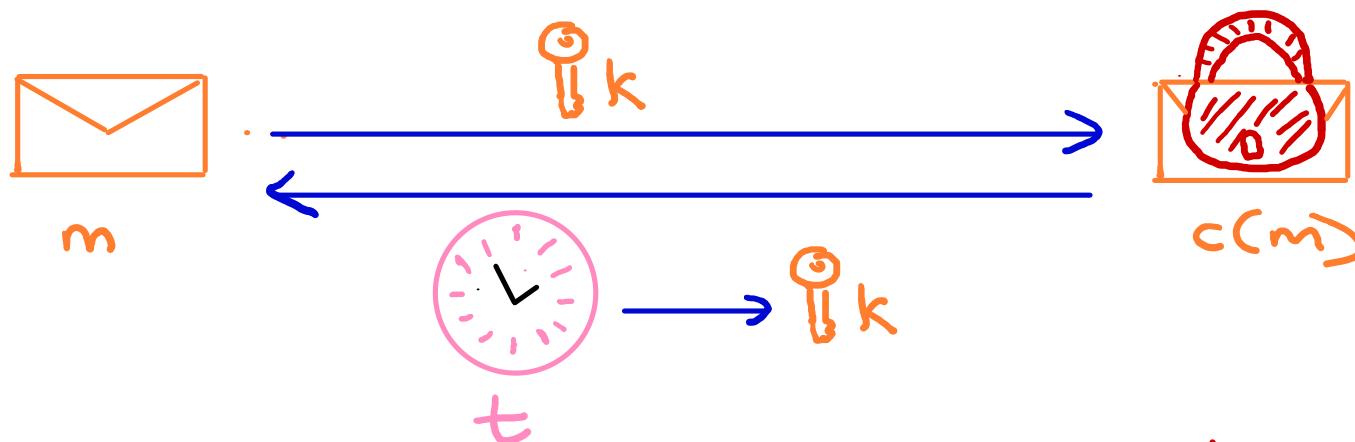
How to implement?



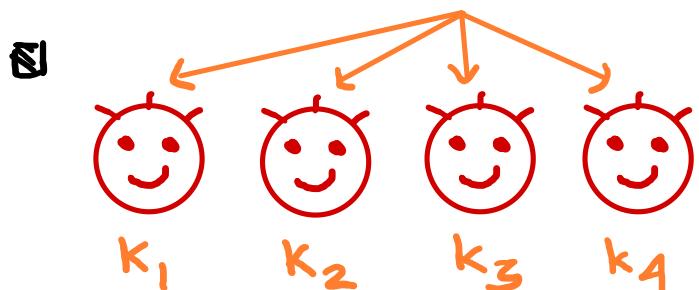
cannot reconstruct k
without knowing $k_i, i=1\dots 4$

$\mathbb{K} = (k_1, k_2, k_3, k_4)$

Timed-release crypto



How to implement?



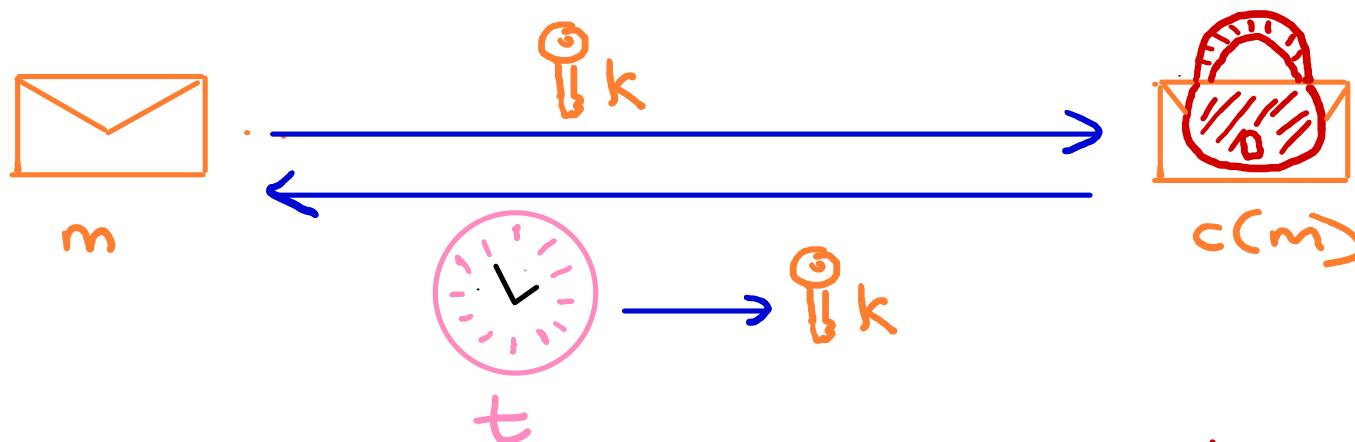
cannot reconstruct k
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$$\mathbb{b}k = (k_1, k_2, k_3, k_4)$$

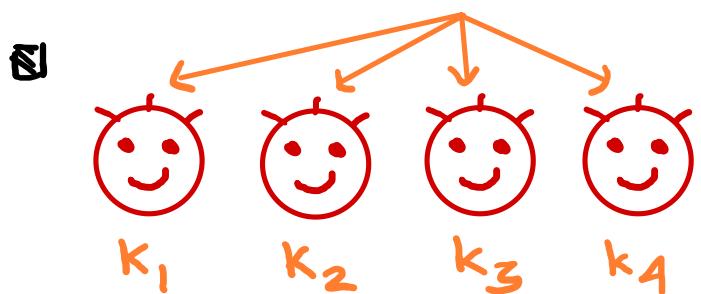
but...

collusion / death / disappearance!

Timed-release crypto



How to implement?



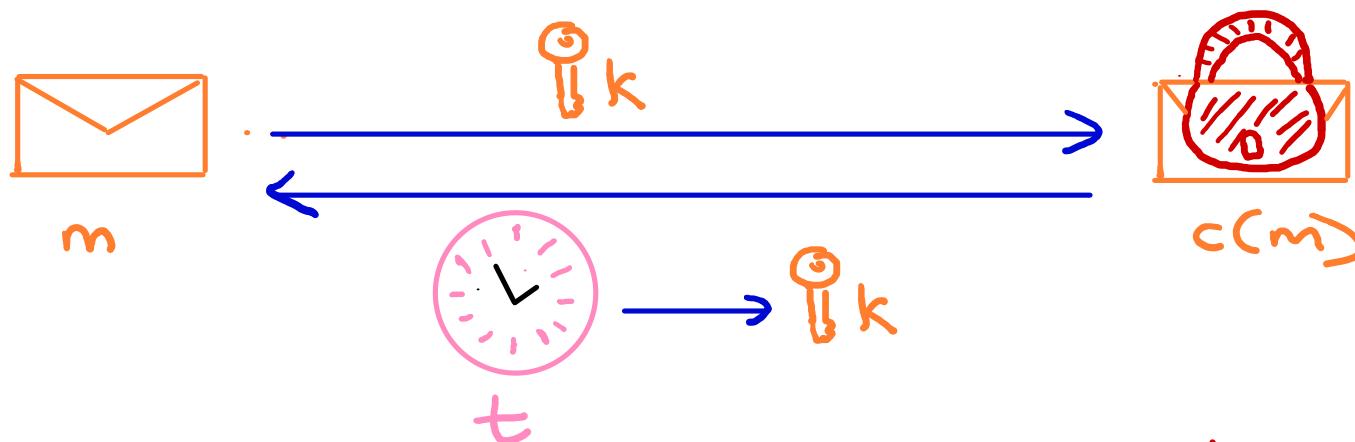
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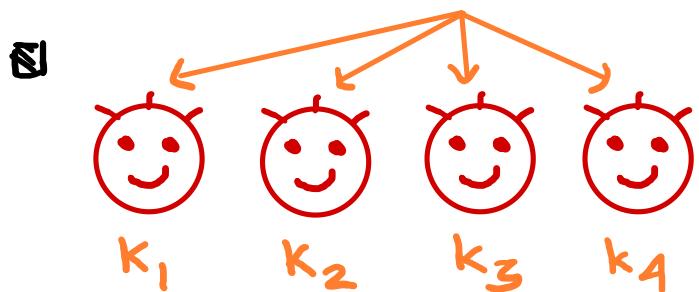
but...

collusion / death / disappearance!
use Shamir's secret sharing!
[S'79]

Timed-release crypto



How to implement?

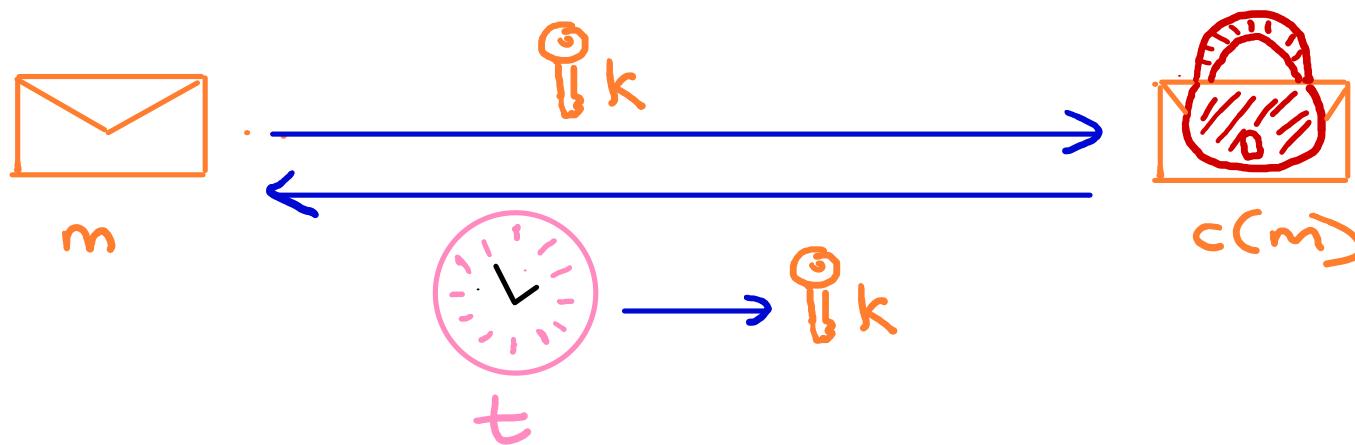


cannot reconstruct k
without knowing $k_i, i=1\dots 4$

$$\mathbb{B}^k = (k_1, k_2, k_3, k_4)$$

but... still need to rely/trust!
collusion/death/disappearance!
use Shamir's secret sharing!
[S'79]

Timed-release crypto



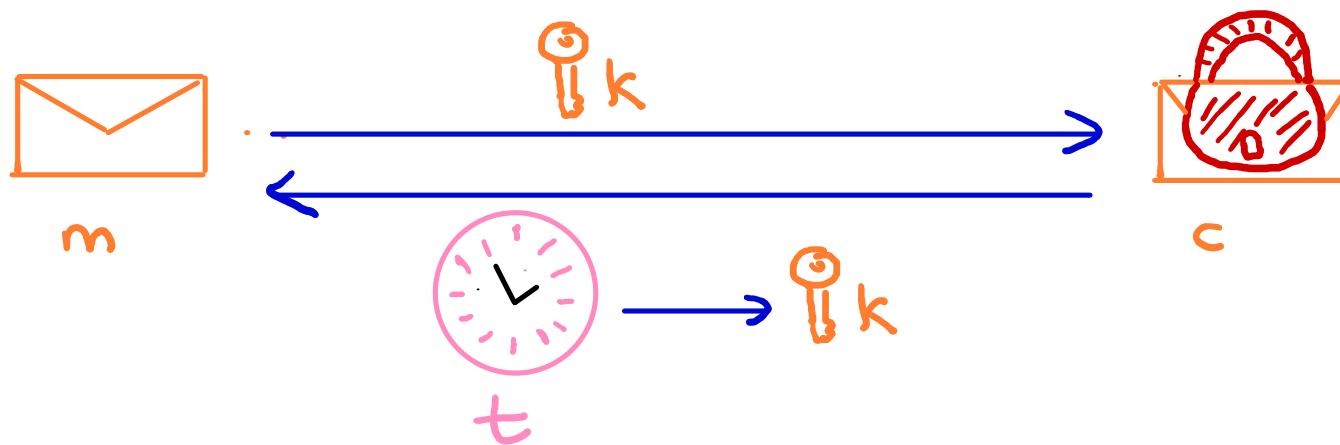
How to implement?

- Use time-lock puzzles!
[RSW'96]

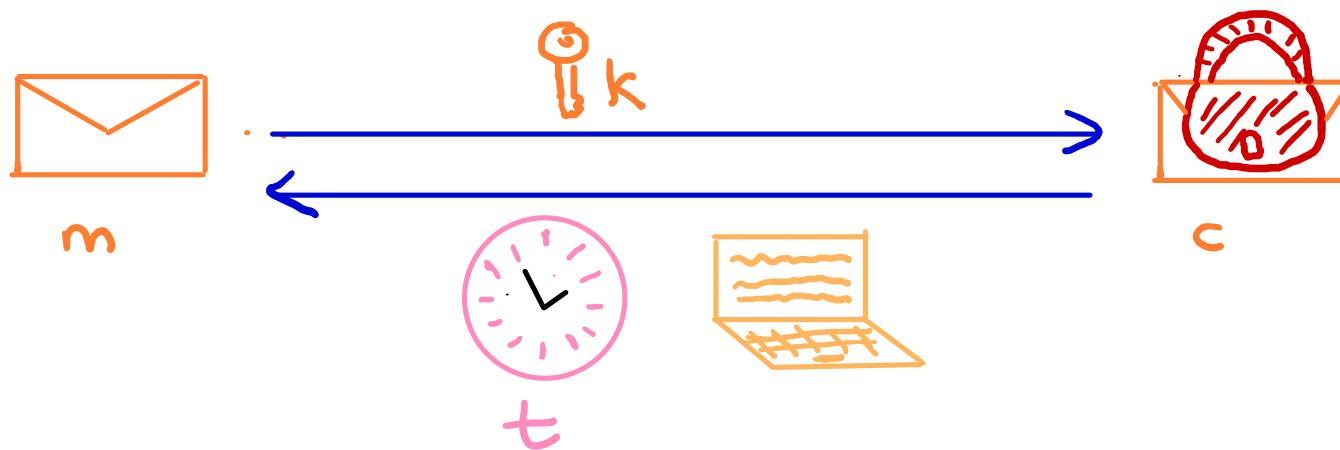
Plan for the afternoon

- Timed-release crypto
- Time-lock puzzles
- Verifiable Delay Functions
- Pietrzak's construction

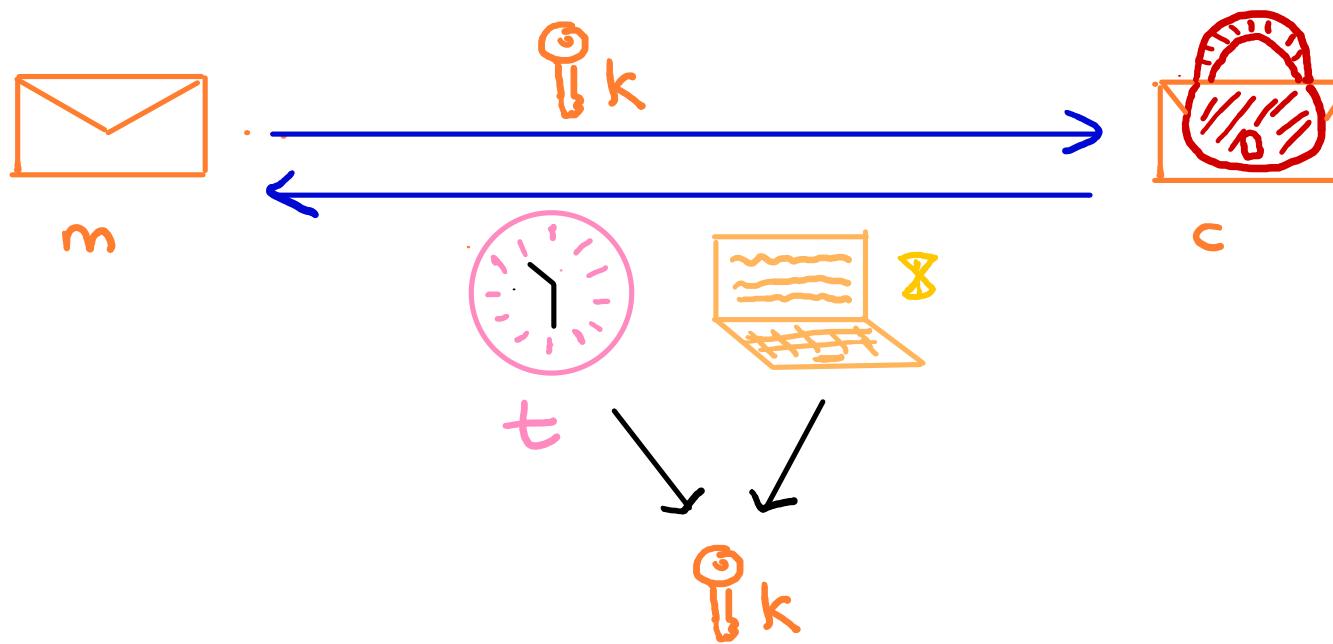
Time-lock Puzzle



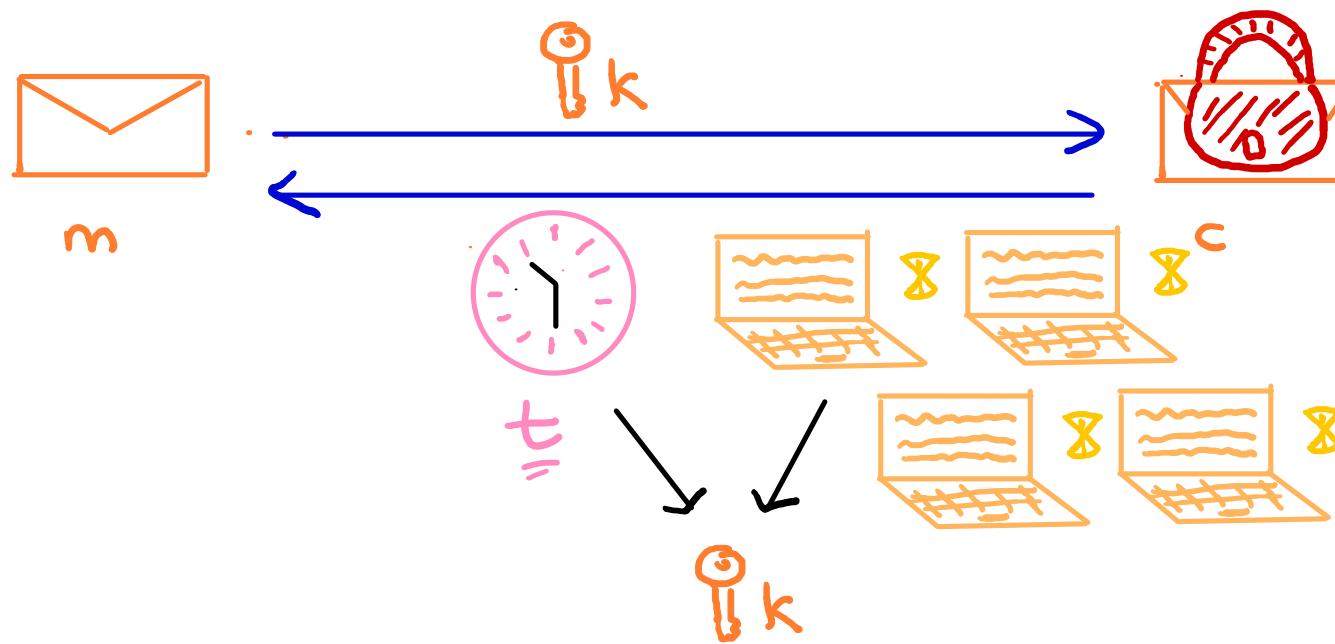
Time-lock Puzzle



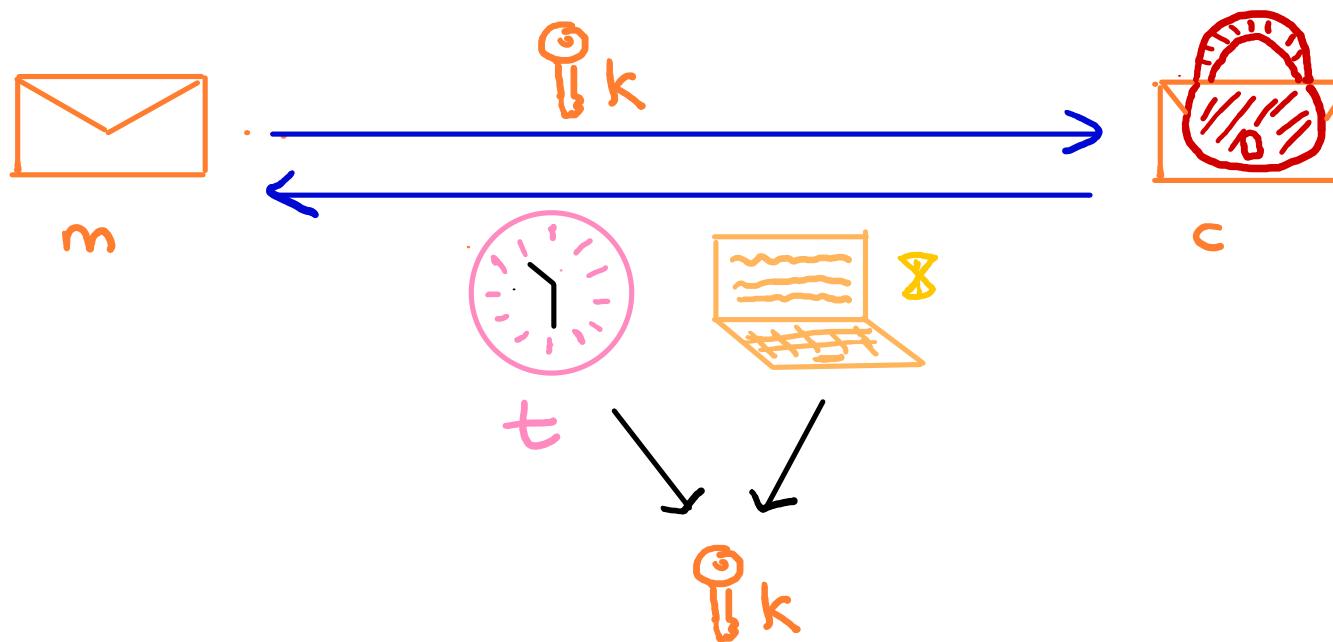
Time-lock Puzzle



Time-lock Puzzle



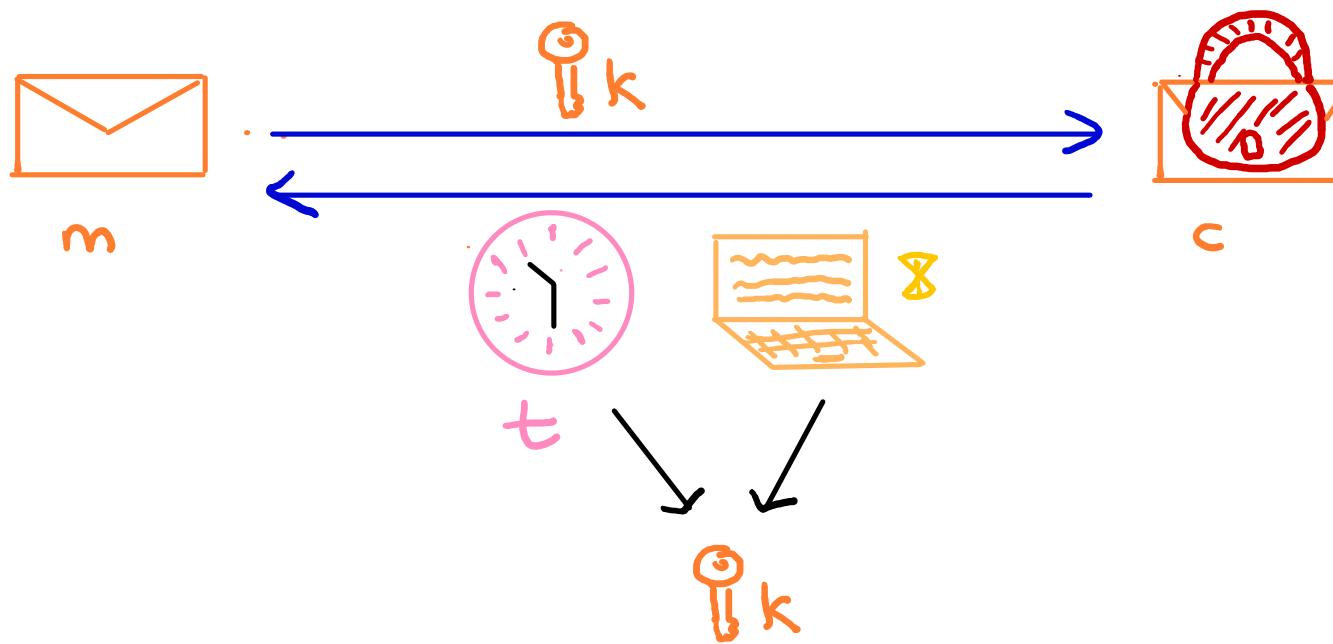
Time-lock Puzzle



Solutions?

- just AES encrypt with $|k| = \log(2st)$ bits,
 $s \rightarrow$ no. of decryptions/s

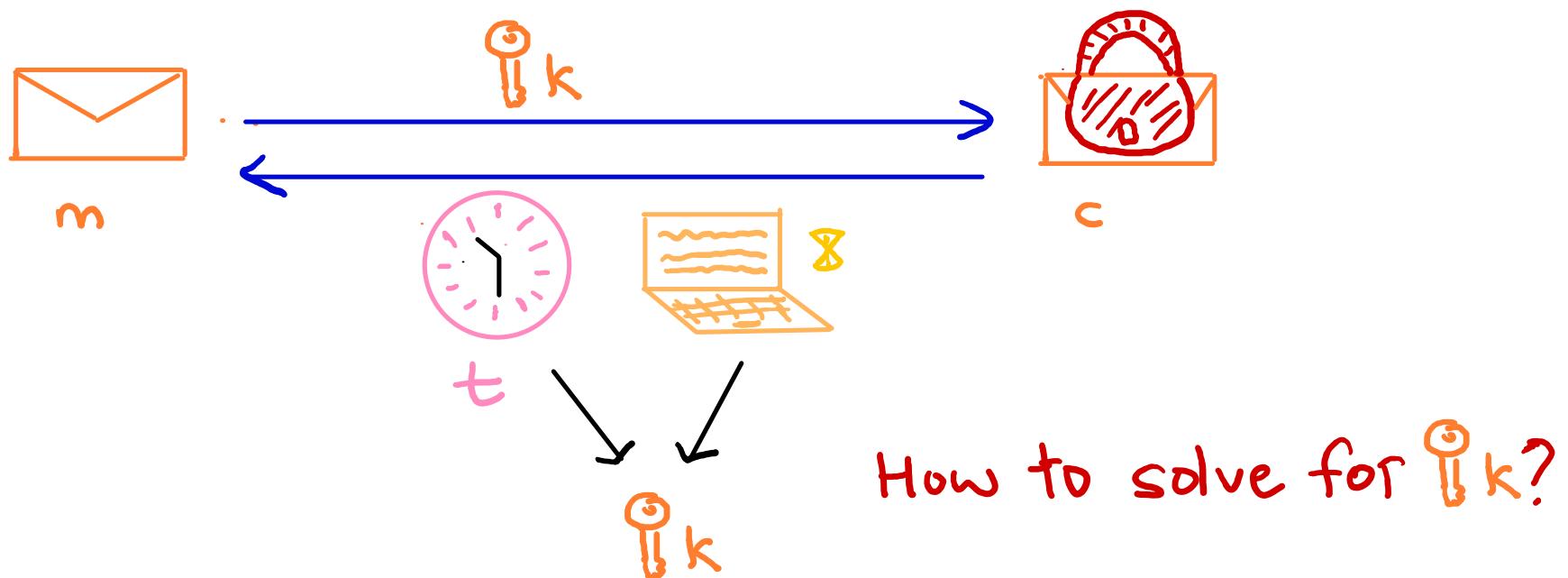
Time-lock Puzzle



Solutions?

- just AES encrypt with $|k| = \log(2st)$ bits,
 $s \rightarrow$ no. of decryptions/s
and $k \xrightarrow{\text{discard}} \text{trash}$

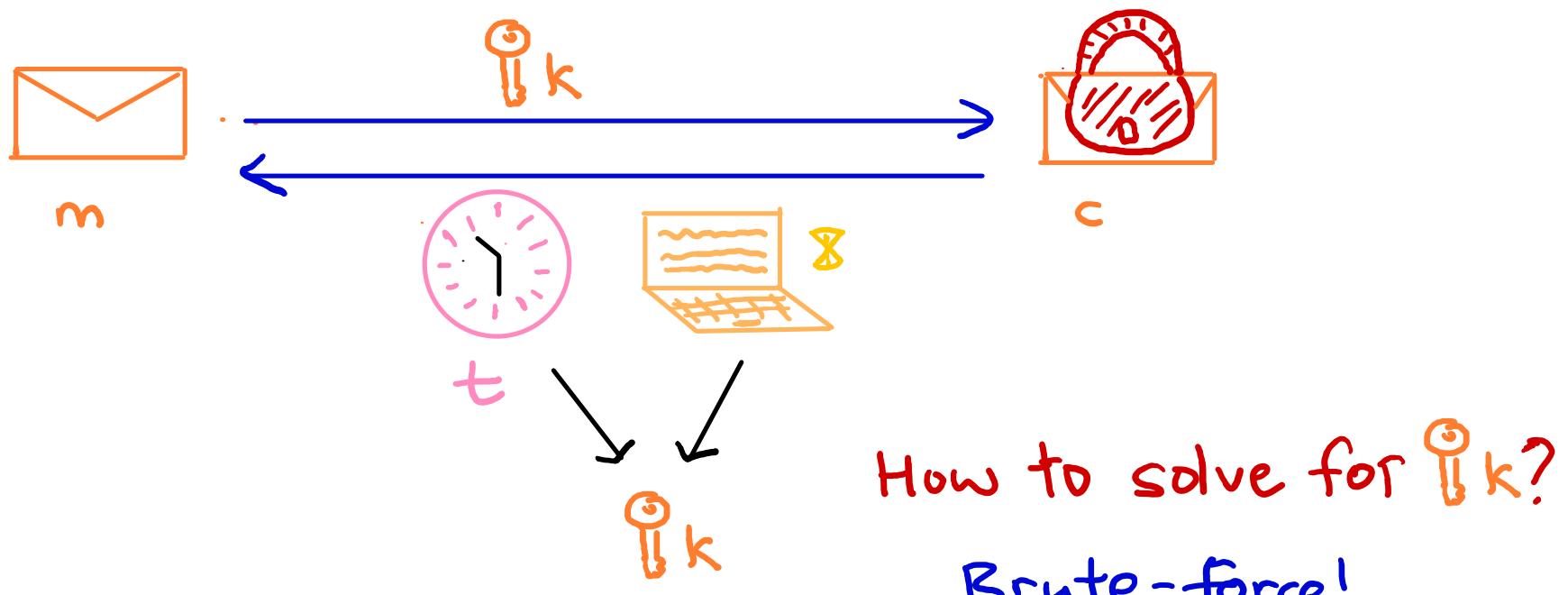
Time-lock Puzzle



Solutions?

- just AES encrypt with $|k| = \log(2st)$ bits,
 $s \rightarrow$ no. of decryptions/s
and $\mathbb{K} \xrightarrow{\text{discard}} \text{trash}$

Time-lock Puzzle



How to solve for \mathbb{K} ?

Brute-force!

Solutions?

- just AES encrypt with $|k| = \log(2st)$ bits,
 $s \rightarrow$ no. of decryptions/s
and $\mathbb{K} \xrightarrow{\text{discard}} \text{trash}$

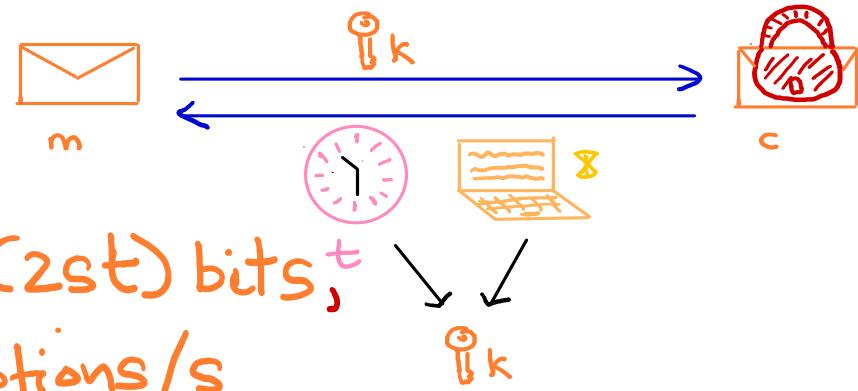
Time-lock Puzzle

Solutions?

- just AES encrypt with $|k| = \log(2st)$ bits,
 \dots $s \rightarrow$ no. of decryptions/s

and $k \xrightarrow{\text{discard}} \text{lock}$ How to solve for k ?

Brute-force!



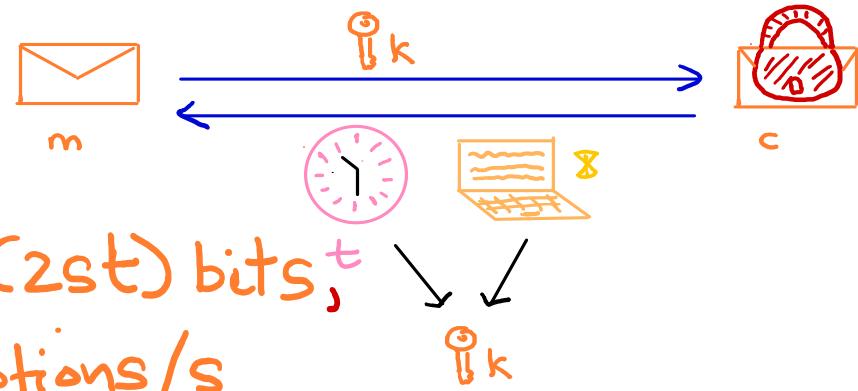
Time-lock Puzzle

Solutions?

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Brute-force!

Problems:



Time-lock Puzzle

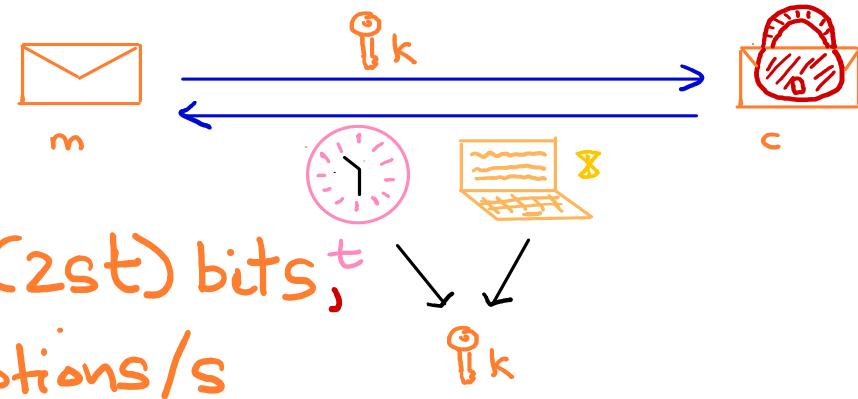
Solutions?

- just AES encrypt with $|k| = \log(2st)$ bits,
 \dots $s \rightarrow$ no. of decryptions/s
- and $k \xrightarrow{\text{discard}} \text{locker}$ How to solve for k ?

Brute-force!

Problems:

-  can find k in less than t



Time-lock Puzzle

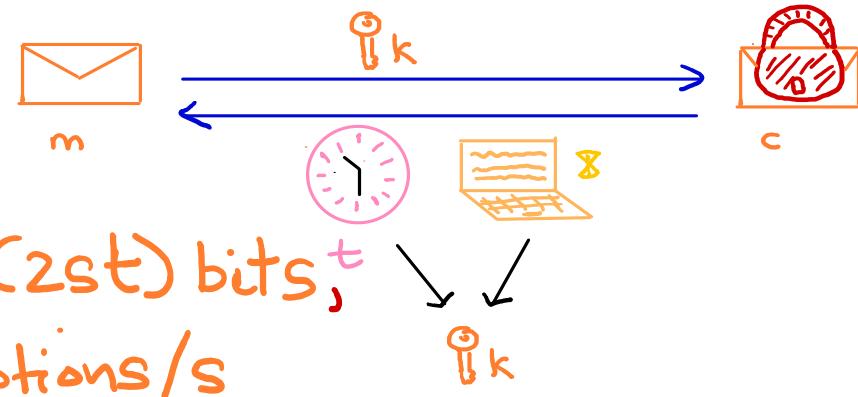
Solutions?

- just AES encrypt with $|k| = \log(2st)$ bits,
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- and k $\xrightarrow{\text{discard}}$ How to solve for k ?

Brute-force!

Problems:

- can find k in less than t
needs to be sequential!



Time-lock Puzzle

Solutions?

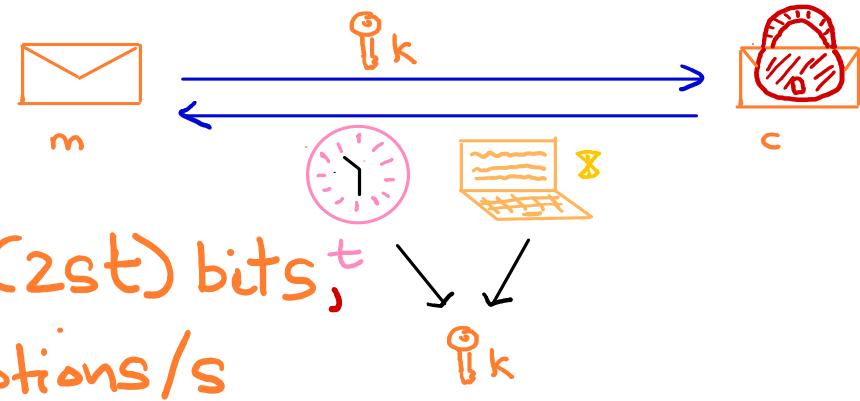
- just AES encrypt with $|k| = \log(2st)$ bits,
 \dots $s \rightarrow$ no. of decryptions/s
- and $k \xrightarrow{\text{discard}} \text{key}$ How to solve for k ?

Brute-force!

Problems:

-  can find k in less than t
needs to be sequential!

i.e. k cannot be found in, say,
 $t' = t^c, c < 1$



Time-lock Puzzle

Solutions?

- just AES encrypt with $|k| = \log(2st)$ bits,
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Brute-force!

Problems:

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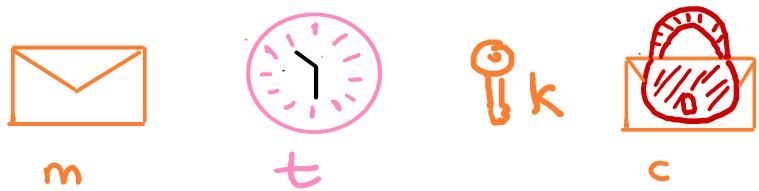
i.e. k cannot be found in, say,
 $t' = t^c$, $c < 1$ (allow minor variations)

Time-lock Puzzle

→ RSW construction:

Time-lock Puzzle

→ RSW construction:



Time-lock Puzzle

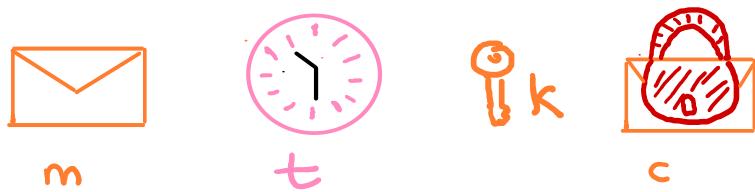
→ RSW construction:



■ Generate $n = pq$

Time-lock Puzzle

→ RSW construction:



■ Generate $n = pq$ (for two large random secret primes p, q)

Time-lock Puzzle

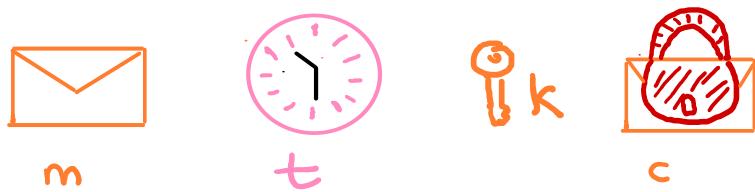
→ RSW construction:



- Generate $n = pq$ (for two large random secret primes p, q)
- Compute $\phi(n) = (p-1)(q-1)$

Time-lock Puzzle

→ RSW construction:



- Generate $n = pq$ (for two large random secret primes p, q)
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Time-lock Puzzle

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- Compute $c_k = k + a^{2^t} \pmod{n}$

Time-lock Puzzle

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How?

Time-lock Puzzle

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How? compute $e = 2^t \pmod{\phi(n)}$

Time-lock Puzzle

→ RSW construction:

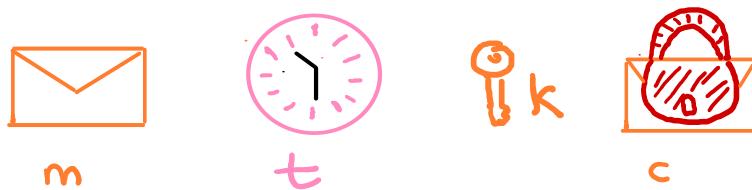


- Generate $n = pq$ (for two large random secret primes p, q)
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- Pick random a ($1 < a < n$)
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How? compute $e = 2^t \pmod{\phi(n)}$
 $b = a^e \pmod{n}$

Time-lock Puzzle

→ RSW construction:



- Generate $n = pq$ (for two large random secret primes p, q)
- Compute $\phi(n) = (p-1)(q-1)$
- Pick random a ($1 < a < n$)
- Compute $c_k = k + a^{2^t} \pmod{n}$
- Output (n, a, t, c, c_k) and a small icon of a padlock.

Time-lock Puzzle

→ RSW construction:

why does this work?

Time-lock Puzzle

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why does this work?

- brute-forcing the AES key → infeasible

Time-lock Puzzle

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- fastest way to solve puzzle:

Time-lock Puzzle

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- fastest way to solve puzzle:

$$a \rightarrow a^2 \rightarrow a^{2^2} \rightarrow a^{2^3} \rightarrow \dots \rightarrow a^{2^t} \pmod{n}$$

Time-lock Puzzle

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t sequential steps

Time-lock Puzzle

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- can easily compute if $\phi(n)$ known

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- can easily compute if $\phi(n)$ known
but $n \rightarrow \phi(n) \approx$ factoring n !

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infeasible for large p, q

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fastest way to **solve puzzle**:

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t sequential steps

(no known algo. to parallelize)

- can easily compute if $\phi(n)$ known

but $n \rightarrow \phi(n) \approx$ factoring n !

infeasible for large p, q

Time-lock Puzzle

→ RSW construction:

What about verifiability of $b = a^t$?

Time-lock Puzzle

→ RSW construction:

What about verifiability of $b = a^{2^t}$?

simply check if $\text{AES.Dec}(c, c_k - b \pmod n) = m$?

Time-lock Puzzle

→ RSW construction:

What about verifiability of $b = a^{2^t}$?

Simply check if $\text{AES.Dec}(c, c_k - b \pmod n) = m$?

What about public and efficient verifiability?

Time-lock Puzzle

→ RSW construction:

What about verifiability of $b = a^{2^t}$?

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reveal $\phi(n)$ (or p, q)

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simply check if $\text{AES.Dec}(c, c_k - b \pmod n) = m$?

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reveal $\phi(n)$ (or p, q)

↑ breaks sequentiality!

can trivially compute b if $\phi(n)$ known!

Time-lock Puzzle

→ RSW construction:

What about verifiability of $b = a^{2^t}$?

Simply check if $\text{AES.Dec}(c, c_k - b \pmod n) = m$?

What about public and efficient verifiability?

given (a, b, t) , $b = a^{2^t}$

Time-lock Puzzle

→ RSW construction:

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given (a, b, t) , $b = a^{2^t}$, some proof Π so that

Time-lock Puzzle

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hmm...



(a, b, t, π)

Any Body

Time-lock Puzzle

→ RSW construction:

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Simply check if $\text{AES.Dec}(c, c_k - b \pmod n) = m$?

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What about public and efficient verifiability?

given (a, b, t) , $b = a^{2^t}$, some proof π so that



$(a, b, t, \pi) \leftarrow$ should take time
 $t' \lll t$

Any Body

Time-lock Puzzle

→ RSW construction:

What about verifiability of $b = a^{2^t}$?

Simply check if $\text{AES.Dec}(c, c_k - b \pmod n) = m$?

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given (a, b, t) , $b = a^{2^t}$, some proof π so that



$(a, b, t, \pi) \leftarrow$ should take time
 $t' \lll t$

Any Body

Possible?

Time-lock Puzzle

→ RSW construction:

What about verifiability of $b = a^{2^t}$?

Simply check if $\text{AES.Dec}(c, c_k - b \pmod n) = m$?

What about public and efficient verifiability?

given (a, b, t) , $b = a^{2^t}$, some proof π so that



Any Body

$(a, b, t, \pi) \leftarrow$ should take time
 $t' \lll t$

Possible? Yes, using VDFs!

Plan for the afternoon

- Timed-release crypto
- Time-lock puzzles
- Verifiable Delay Functions
- Pietrzak's construction

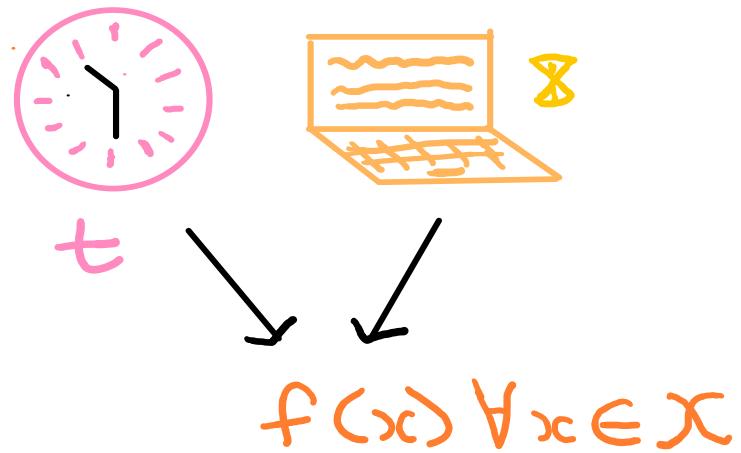
Verifiable Delay Functions [BBBF'18]

Verifiable Delay Functions

→ $f: \mathcal{X} \rightarrow \mathcal{Y}$

Verifiable Delay Functions

→ $f: \mathcal{X} \rightarrow \mathcal{Y}$



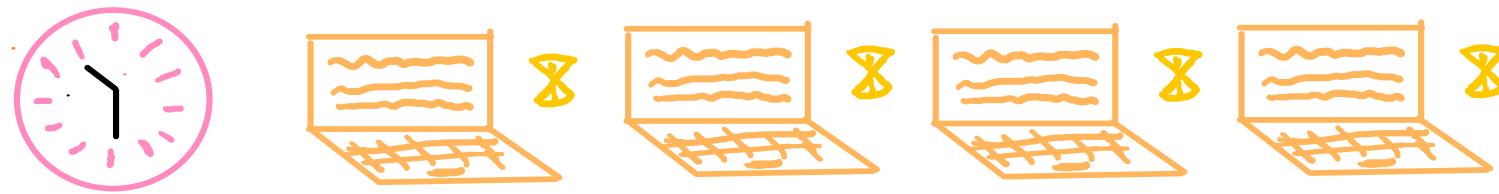
Verifiable Delay Functions

→ $f: \mathcal{X} \rightarrow \mathcal{Y}$



Verifiable Delay Functions

$$\rightarrow f: X \rightarrow Y$$



$t =$



Any Body

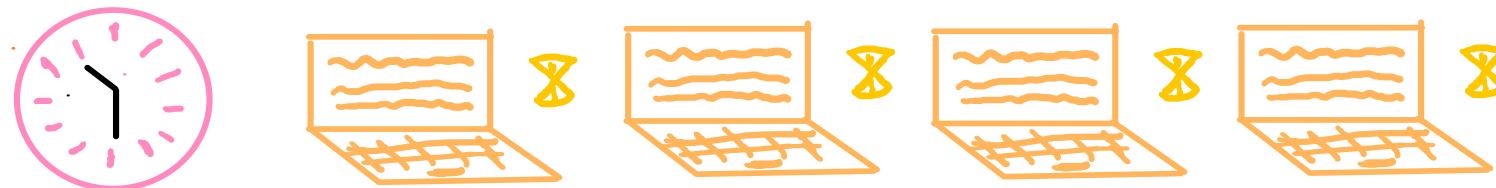
$$f(x) \forall x \in X$$

$$(x, y, t, \pi), y = f(x)$$

(Correctness)

Verifiable Delay Functions

$$\rightarrow f: X \rightarrow Y$$



$t =$



$$f(x) \forall x \in X$$

$$(x, y, t, \pi), y = f(x)$$

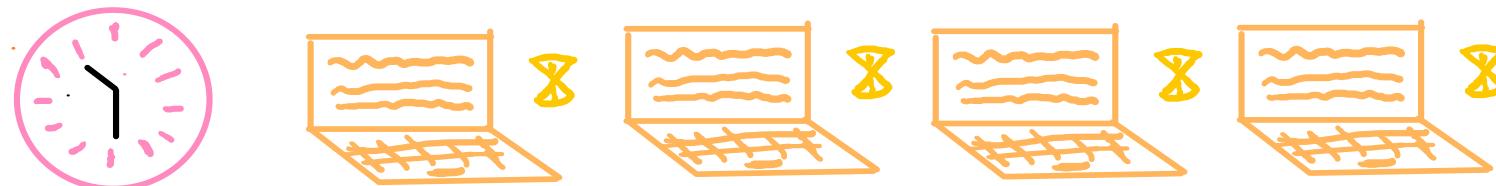
$$t' \ll t$$

Any Body

↑ efficiency

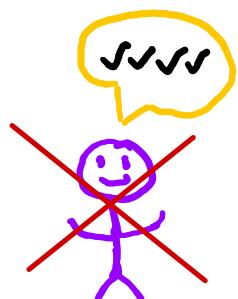
Verifiable Delay Functions

→ $f: X \rightarrow Y$



$t =$

$f(x) \forall x \in X$



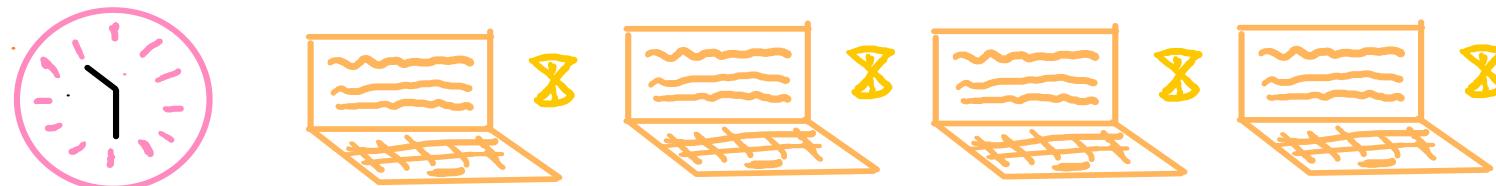
No Body

$(x, y, t, \pi), y \neq f(x)$

(soundness)

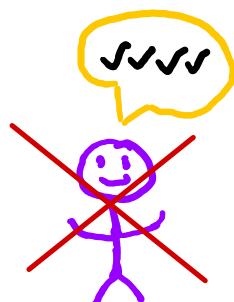
Verifiable Delay Functions

$$\rightarrow f: X \rightarrow Y$$



$t =$

$$f(x) \forall x \in X$$



No Body

$$(x, y, t, \pi), y \neq f(x)$$

(soundness)

unique valid output

for all $x \in X$

Verifiable Delay Functions

→ More specifically, a tuple of 3 algorithms:

Verifiable Delay Functions

→ More specifically, a tuple of 3 algorithms:

- $\text{Setup}(\lambda, T) \rightarrow \text{PP}$

Verifiable Delay Functions

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- $\text{Setup}(\lambda, T) \rightarrow \text{PP}$
 T
security

Verifiable Delay Functions

→ More specifically, a tuple of 3 algorithms:

- $\text{Setup}(\lambda, T) \rightarrow \text{PP} \leftarrow \text{public param}$
 T \nwarrow
security delay

Verifiable Delay Functions

→ More specifically, a tuple of 3 algorithms:

- $\text{Setup}(\lambda, T) \xrightarrow{\$} \text{PP} \leftarrow \begin{array}{l} \text{public param} \\ (\text{set } x, y) \end{array}$
 T \nwarrow delay
 λ \nwarrow security

Verifiable Delay Functions

→ More specifically, a tuple of 3 algorithms:

- $\text{Setup}(\lambda, \tau) \xrightarrow{\$} \text{PP} \leftarrow \begin{array}{l} \text{public param} \\ (\text{set } x, y) \end{array}$
 τ delay
security
- $\text{Eval}(\text{PP}, x) \rightarrow (y, \pi)$

Verifiable Delay Functions

→ More specifically, a tuple of 3 algorithms:

- $\text{Setup}(\lambda, \tau) \xrightarrow{\$} \text{PP} \leftarrow \begin{array}{l} \text{public param} \\ (\text{set } x, y) \end{array}$
 τ delay
security
- $\text{Eval}(\text{PP}, x) \rightarrow (y, \pi)$
 x input
 y, π output proof

Verifiable Delay Functions

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 x input
 y output
 π proof
- $\text{Verify}(\text{PP}, x, y, \pi) \rightarrow \{0, 1\}$

Verifiable Delay Functions

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 y output
 x input
 π proof
- $\text{Verify}(\text{PP}, x, y, \pi) \rightarrow \{0, 1\}$
output if $(y, \pi) \leftarrow \text{Eval}(\text{PP}, x)$

Verifiable Delay Functions

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- $\text{Setup}(\lambda, \tau) \xrightarrow{\$} \text{PP} \leftarrow \begin{array}{l} \text{public param} \\ (\text{set } x, y) \end{array}$
 τ delay
security
- $\text{Eval}(\text{PP}, x) \rightarrow (y, \pi)$
 y output
 x input
 π proof
- $\text{Verify}(\text{PP}, x, y, \pi) \rightarrow \{0, 1\}$
otherwise
output if $(y, \pi) \leftarrow \text{Eval}(\text{PP}, x)$

Verifiable Delay Functions

→ More specifically, a tuple of 3 algorithms:

- $\text{Setup}(\lambda, T) \xrightarrow{\$} \text{PP} \leftarrow \text{public param}$
 \uparrow security delay
 \uparrow (set x, y)
- $\text{Eval}(\text{PP}, x) \rightarrow (y, \pi)$
 \uparrow input
 \uparrow output
 \downarrow proof
- $\text{Verify}(\text{PP}, x, y, \pi) \rightarrow \{0, 1\}$
 \uparrow otherwise
 \uparrow output if $(y, \pi) \leftarrow \text{Eval}(\text{PP}, x)$
 \uparrow $\text{PP} \leftarrow \text{Setup}(\lambda, t)$
 \uparrow $x \in X$

Verifiable Delay Functions

→ More specifically, a tuple of 3 algorithms:

- $\text{Setup}(\lambda, T) \xrightarrow{\$} \text{PP} \leftarrow \begin{array}{l} \text{public param} \\ (\text{set } x, y) \end{array}$
 T delay
 λ security
- $\text{Eval}(\text{PP}, x) \rightarrow (y, \pi)$
 x input
 y output
 π proof
- $\text{Verify}(\text{PP}, x, y, \pi) \rightarrow \{0, 1\}$
otherwise
output if $(y, \pi) \leftarrow \text{Eval}(\text{PP}, x)$
 $\text{PP} \leftarrow \text{Setup}(\lambda, t)$
 $x \in X$
- $\text{TEval}(t_d, \text{PP}, x) \rightarrow (y, \pi)$

Verifiable Delay Functions

→ More specifically, a tuple of 3 algorithms:

- $\text{Setup}(\lambda, T) \xrightarrow{\$} \text{PP} \leftarrow \text{public param}$
 T delay
 λ security
(set x, y)
- $\text{Eval}(\text{PP}, x) \rightarrow (y, \pi)$
 x input
 y, π output
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 $x \in X$
- $\text{TEval}(t_d, \text{PP}, x) \rightarrow (y, \pi)$
trapdoor

Verifiable Delay Functions

→ More specifically, a tuple of 3 algorithms:

- $\text{Setup}(\lambda, T) \xrightarrow{\$} \text{PP} \leftarrow \text{public param}$
 \uparrow security delay
 \uparrow (set x, y)
- $\text{Eval}(\text{PP}, x) \rightarrow (y, \pi)$
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 \uparrow output if $(y, \pi) \leftarrow \text{Eval}(\text{PP}, x)$
 \uparrow $\text{PP} \leftarrow \text{Setup}(\lambda, t)$
 \uparrow $x \in X$
- $\text{TEval}(t_d, \text{PP}, x) \rightarrow (y, \pi)$
 \uparrow trapdoor → use to compute (y, π) in $T' \llll T$

Verifiable Delay Functions

→ More specifically, a tuple of 3 algorithms:

- $\text{Setup}(\lambda, T) \xrightarrow{\$} \text{PP} \leftarrow \text{public param}$
 \uparrow security delay
 \uparrow (set x, y)
- $\text{Eval}(\text{PP}, x) \rightarrow (y, \pi)$
 \uparrow input
 \uparrow output
 \downarrow proof
- $\text{Verify}(\text{PP}, x, y, \pi) \rightarrow \{0, 1\}$
 \uparrow otherwise
 \uparrow output if $(y, \pi) \leftarrow \text{Eval}(\text{PP}, x)$
 \uparrow $\text{PP} \leftarrow \text{Setup}(\lambda, t)$
 \uparrow $x \in X$

Sometimes, also

- $\text{TEval}(t_d, \text{PP}, x) \rightarrow (y, \pi)$
keep secret! trapdoor → use to compute (y, π) in $T' \llll T$

Verifiable Delay Functions

→ Now, a concrete construction?

Verifiable Delay Functions

→ Now, a concrete construction?

RSW goes publicly verifiable!

Plan for the afternoon

- Timed-release crypto
- Time-lock puzzles
- Verifiable Delay Functions
- Pietrzak's construction

Pietrzak's VDF [Pie'19]

Pietrzak's VDF

→ Let's keep things simple!

Pietrzak's VDF

→ Let's keep things simple!

Without loss of generality

set $T = 2^k$, $k \in \mathbb{N}$

Pietrzak's VDF

→ Let's keep things simple!

Without loss of generality

set $T = 2^k, k \in \mathbb{N}$

$f: \mathbb{G} \rightarrow \mathbb{G}, h = f(g) = g^{2^T} (\text{in } \mathbb{G})$

Pietrzak's VDF

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set $T = 2^k, k \in \mathbb{N}$

$f: \mathbb{G} \rightarrow \mathbb{G}, h = f(g) = g^{2^T} \text{ (in } \mathbb{G})$

$\text{Setup}(\lambda, T) \longrightarrow \text{pp} := (\mathbb{G}, T)$

Pietrzak's VDF

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set $T = 2^k, k \in \mathbb{N}$

$f: \mathbb{G} \rightarrow \mathbb{G}, h = f(g) = g^{2^T} \text{ (in } \mathbb{G})$

$\text{Setup}(\lambda, T) \rightarrow \text{PP} := (\mathbb{G}, T)$

$\text{Eval}(\text{PP}, g) \rightarrow (h, \pi)$

But how does $\text{Verify}(\text{PP}, g, h, \pi)$ work?

Pietrzak's VDF

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let's make it interactive for now...

Pietrzak's VDF

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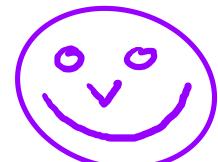
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Pat



Victor

Pietrzak's VDF

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Pat



Victor

Pietrzak's VDF

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Without loss of generality

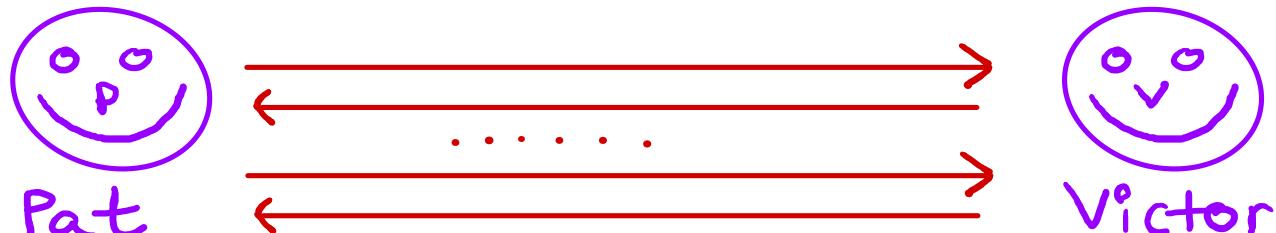
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Without loss of generality

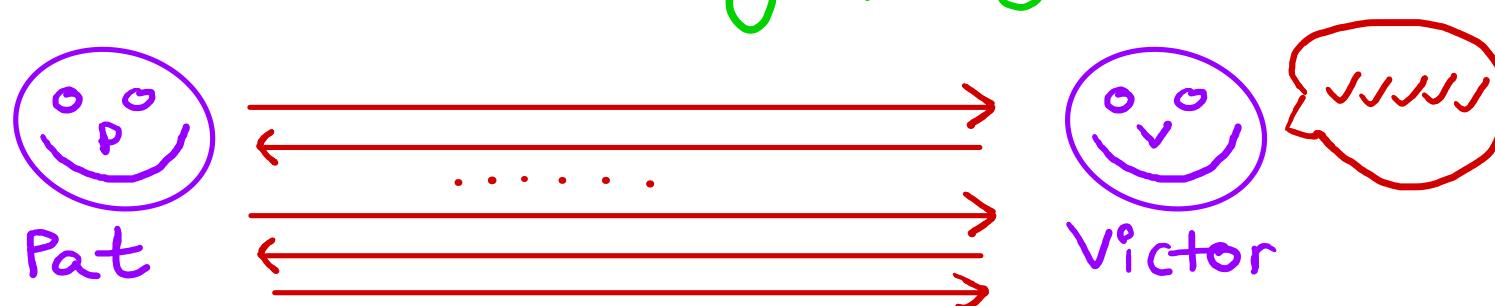
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But how does $\text{Verify}(\text{pp}, g, h, \pi)$ work?



Pietrzak's VDF

Halving subprotocol:
(or meet-in-the-middle)

Pietrzak's VDF

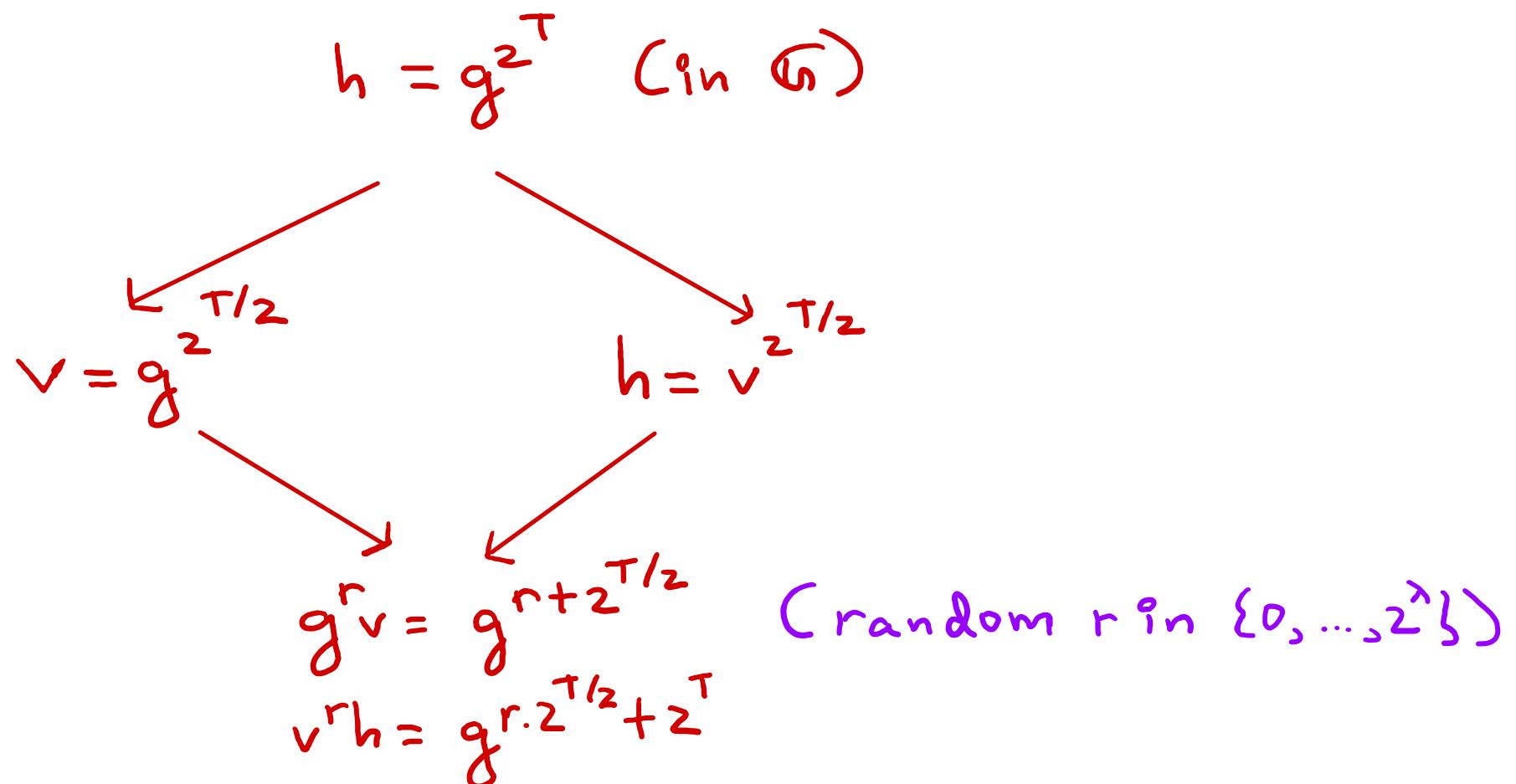
Halving subprotocol:
(or meet-in-the-middle)

$$h = g^{2^T} \text{ (in } \mathbb{G})$$
$$v = g^{2^{T/2}}$$
$$h = v^{2^{T/2}}$$

```
graph TD; A[h = g^{2^T} (in G)] --> B[v = g^{2^{T/2}}]; A --> C[h = v^{2^{T/2}}]
```

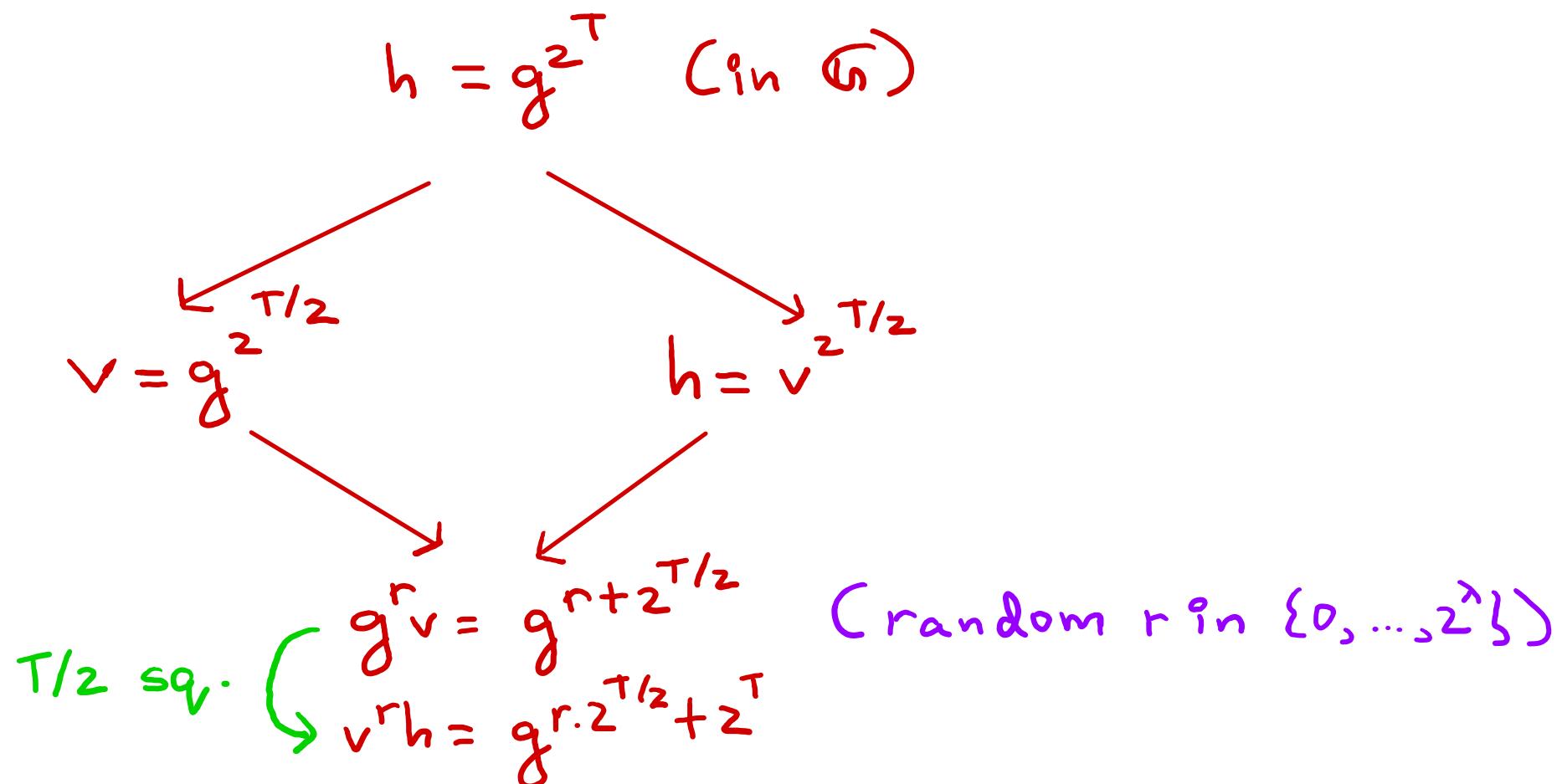
Pietrzak's VDF

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Pietrzak's VDF

Halving subprotocol:
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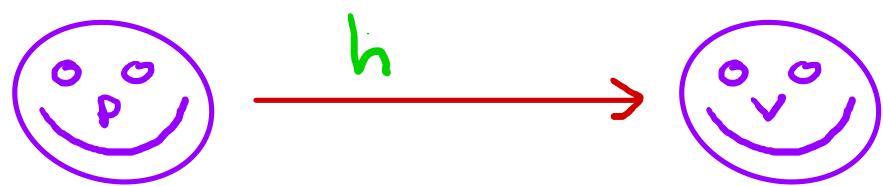
Pietrzak's VDF

Halving subprotocol:



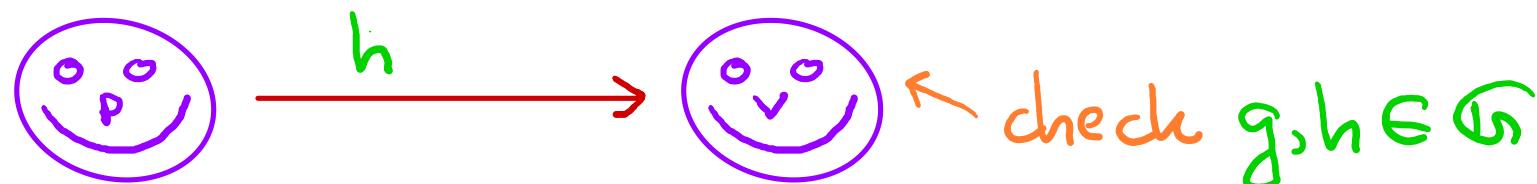
Pietrzak's VDF

Halving subprotocol:



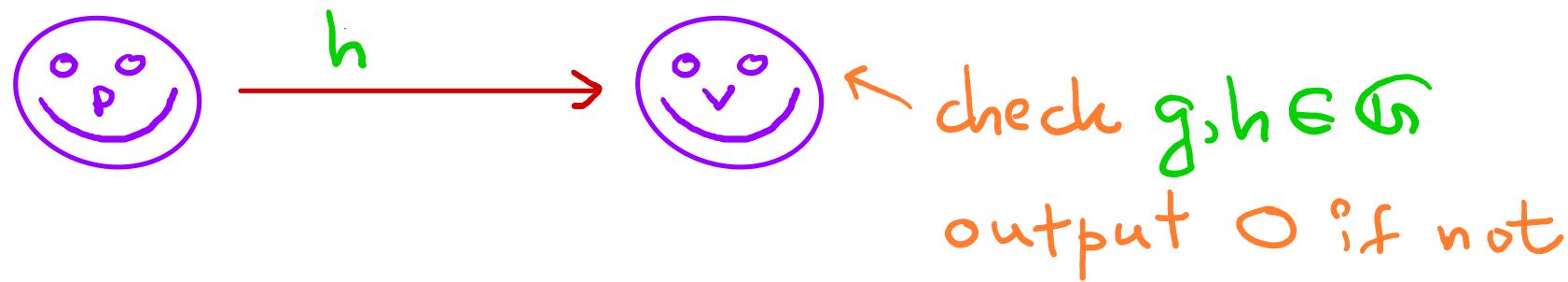
Pietrzak's VDF

Halving subprotocol:



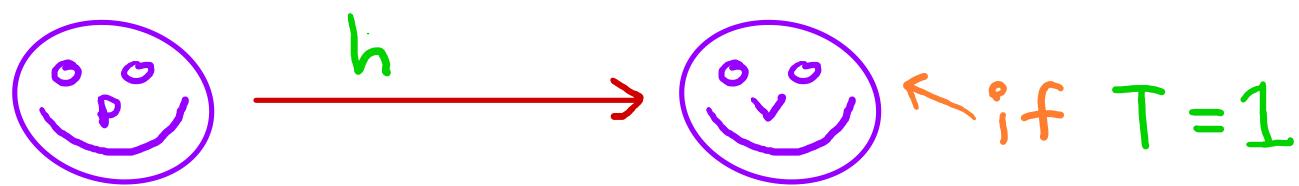
Pietrzak's VDF

Halving subprotocol:



Pietrzak's VDF

Halving subprotocol:



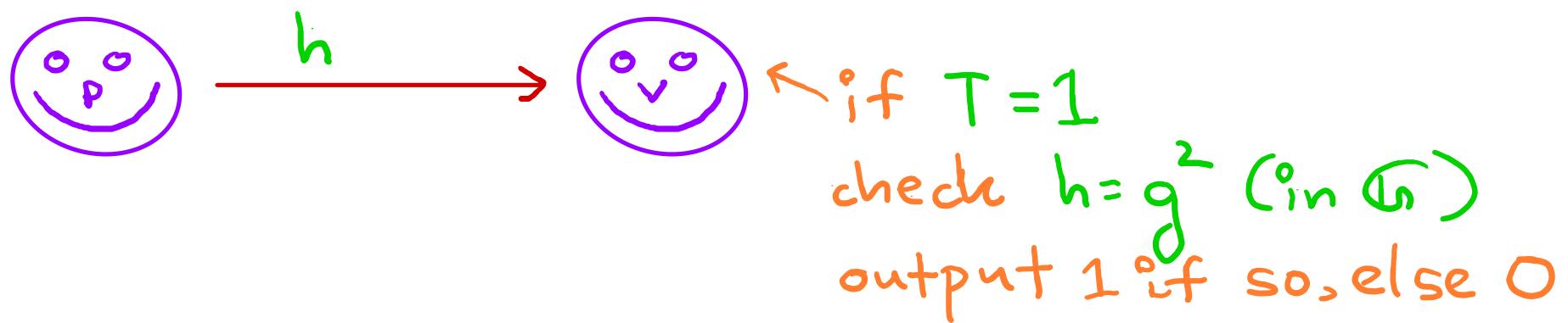
Pietrzak's VDF

Halving subprotocol:



Pietrzak's VDF

Halving subprotocol:



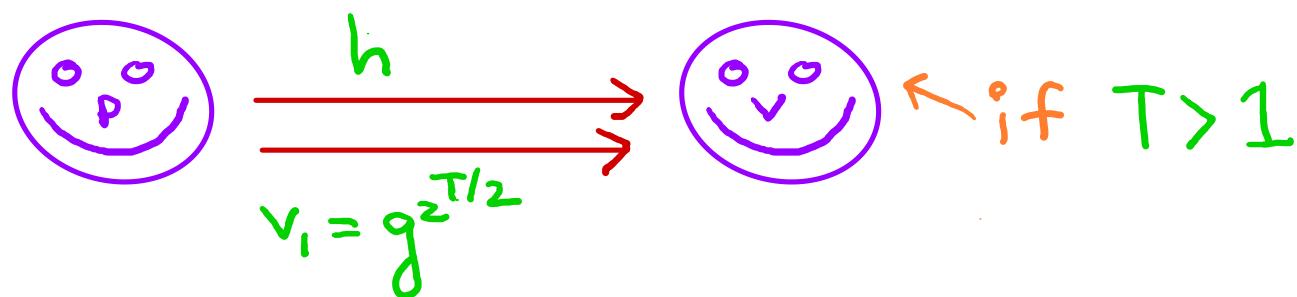
Pietrzak's VDF

Halving subprotocol:



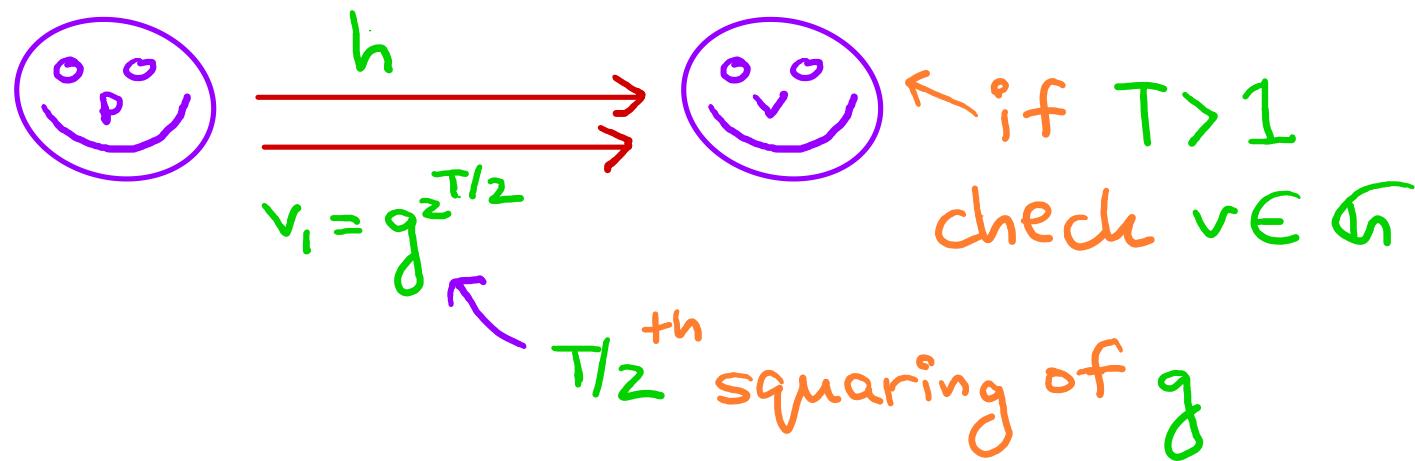
Pietrzak's VDF

Halving subprotocol:



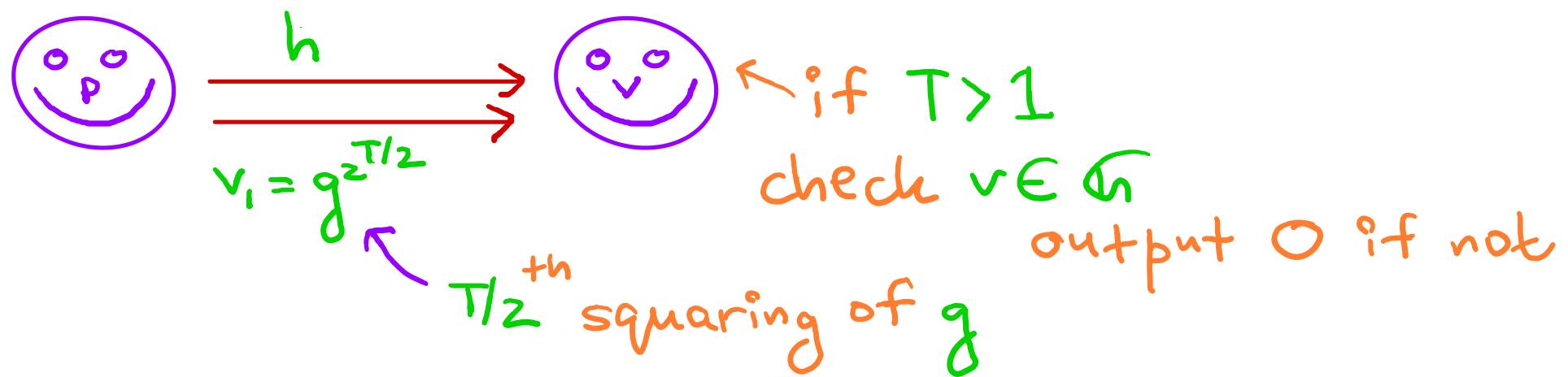
Pietrzak's VDF

Halving subprotocol:



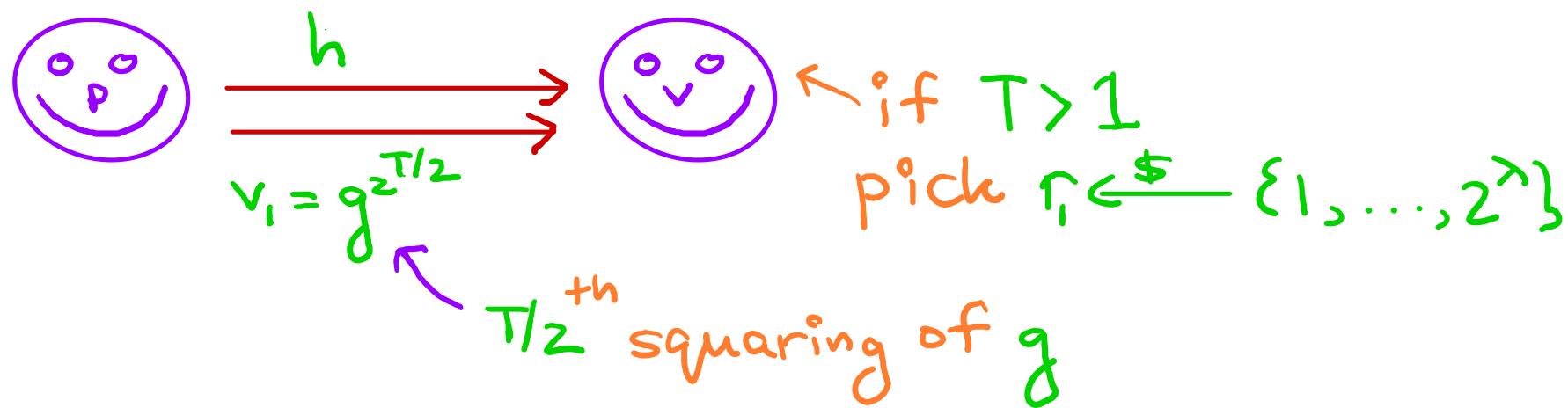
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Halving subprotocol:



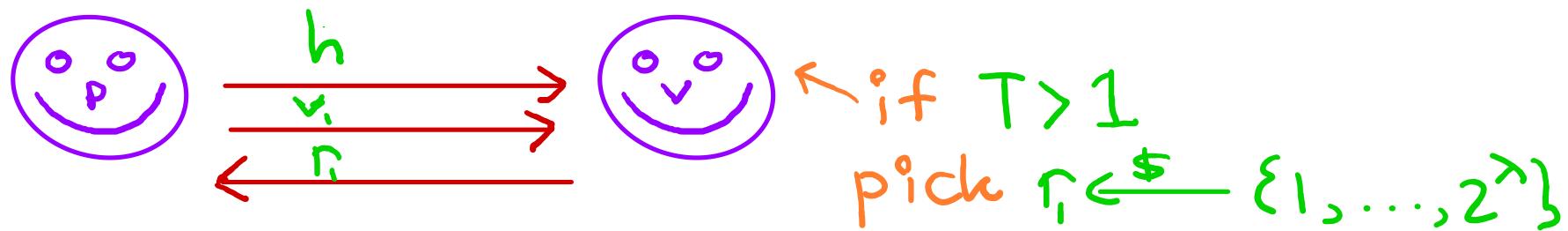
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Halving subprotocol:



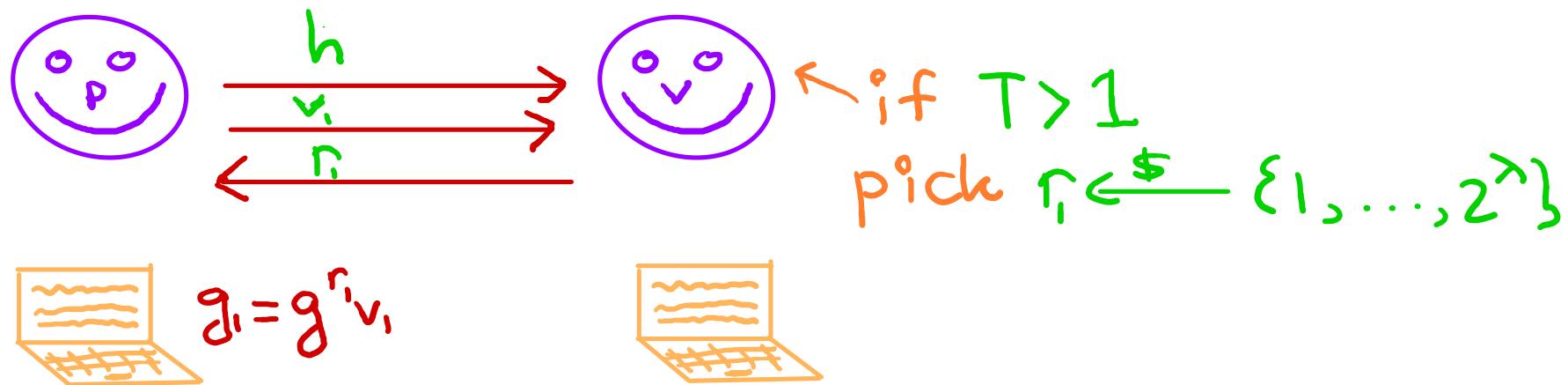
Pietrzak's VDF

Halving subprotocol:



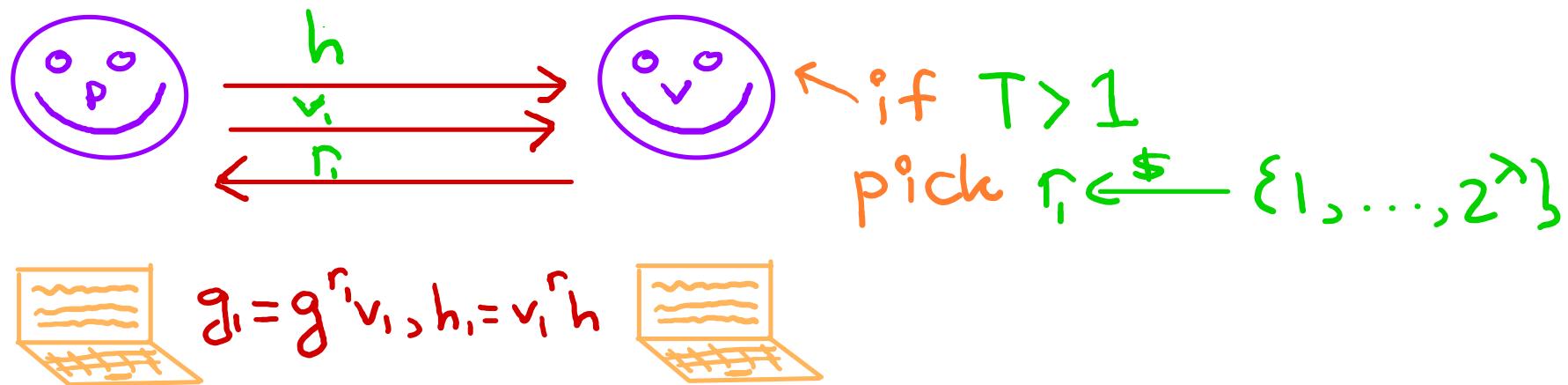
Pietrzak's VDF

Halving subprotocol:



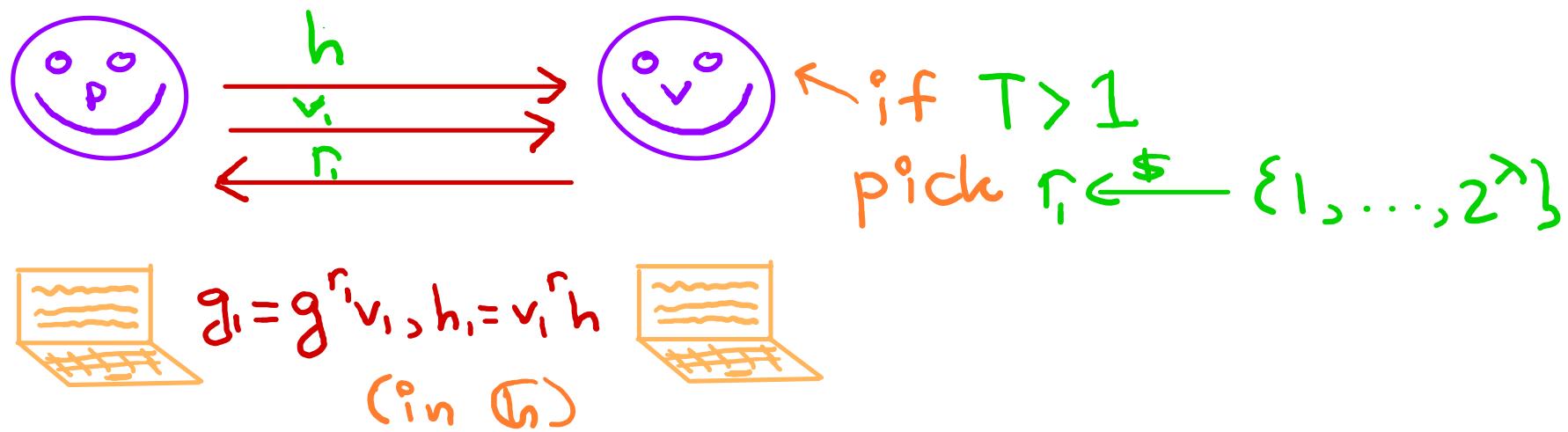
Pietrzak's VDF

Halving subprotocol:



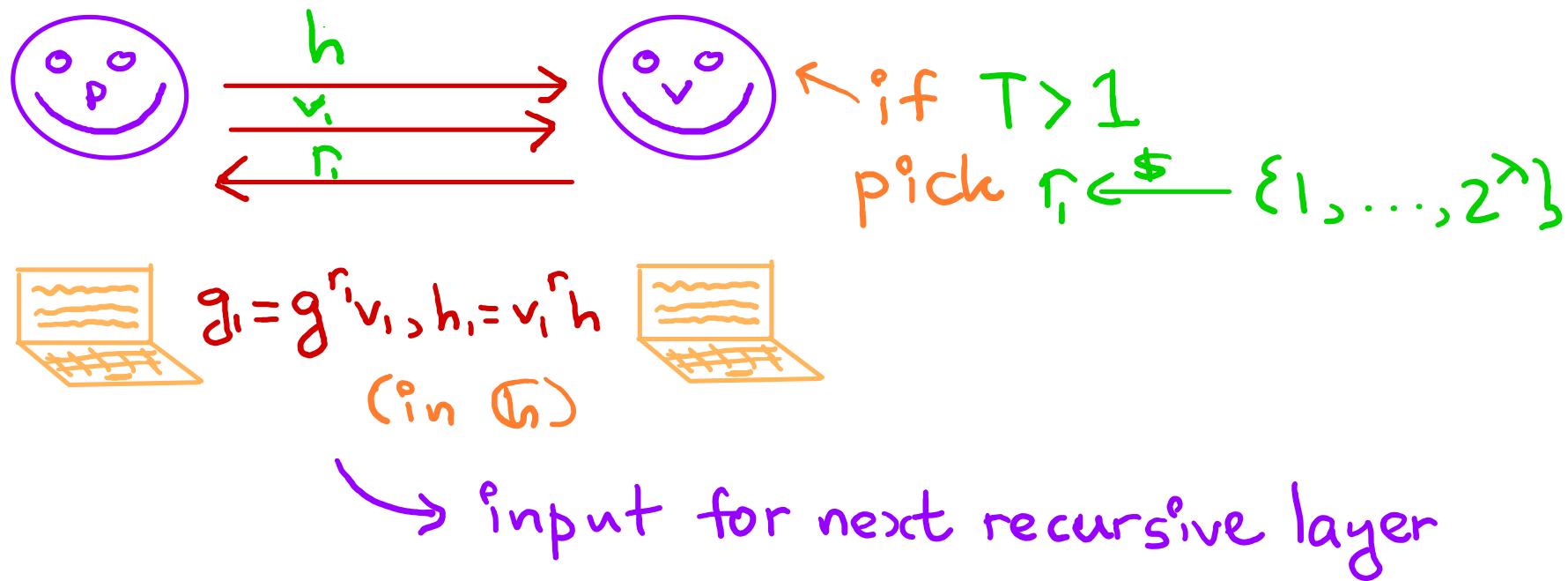
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Pietrzak's VDF

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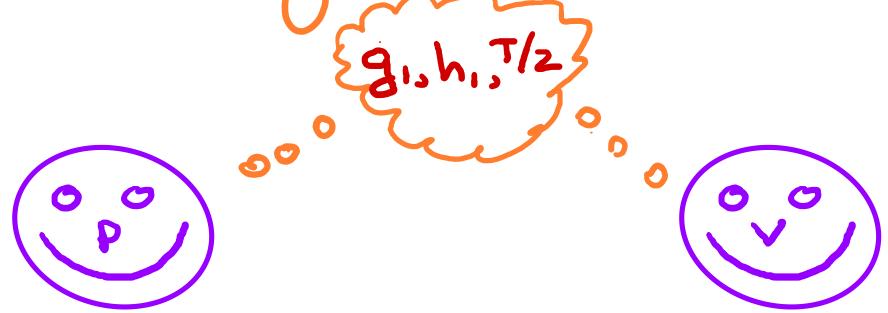
Pietrzak's VDF

Halving subprotocol:



Pietrzak's VDF

Halving subprotocol:



if $T = 1$

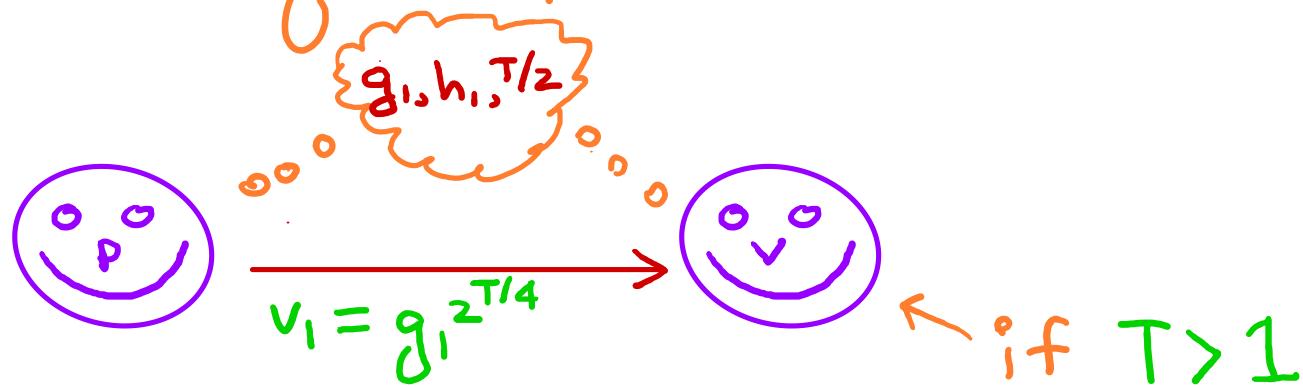
check $h_i = g_i^2$ (in \mathbb{G})

output 1 if so, else 0

STOP

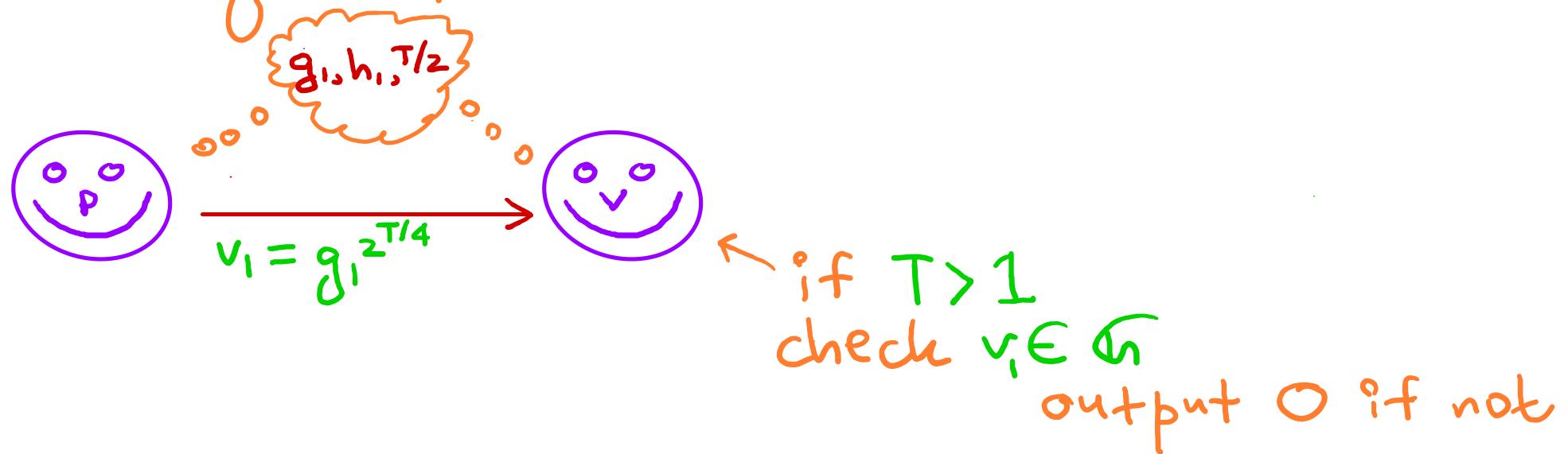
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Halving subprotocol:



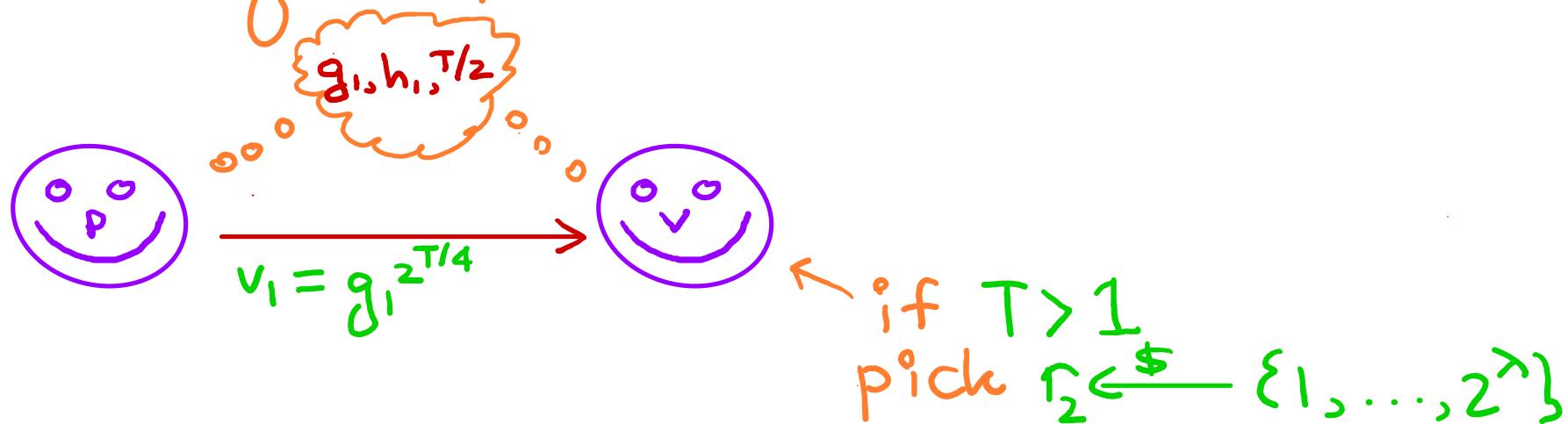
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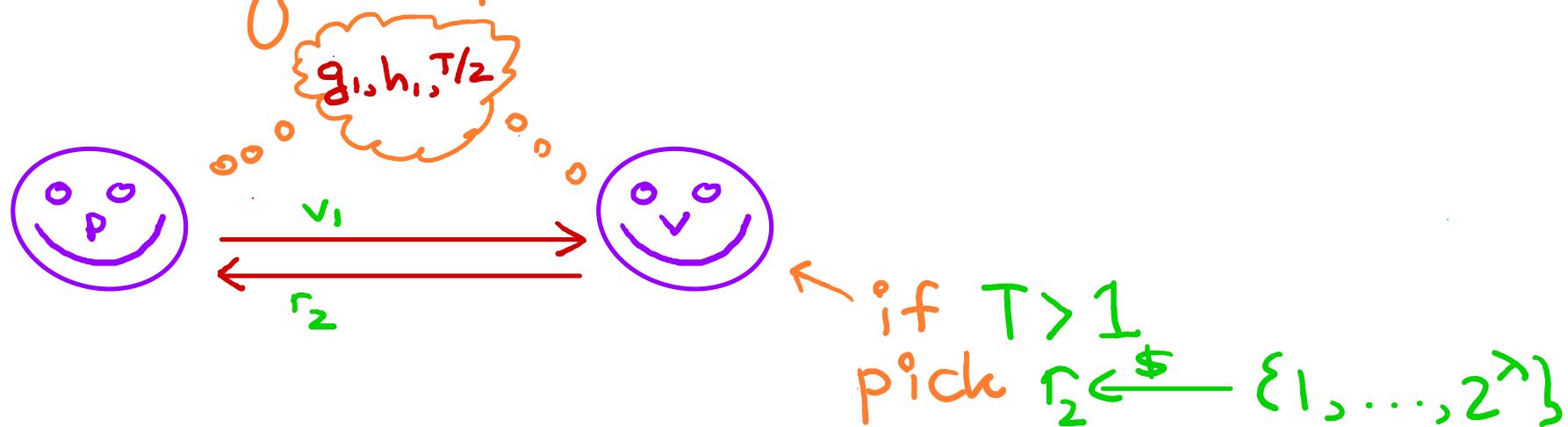
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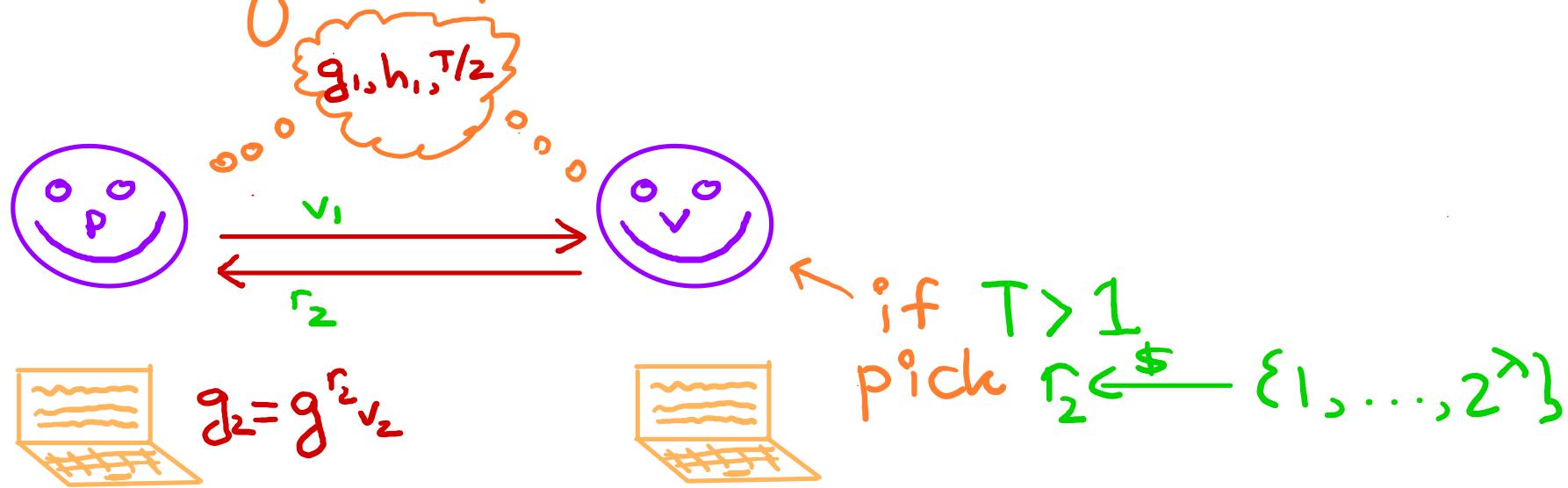
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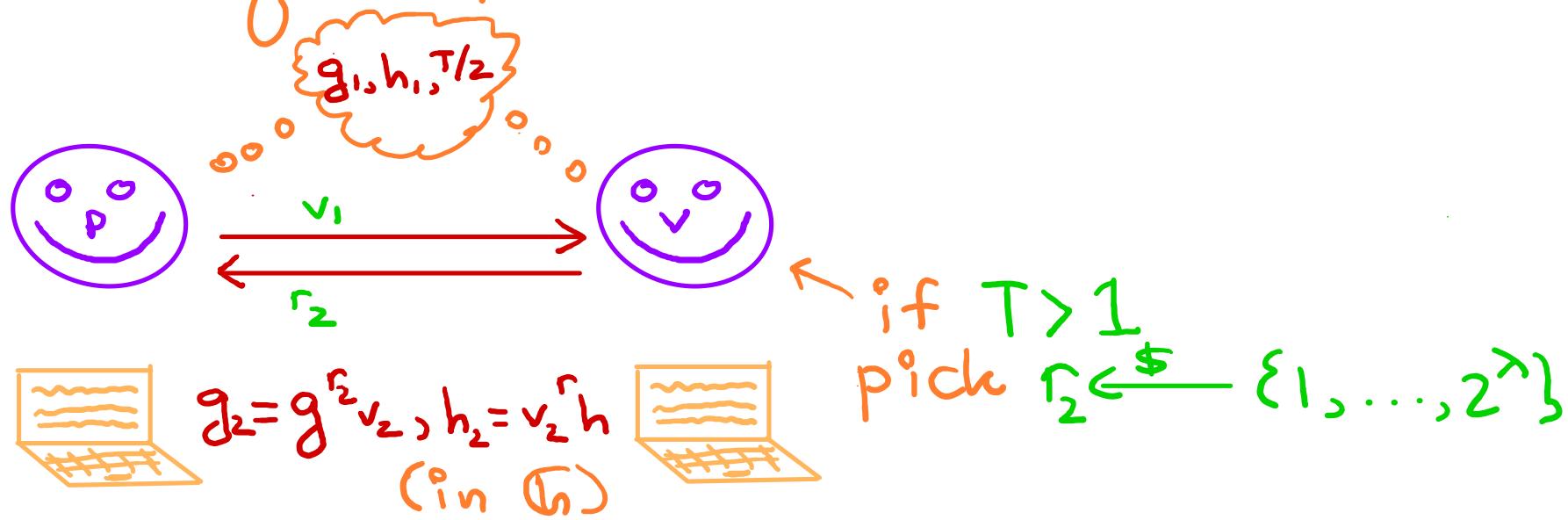
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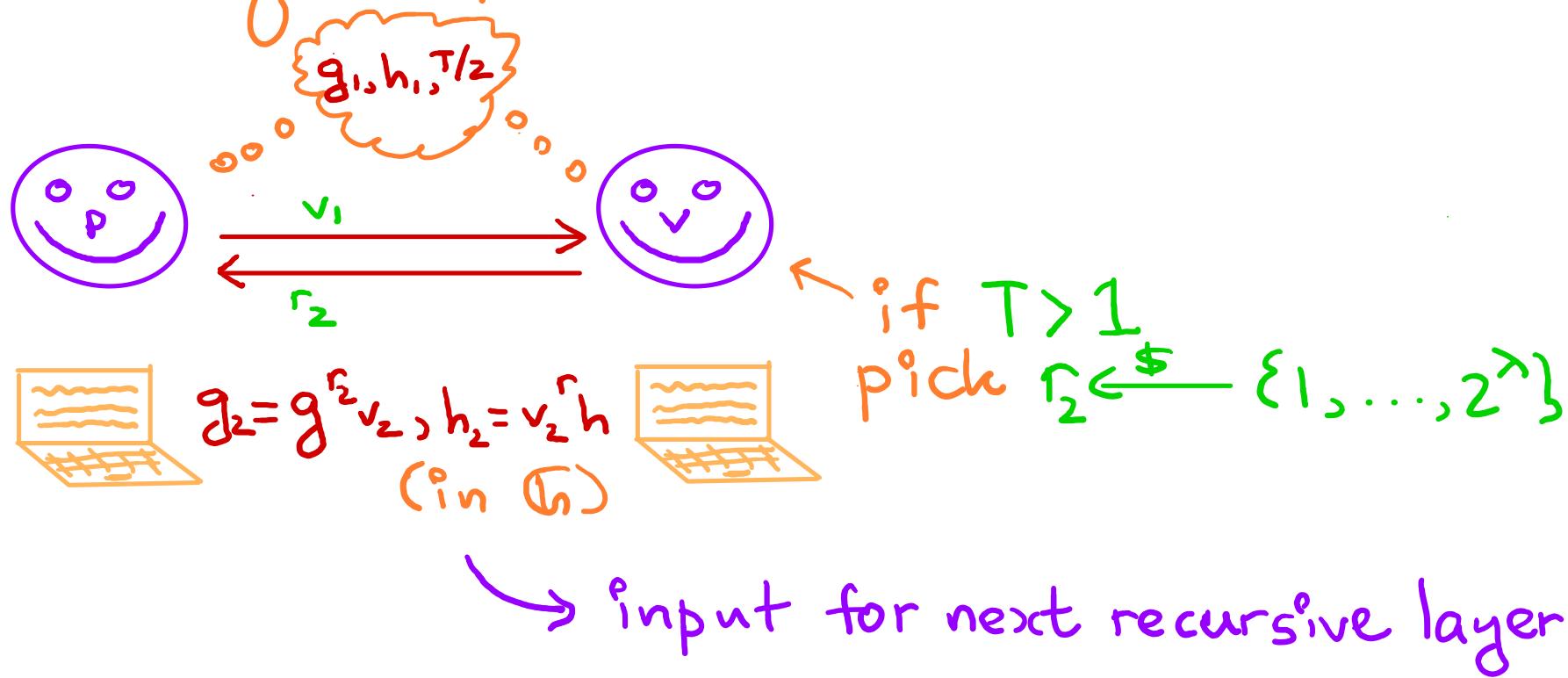
Pietrzak's VDF

Halving subprotocol:



Pietrzak's VDF

Halving subprotocol:



Pietrzak's VDF

Halving subprotocol:



Pietrzak's VDF

Halving subprotocol:



rinse and repeat!

Pietrzak's VDF

Halving subprotocol:



rinse and repeat!

$\log_2 T$ rounds

Pietrzak's VDF

Halving subprotocol:



rinse and repeat!

$\log_2 T$ rounds → can be made non-interactive

Pietrzak's VDF

Halving subprotocol:



rinse and repeat!

$\log_2 T$ rounds → can be made non-interactive
(Fiat-Shamir in ROM)

[FS'86]

Pietrzak's VDF

Halving subprotocol:



rinse and repeat!

$\log_2 T$ rounds → can be made non-interactive
(Fiat-Shamir in ROM)

[FS'86]

then Π has $\log_2 T$ elements

Pietrzak's VDF

Halving subprotocol:



rinse and repeat!

$\log_2 T$ rounds → can be made non-interactive
(Fiat-Shamir in ROM)

[FS'86]

then Π has $\log_2 T$ elements

let's unwind the recursion for $T=8$!

Pietrzak's VDF

Halving subprotocol for $T=8$:

Pietrzak's VDF

Halving subprotocol for $T=8$:

$$h = g^{2^8}$$

Pietrzak's VDF

Halving subprotocol for $T=8$:

$$h = g^{2^8} = g^{256}$$

Pietrzak's VDF

Halving subprotocol for $T=8$:

$$h = g^{2^8} = g^{256}$$

$$v_1 = g^{2^{8/2}}$$

Pietrzak's VDF

Halving subprotocol for $T=8$:

$$h = g^{2^8} = g^{256}$$

$$v_1 = g^{2^{8/2}} = g^{16}$$

$$P \xrightarrow{v_1} V$$

Pietrzak's VDF

Halving subprotocol for $T=8$:

$$h = g^{2^8} = g^{256}$$

$$v_1 = g^{2^{8/2}} = g^{16}$$

$$P \xrightarrow{v_1} V$$

Pietrzak's VDF

Halving subprotocol for $T=8$:

$$h = g^{2^8} = g^{256}$$

$$v_1 = g^{2^{8/2}} = g^{16}$$

$$P \xrightarrow[r_1]{v_1} V$$

$$g_1 = g^{r_1} v_1$$

Pietrzak's VDF

Halving subprotocol for $T=8$:

$$h = g^{2^8} = g^{256}$$
$$v_1 = g^{2^{8/2}} = g^{16}$$

$$P \xrightarrow[r_1]{v_1} V$$

$$g_1 = g^{r_1} v_1 = g^{r_1} \cdot g^{16} = g^{r_1 + 16}$$

Pietrzak's VDF

Halving subprotocol for $T=8$:

$$h = g^{2^8} = g^{256}$$

$$v_1 = g^{2^{8/2}} = g^{16}$$

$$P \xrightarrow[r_1]{v_1} V$$

$$g_1 = g^{r_1} v_1 = g^{r_1} \cdot g^{16} = g^{r_1 + 16}$$

$$h_1 = v_1^{r_1} h$$

Pietrzak's VDF

Halving subprotocol for $T=8$:

$$h = g^{2^8} = g^{256}$$

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$$h_1 = v_1^{r_1} h = g^{16r_1} \cdot g^{256} = g^{16r_1 + 256}$$

Pietrzak's VDF

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$$g_1 = g^{r_1} v_1 = g^{r_1} \cdot g^{16} = g^{r_1 + 16}$$

$$h_1 = v_1^{r_1} h = g^{16r_1} \cdot g^{256} = g^{16r_1 + 256}$$

input for next recursive layer:

$$T=4, g_1, h_1$$

Pietrzak's VDF

Halving subprotocol for T=4 :

$$g_1 = g^{r_i + 16}$$

$$h_1 = g^{16r_i + 256}$$

$$v_2 = g_1^{2^{4/2}}$$

Pietrzak's VDF

Halving subprotocol for T=4 :

$$g_1 = g^{r_i + 16}$$

$$h_1 = g^{16r_i + 256}$$

$$v_2 = g_1^{2^{4/2}} = (g^{r_i + 16})^4 = g^{4r_i + 64}$$

Pietrzak's VDF

Halving subprotocol for T=4 :

$$g_1 = g^{r_i + 16}$$

$$h_1 = g^{16r_i + 256}$$

$$v_2 = g_1^{2^{4/2}} = (g^{r_i + 16})^4 = g^{4r_i + 64}$$

$$P \xrightarrow{v_2} V$$

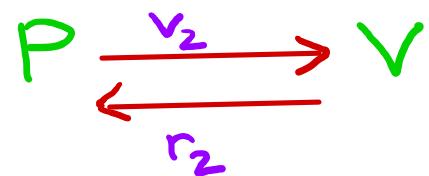
Pietrzak's VDF

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Pietrzak's VDF

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$$P \xrightarrow[r_2]{v_2} V \quad g_2 = g_1^{r_2} v_2$$

Pietrzak's VDF

Halving subprotocol for T=4 :

$$g_1 = g^{r_1 + 16}$$

$$h_1 = g^{16r_1 + 256}$$

$$v_2 = g_1^{2^{4/2}} = (g^{r_1 + 16})^4 = g^{4r_1 + 64}$$

$$\begin{array}{ccc} P & \xrightarrow[r_2]{v_2} & \checkmark \\ & \xleftarrow[r_2]{} & \end{array} \quad g_2 = g_1 v_2 = g^{r_1 r_2 + 16r_2} \cdot g^{4r_1 + 64} \\ = g^{4r_1 + r_1 r_2 + 16r_2 + 64}$$

Pietrzak's VDF

Halving subprotocol for T=4 :

$$g_1 = g^{r_1 + 16}$$

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$$h_2 = v_2^{r_2} h_1$$

Pietrzak's VDF

Halving subprotocol for T=4 :

$$g_1 = g^{r_1 + 16}$$

$$h_1 = g^{16r_1 + 256}$$

$$v_2 = g_1^{2^{4/2}} = (g^{r_1 + 16})^4 = g^{4r_1 + 64}$$

$$P \xrightarrow[r_2]{v_2} V \quad g_2 = g_1^{r_2} v_2 = g^{r_1 r_2 + 16r_2} \cdot g^{4r_1 + 64}$$

$$= g^{4r_1 + r_1 r_2 + 16r_2 + 64}$$

$$h_2 = v_2^{r_2} h_1 = g^{4r_1 r_2 + 64r_2} \cdot g^{16r_1 + 256}$$

$$= g^{16r_1 + 4r_1 r_2 + 64r_2 + 256}$$

Pietrzak's VDF

Halving subprotocol for $T=4$:

$$g_1 = g^{r_1 + 16}$$

$$h_1 = g^{16r_1 + 256}$$

$$v_2 = g_1^{2^{4/2}} = (g^{r_1 + 16})^4 = g^{4r_1 + 64}$$

$$\begin{array}{ccc} P & \xrightarrow[r_2]{v_2} & \checkmark \\ & \xleftarrow[r_2]{} & \end{array} \quad g_2 = g_1 v_2 = g^{r_1 r_2 + 16r_2} \cdot g^{4r_1 + 64} \\ = g^{4r_1 + r_1 r_2 + 16r_2 + 64}$$

$$\begin{aligned} h_2 = v_2^{r_2} h_1 &= g^{4r_1 r_2 + 64r_2} \cdot g^{16r_1 + 256} \\ &= g^{16r_1 + 4r_1 r_2 + 64r_2 + 256} \end{aligned}$$

input for next recursive layer:

$$T=2, g_2, h_2$$

Pietrzak's VDF

Halving subprotocol for T=2 :

$$g_2 = g^{4r_1 + r_1r_2 + 16r_2 + 64}$$

$$h_2 = g^{16r_1 + 4r_1r_2 + 64r_2 + 256}$$

$$v_3 = g_2^{z_{12}}$$

Pietrzak's VDF

Halving subprotocol for T=2:

$$g_2 = g^{4r_1 + r_1r_2 + 16r_2 + 64}$$

$$h_2 = g^{16r_1 + 4r_1r_2 + 64r_2 + 256}$$

$$v_3 = g_2^{z_{12}} = g^{8r_1 + 2r_1r_2 + 32r_2 + 128}$$

$$P \xrightarrow{v_3} V$$

Pietrzak's VDF

Halving subprotocol for T=2:

$$g_2 = g^{4r_1 + r_1r_2 + 16r_2 + 64}$$

$$h_2 = g^{16r_1 + 4r_1r_2 + 64r_2 + 256}$$

$$v_3 = g_2^{z_{12}} = g^{8r_1 + 2r_1r_2 + 32r_2 + 128}$$

$$\begin{array}{ccc} P & \xrightarrow{v_3} & V \\ & \xleftarrow{r_3} & \end{array} \quad g_3 = g_2^{r_3} v_3$$

Pietrzak's VDF

Halving subprotocol for T=2:

$$g_2 = g^{4r_1 + r_1 r_2 + 16r_2 + 64}$$

$$h_2 = \frac{16r_1 + 4r_1r_2 + 64r_2 + 256}{g}$$

$$v_3 = \frac{g^2}{g_2^{1/2}} = g^{8r_1 + 2r_1r_2 + 32r_2 + 128}$$

$$P \xrightarrow[r_3]{v_3} V \quad g_3 = g_2 v_3 = g^{4r_1 r_3 + r_1 r_2 r_3 + 16r_2 r_3 + 64r_3} \cdot g^{8r_1 + 2r_1 r_2 + 32r_2 + 128} \\ = g^{8r_1 + 32r_2 + 64r_3 + 2r_1 r_2 + 4r_1 r_3 + 16r_2 r_3 + r_1 r_2 r_3 + 128}$$

Pietrzak's VDF

Halving subprotocol for T=2:

$$g_2 = g^{4r_1 + r_1r_2 + 16r_2 + 64}$$

$$h_2 = g^{16r_1 + 4r_1r_2 + 64r_2 + 256}$$

$$v_3 = g_2^{z_{12}} = g^{8r_1 + 2r_1r_2 + 32r_2 + 128}$$

$$\begin{array}{ccc} P & \xrightarrow{v_3} & V \\ & \xleftarrow{r_3} & \end{array} \quad g_3 = g_2^{r_3} \quad v_3 = g^{4r_1r_3 + r_1r_2r_3 + 16r_2r_3 + 64r_3} \\ = g^{8r_1 + 2r_1r_2 + 32r_2 + 128} \\ = g^{8r_1 + 32r_2 + 64r_3 + 2r_1r_2 + 4r_1r_3 + 16r_2r_3 + r_1r_2r_3 + 128}$$

$$h_3 = v_3^{r_3} h_2$$

Pietrzak's VDF

Halving subprotocol for T=2:

$$g_2 = g^{4r_1 + r_1r_2 + 16r_2 + 64}$$

$$h_2 = g^{16r_1 + 4r_1r_2 + 64r_2 + 256}$$

$$v_3 = g_2^{z_{12}} = g^{8r_1 + 2r_1r_2 + 32r_2 + 128}$$

$$\begin{array}{ccc} P & \xrightarrow{v_3} & V \\ & \xleftarrow{r_3} & \end{array} \quad g_3 = g_2^{r_3} \quad v_3 = g^{4r_1r_3 + r_1r_2r_3 + 16r_2r_3 + 64r_3} \\ = g^{8r_1 + 2r_1r_2 + 32r_2 + 128} \\ = g^{8r_1 + 32r_2 + 64r_3 + 2nr_2 + 4r_1r_3 + 16r_2r_3 + r_1r_2r_3 + 128}.$$

$$h_3 = v_3^{r_3} \quad h_2 = g^{8n r_3 + 2n r_1 r_2 r_3 + 32 r_2 r_3 + 128 r_3}.$$

$$g^{16r_1 + 4r_1r_2 + 64r_2 + 256} \\ = g^{16r_1 + 64r_2 + 128r_3 + 4nr_2 + 8r_1r_3 + 32r_2r_3 + 2nr_2r_3 + 256}$$

Pietrzak's VDF

Halving subprotocol for $T=2$:

$$g_2 = g^{4r_1 + r_1r_2 + 16r_2 + 64} \quad \text{input for final recursive layer:}$$

$$h_2 = g^{16r_1 + 4r_1r_2 + 64r_2 + 256}$$

$$v_3 = g_2^{z_{12}} = g^{8r_1 + 2r_1r_2 + 32r_2 + 128}$$

$$\begin{array}{ccc} P & \xrightarrow{v_3} & V \\ & \xleftarrow{r_3} & \end{array} \quad g_3 = g_2^{r_3} \quad v_3 = g^{4r_1r_3 + r_1r_2r_3 + 16r_2r_3 + 64r_3} \\ = g^{8r_1 + 2r_1r_2 + 32r_2 + 128}.$$

$$= g^{8r_1 + 32r_2 + 64r_3 + 2nr_2 + 4r_1r_3 + 16r_2r_3 + r_1r_2r_3 + 128}$$

$$h_3 = v_3^{r_3} \quad h_2 = g^{8n r_3 + 2n r_1 r_2 r_3 + 32 r_2 r_3 + 128 r_3}.$$

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Pietrzak's VDF

Halving subprotocol for $T=1$:

$$g_3 = g^{8r_1 + 32r_2 + 64r_3 + 2nr_2 + 4nr_3 + 16r_2r_3 + r_1r_2r_3 + 128}$$

$$h_3 = g^{16r_1 + 64r_2 + 128r_3 + 4nr_2 + 8nr_3 + 32r_2r_3 + 2nr_2r_3 + 256}$$

base case \rightarrow check $h_3 = (g_3)^2$

Pietrzak's VDF

Halving subprotocol for $T=1$:

$$g_3 = g^{8r_1 + 32r_2 + 64r_3 + 2nr_2 + 4nr_3 + 16r_2r_3 + r_1r_2r_3 + 128}$$

$$h_3 = g^{16r_1 + 64r_2 + 128r_3 + 4nr_2 + 8nr_3 + 32r_2r_3 + 2nr_2r_3 + 256}$$

base case \rightarrow check $h_3 = (g_3)^2$

Yes, indeed!

Pietrzak's VDF

Halving subprotocol for $T=1$:

$$g_3 = g^{8r_1 + 32r_2 + 64r_3 + 2nr_2 + 4nr_3 + 16r_2r_3 + r_1r_2r_3 + 128}$$

$$h_3 = g^{16r_1 + 64r_2 + 128r_3 + 4nr_2 + 8nr_3 + 32r_2r_3 + 2nr_2r_3 + 256}$$

base case \rightarrow check $h_3 = (g_3)^2$

Yes, indeed! V outputs 1

Pietrzak's VDF

Halving subprotocol for $T=1$:

$$g_3 = g^{8r_1 + 32r_2 + 64r_3 + 2nr_2 + 4nr_3 + 16r_2r_3 + r_1r_2r_3 + 128}$$

$$h_3 = g^{16r_1 + 64r_2 + 128r_3 + 4nr_2 + 8nr_3 + 32r_2r_3 + 2nr_2r_3 + 256}$$

base case \rightarrow check $h_3 = (g_3)^2$

Yes, indeed! V outputs 1

(takes $2 \log_2 T$ exponentiations)

Pietrzak's VDF

Security:

Pietrzak's VDF

Security:

Can construct VDF assuming hardness of iterated squaring in \mathbb{G}

Pietrzak's VDF

Security:

Can construct VDF assuming hardness of iterated squaring in \mathbb{G} and ideal hash fn. (ROM)

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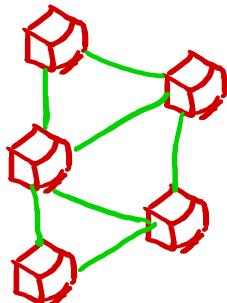
Applications:

Pietrzak's VDF

Security:

Can construct VDF assuming hardness of iterated squaring in \mathbb{G} and ideal hash fn. (ROM)

Applications:

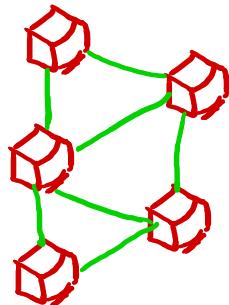


Pietrzak's VDF

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Applications:



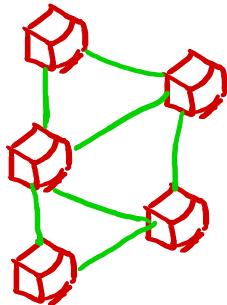
Chia network

Pietrzak's VDF

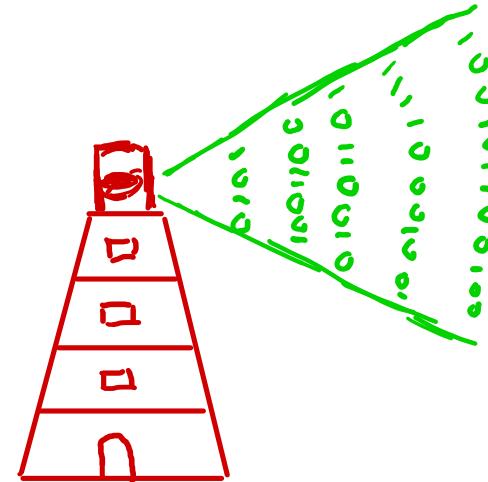
Security:

Can construct VDF assuming hardness of iterated squaring in \mathbb{G} and ideal hash fn. (ROM)

Applications:



Chia network



RandRunner

Thank

You!

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