

## HITACHI SEMICONDUCTOR TECHNICAL UPDATE

Classification of Production	MPU		No	TN-SH7-440A/E	Rev	1
THEME	PCIC Setup time specification change		Classification of Information	①. Spec change 2. Supplement of Documents 3. Limitation of Use 4. Change of Mask 5. Change of Production Line		
PRODUCT NAME	HD6417751R (only QFP package)	Lot No.	Reference Documents	SH7751 series Hardware Manual ADE-602-201B Rev.3.0		Effective Date
		ALL				Eternity

## 1. PCIC Setup time specification change

PCIC Setup time specifications of the QFP Package version of SH7751R are changed as follows:

Table1 Before change of PCIC Setup Time

(VDDQ=3.0 to 3.6V,VDD=1.5V,Ta= -20 to 75°C,CL=30pF)

Pin	Item	Symbol	33MHz		66MHz		Unit
			Min	Max	Min	Max	
IDSEL	Input Setup Time	tPCISU	3.0		3.0		ns
AD31 to AD0,C/BE3 to C/BE0, PAR,PCIFRAME,IRDY,TRDY, PCISTOP,PCILOCK, DEVSEL,PERR	Input Setup Time	tPCISU	3.0		3.0		ns
PCIREQ1/GNTIN, PCIREQ2/MD9,PCIREQ3/MD10, PEIREQ4/PCIGNT1/REQOUT, PCIGNT4 to PCIGNT1	Input Setup Time	tPCISU	3.0		3.0		ns

Table2 After change of PCIC Setup Time

(VDDQ=3.0 to 3.6V,VDD=1.5V,Ta= -20 to 75°C,CL=30pF)

Pin	Item	Symbol	33MHz		66MHz		Unit
			Min	Max	Min	Max	
IDSEL	Input Setup Time	tPCISU	3.5		3.5		ns
AD31 to AD0,C/BE3 to C/BE0, PAR,PCIFRAME,IRDY,TRDY, PCISTOP,PCILOCK, DEVSEL,PERR	Input Setup Time	tPCISU	3.5		3.5		ns
PCIREQ1/GNTIN, PCIREQ2/MD9,PCIREQ3/MD10, PEIREQ4/PCIGNT1/REQOUT, PCIGNT4 to PCIGNT1	Input Setup Time	tPCISU	3.5		3.5		ns

The Specification change is only for the QFP package.

The specification of the BGA package is not changed.