date: 2002/08/07

HITACHI SEMICONDUCTOR TECHNICAL UPDATE

Classification of Production	MPU			No	TN-SH7-361B/E	Rev	2 nd
THEME	SH-4 version register Classification of Information		Spec change Supplement of Documents Limitation of Use Change of Mask Change of Production Line				
PRODUCT NAME	SH7750, SH7750S, SH7750R, SH7751, SH7751R	Lot No.				ective Oate	
		ALL	Reference Documents		SH7750 series Hardware manual SH7751 series Hardware manual		у

The control registers of SH7750, SH7750S, SH7750R, SH7751, and SH7751R have added.

The additional registers are as follows.

Table1. PVR/PRR register

Name	Abbreviation	R/W	Initial	P4 address	Area7 address	Access
			value			size
PVR register	PVR	R	*1	H'FF000030	H'1F000030	32
PRR register	PRR	R	*1	H'FF000044	H'1F000044	32

^{*1:} Please refer the table 2.

Table 2. The initial value of PVR/PRR

Series	PVR *2	PRR *2
SH7750	H'040205xx	H'xxxxxxxx
SH7750S	H'040206xx	H'xxxxxxxx
SH7750R	H'040500xx	H'0000010x
SH7751	H'041100xx	H'xxxxxxxx
SH7751R	H'040500xx	H'0000011x

^{*2: &}quot;x" = undefined value