

HITACHI SEMICONDUCTOR TECHNICAL UPDATE

Classification of Production	MPU		No	TN-SH7-419A/E	Rev	1
THEME	Errata in AC Characteristics of SH7751R		Classification of Information	1. Spec change ② Supplement of Documents 3. Limitation of Use 4. Change of Mask 5. Change of Production Line		
PRODUCT NAME	SH7751R	Lot No.	Reference Documents	SH7751 series Hardware manual Rev 3.0 (ADE-602-201B)	Effective Date	
		ALL			Eternity	

There are some errata in the hardware manual.

The correct specifications are as follows.

1. Contents

(1)Current descriptions in the hardware manual

Table 23.12 Clock Timing (HD6417751RBP240)

Item		Symbol	Min	Typ	Max	Unit
Operating	CPU, FPU, cache, TLB	f	1	—	240	MHz
Frequency	External bus		1	—	84	
	Peripheral modules		1	—	60	

Table 23.19 Clock and Control Signal Timing (HD6417751RF240)

Item	Symbol	Min	Max	Unit
CKIO clock output	fop	1	84	MHz

(PLL1/PLL2 not operating)

(2)Correct specifications

Table 23.12 Clock Timing (HD6417751RBP240)

Item		Symbol	Min	Typ	Max	Unit
Operating	CPU, FPU, cache, TLB	f	1	—	240	MHz
frequency	External bus		1	—	120	
	Peripheral modules		1	—	60	

Table 23.19 Clock and Control Signal Timing (HD6417751RF240)

Item	Symbol	Min	Max	Unit
CKIO clock output	fop	1	34	MHz

(PLL1/PLL2 not operating)