HITACHI MICROCOMPUTER TECHNICAL UPDATE

DATE	4 July 2001	No.	TN-SH7-342A/E
THEME	Writing to ASID field of PTEH Register and Instruction Fetches after its Update		
CLASSIFICATION	☐ Spec change ■ Supplement of Documents	☐ Limitation on Use	
PRODUCT NAME	SH7750, SH7750S, SH7751	Lot : All	

REFERENCE DOCUMENTS	SH7750 Series Hardware Manual (- 5th Edition) SH7751 Series Hardware Manual (1st Edition) SH-4 Programming Manual (- 5th Edition)	Rev.	Effective Date
			Eternity

The following notice is added to the explanations of Page table entry high register (PTEH) in Section 3.2 in SH7750 Series Hardware Manual, SH7751 Series Hardware Manual, and SH-4 Programming Manual.

1. Contents

After updating the ASID field of PTEH register, a branch instruction to the P0, P3, or U0 area, where the new ASID value will be used, should be located at least 6 instructions after the PTEH update instruction.