

RENESAS TECHNICAL UPDATE

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Product Category	User Development Environment		Document No.	TN-OS*-080A/EA	Rev.	1.0
Title	HI7750/4 Updates		Information Category	Specification Change		
Applicable Product	HI7750/4 (HS0775ITI41SRE, HS0775ITI41SRB, HS0775ITI41SRS, HS0775ITI41SRE-E, HS0775ITI41SRB-E, HS0775ITI41SRS-E)	Lot No.	Reference Document	HI7000/4 series User's Manual REJ10B0060-0300H Rev.3.0		
		All version (V1.00r1, V1.01r1, V1.0Ar1, V1.0Br1, V1.0Cr1, V1.0.04, V1.0.05)				

The HI7750/4 is updated to Ver.1.1.00.

[Updates in Kernel]

- (1) Support SH-4A CPU core. The API of cache support service calls is changed for SH-4A.
- (2) Support Compiler package V8/HEW3 and Compiler package V7.1/HEW3. And the support of Compiler package V5.1/HEW1.2 is deleted.
- (3) Sample programs for SH7770 and SH7760 are added.
- (4) The company name notation in the source code is changed. T_RVER.maker, which is returned from the ref_ver system call, and macro TKERNEL_MAKER for configuring the kernel are modified from 0x000a to 0x0115.
- (5) T_RVER.prver, which is returned from the ref_ver system call, and macro TKERNEL_PRVER for configuring the kernel are modified from 0x0104 to 0x0110.

[Manuals]

The HI7000/4 series User's Manual is revised.

- Japanese : Rev.3 (ADJ-702-298B) --> Rev.4 (RJJ10B0063-0400H)
- English : Rev.2 (ADE-702-248A) --> Rev.3 (REJ10B0060-0300H)

For detail, refer to "List of Items Revised or Added for This Version" in the manual.

[Download]

The HI7750/4 V1.1.00 can be downloaded from following URL.

http://www.renesas.com/eng/products/mpumcu/tool/download/realtime_os/itron/hi7750_4_v1100/index.html