HITACHI MICROCOMPUTER TECHNICAL UPDATE

DATE	30 August 2001	No.	TN-SH7-349A/E

THEME	About initial value of bypass register.

CLASSIFICATION	☐ Spec change ■ Limitation on Use ☐ Supplement of Documents

PRODUCT NAME	HD6417750 HD6417750S HD6417751	Lot	Lot#:All

REFERENCE DOCUMENTS	SH7750 series Hardware Manual (Rev.1-4) SH7751 Hardware Manual (Rev.1-2)	Rev.	EffectiveDate Eternity

When using bypass mode of SH-4 H-UDI function, pay attention to the following item:

1. Contents
    In IEEE Std 1149.1, there is prescription on the bypass mode as follows.
    When the TAP controller transfers to Capture-DR State, the bypass register is set to logic 0.

    On the other hand, in SH-4,
    When the TAP controller transfers to Capture-DR State, the bypass register has an
    undefined value.

2. Workaround
    Do not transfer the data expecting the fact that the bypass register is set to logic 0 when the
    TAP controller transfers to Capture-DR State on bypass mode.