



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

ANANTHAPURAMU - 515 002, ANDHRA PRADESH, INDIA

CONSOLIDATED MARKS MEMO / CREDIT SHEET



CMM. No. : JAC 158781

Serial No. : 900187968

Name : ORUPALLI SRIKANTH

Course : ELECTRONICS & COMMUNICATION ENGINEERING

Admission No. : 113G1A0442

Year of Admission : 2011

Year of Completion : 2015

Name of the College KMMITS-RAMIREDDIPALLE

TIRUPATI

Class Awarded

*** First Class ***

S. No.	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS	S. No.	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS
--------	--------------	------------	------------	-------	---------	--------	--------------	------------	------------	-------	---------

I YEAR

1 ENGLISH	23	41	64	4	6 ENGINEERING DRAWING	25	27	52	6
2 ENGINEERING PHYSICS	22	25	47	4	7 MATHEMATICAL METHODS	16	26	42	6
3 ENGINEERING CHEMISTRY	19	27	46	4	8 C PROGRAMMING AND DATA STRUCTURES LAB	21	40	61	4
4 MATHEMATICS-I	27	25	52	6	9 ENGINEERING AND I.T. WORKSHOP	21	39	60	4
5 PROGRAMMING IN C AND DATA STRUCTURES	24	36	60	6	10 ENGINEERING PHYSICS AND ENGINEERING CHEMISTRY LAB	21	37	58	4
					11 ENGLISH LANGUAGE AND COMMUNICATION SKILLS LAB	20	44	64	4

I SEMESTER

II YEAR

II SEMESTER

1 MATHEMATICS-III	20	37	57	4	1 MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS	22	34	56	4
2 ENVIRONMENTAL SCIENCE	25	36	61	4	2 PRINCIPLES OF ELECTRICAL ENGINEERING	27	25	52	4
3 ELECTRICAL CIRCUITS	21	25	46	4	3 ELECTRONIC CIRCUIT ANALYSIS	17	37	54	4
4 PROBABILITY THEORY AND STOCHASTIC PROCESSES	26	32	58	4	4 PULSE AND DIGITAL CIRCUITS	24	25	49	4
5 ELECTRONIC DEVICES AND CIRCUITS	19	40	59	4	5 SWITCHING THEORY AND LOGIC DESIGN	28	34	62	4
6 SIGNALS AND SYSTEMS	25	28	53	4	6 ELECTROMAGNETIC THEORY AND TRANSMISSION LINES	29	25	54	4
7 ELECTRONIC DEVICES AND CIRCUITS LAB	21	40	61	2	7 ELECTRONIC CIRCUIT ANALYSIS LAB	22	33	55	2
8 BASIC SIMULATION LAB	23	32	55	2	8 ELECTRICAL ENGINEERING LAB	22	46	68	2

I SEMESTER

III YEAR

II SEMESTER

1 CONTROL SYSTEMS	23	35	58	4	1 DIGITAL COMMUNICATIONS	26	26	52	4
2 ANALOG COMMUNICATIONS	23	30	53	4	2 MICROPROCESSORS AND MICROCONTROLLERS	28	25	53	4
3 LINEAR IC APPLICATIONS	24	34	58	4	3 DIGITAL SIGNAL PROCESSING	23	25	48	4
4 ANTENNAS AND WAVE PROPAGATION	23	34	57	4	4 ELECTRONIC MEASUREMENTS AND INSTRUMENTATION	18	31	49	4
5 COMPUTER ORGANIZATION	25	25	50	4	5 VLSI DESIGN	27	18	45	0
6 DIGITAL IC APPLICATIONS	22	37	59	4	6 MICROWAVE ENGINEERING	25	36	61	4
7 LINEAR AND DIGITAL IC APPLICATIONS LAB	23	42	65	2	7 ANALOG AND DIGITAL COMMUNICATIONS LAB	20	43	63	2
8 PULSE AND DIGITAL CIRCUITS LAB	24	45	69	2	8 ADVANCED ENGLISH LANGUAGE COMMUNICATION SKILLS LAB	22	43	65	2

I SEMESTER

IV YEAR

II SEMESTER

1 MANAGEMENT SCIENCE	22	26	48	4	1 CELLULAR AND MOBILE COMMUNICATIONS	25	38	63	4
2 EMBEDDED REAL TIME OPERATING SYSTEMS	27	48	75	4	2 DIGITAL IMAGE PROCESSING	26	27	53	4
3 COMPUTER NETWORKS	26	25	51	4	3 SATELLITE COMMUNICATIONS	24	25	49	4
4 OPTICAL COMMUNICATIONS	23	36	59	4	4 WIRELESS COMMUNICATIONS AND NETWORKS	26	34	60	4
5 RADAR SYSTEMS	27	30	57	4	5 SEMINAR	45	-	45	2
6 DIGITAL DESIGN THROUGH VERILOG HDL	28	37	65	4	6 PROJECT WORK	55	137	192	10
7 MICROWAVE AND OPTICAL COMMUNICATIONS LAB	21	42	63	2					
8 MICROPROCESSORS AND DSP LAB	23	41	64	2					

Number of Credits registered for : 220

Total Credits Acquired : 216

Aggregate Marks Secured for best : 3264

Percentage of Marks : 62.77 %

CMM Issue Date : August 2015

(See overleaf for Instructions)

Max.Marks 5200

(*Courses registered but not counted for calculation of aggregate)

CONTROLLER OF EXAMINATIONS