SIMATS ENGINEERING

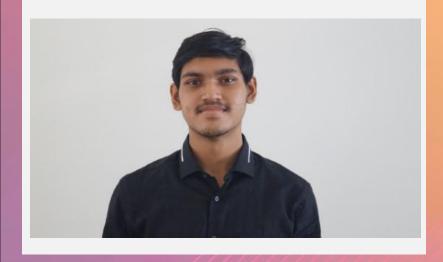
NAME: S.G.DEVSACHIN REGISTER NO: 192111088

Department: CSE

Ongoing Course Attendance

Slot B: EEA0184-Basic Electrical and

Electronics Engineering – 89%



Slot B	Total	Marks	Class Average
	Marks	Obtained	Mark
B Slot	University Theory Exam Completed		

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Dear Parent,

Greetings.

S.G. Devsachin, a committed student from the Department of Computer Science and Engineering (CSE), has consistently exemplified academic excellence and a strong sense of purpose in his career journey. With a commendable 89% attendance in EEA0184 – Basic Electrical and Electronics Engineering, he has successfully completed his Slot B exam. From 9th to 12th April 2025, he is scheduled to present his PDD project. Devsachin is widely recognized for his disciplined nature, obedience, and exemplary conduct, which distinguish him among his peers. His active participation in various national and international workshops reflects his eagerness to expand his horizons beyond the classroom. Passionate about continuous learning, he consistently seeks to upgrade his skills and stay aligned with current technological advancements. With a clear vision for the future, Devsachin remains focused on his academic and career goals. His unwavering dedication and perseverance have opened doors for higher studies and professional opportunities. A true inspiration to his classmates, he balances academics and extracurricular pursuits with remarkable efficiency. His strong work ethic, intellectual curiosity, and proactive approach to learning underscore his passion for engineering and innovation. As he continues to strive for excellence, there is no doubt that Devsachin will achieve great success and make valuable contributions to his field. Wishing him the very best for his PDD presentation and all future endeavors.

Thank you

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MENTEE RESPONSE

PARENT'S RESPONSE

I still need to work on many aspects so I'll make sure to keep myself updated.

Hope he gets better in terms of academics and bolster his career.