

SYCSA, 2024-28 Batch Sem-III									
Day/Time	8:45 - 9:45	9:45 - 10:45	10:45 - 11:45	11:45 - 12:45	12:45 - 1:40	1:40- 2:35	2:35 - 3:30	3:30 - 4:25	
Monday	DMGT	CO	SAML-A1-SDB-3rd Floor Embedded System Lab		B R E A K	PP	SAM	Extended Learning Hour	
	AK	VAH	PPL-A2-SMK-CL-06			SMK	SDB		
			DMGT_Tut-A3-AK-D2				401		
	401	401	DSL-A4-RRG-CL-05			401	DSL-A4-RRG-CL-05		
Tuesday	PP	DMGT	PPL-A1-SMK-CL-03			FOE	CO	Flexi-Credit Full Stack Development CL-09 and CL-10 AMK	
	SMK	AK	Library-A2			TD	VAH		
	401	405	DSL-A3-RRG-CL-04			401	401		
Wednesday	SAML-A2-SDB-3rd Floor Embedded System		Extended Learning Hour	SAM		DS	DSL-A2-RRG-CL-4		
	DSL-A1-RRG-CL-04			SDB		RRG	PPL-A4-DRV-CL-03		
	PPL-A3-SMK-CL-03			401			DMGT_Tut-A1-AK-D2		
	DMGT_Tut-A4-AK-D2					406	SAML-A3-SDB-3rd Floor Embedded System Lab		
Thursday	DS	DS	Flexi-Credit Full Stack Development CL-09 and CL-10 AMK			CO	SAM	EV	
	RRG	RRG				VAH	SDB	VM	
	401	401				401	401	401	
Friday	PP	DMGT	SAML-A4-SDB-3rd Floor Embedded System Lab			Extended Learning Hour	Mentor-Mentee Meeting		
	SMK	AK	DMGT_Tut-A2-AK-D1						
	401	401	Library-A1						
Saturday									

Subjects	Theory	Practical	Tut
DMGT	Discrete Mathematics and Graph Theory	Dr. Amit Kumar(AK)	
DMGT_Tut	Discrete Mathematics and Graph Theory		Dr. Amit Kumar(AK)-A1,A2,A3,A4
CO	Computer Organization	Dr.Vani Hiremani (VAH)	
DS	Data Structures	Dr. Rupali Rajendra Gangarde (RRG)	
DSL	Data Structures Lab		Dr. Rupali Rajendra Gangarde (RRG) - A1,A2, A3,A4
PP	Programming Paradigms	Dr. Sonali Kothari (SMK)	
PPL	Programming Paradigms Lab		Dr. Sonali Kothari(SMK))-A1, A2,A3 Dr. Deepali Vora(DRV)-A4
SAM	Sensors and Microcontrollers	Dr. Sudipta Banerjee (SDB)	
SAML	Sensors and Microcontrollers Lab		Dr. Sudipta Banerjee (SDB) - A1,A2,A3,A4
EV	Entrepreneurship	Mr. Vinay Maishe(VM)	
Flexi credit course-	Flexi-Credit Course (Full Stack Development)	Dr. Pravin Mundhe (PMD)	
FoE	Foundation of Ethics	Mr. Toshan Das (TD) (Dr. Sangeeta Pant - coordinator)	

Class Teacher : Dr. Nilima Zade

Dr. Aditi Sharma			Dr. Sashikala Mishra				Dr. Deepali Vora			
Timetable Incharge, CSE			HOD, CSE				Dy. Director Academics			