**Rubric for grading Lab1:**

Grading the verilog files: 70 points

Grading the report: 30 points

Running their testbench on the correct design. Total points here: 14

1- tb\_FA.v —> 3 points

(One point for each of the 3 test cases in the lab manual.)

2- tb\_FA4.v —> 5 points

(One point for each of the 4 test cases in the lab manual.)

3- tb\_mux21.v —> 2 points

(One point for each of the 2 test cases in the lab manual)

4- tb\_mux41.v —> 4 points

(One point for each 4 test cases in the lab manual)

Running our grading testbench on their design. Total points here: 56

1- FA we have 6 test cases

2- 4FA we have 10 test cases

3- MUX21 we have 4 test cases

4- MUX41 we have 8 test cases

In total we have 28 test cases. Each test case is 2 points. Hence, in total = 28 \* 2 = 56

Report: Total is 30 points

Correct ScreenShot for 4 bit FA with correct Sum and Cout—> 10 points

Correct ScreenShot for the Mux21 with correct Y—> 10 points

Correct ScreenShot for the Mux41 with correct Y —> 10 points