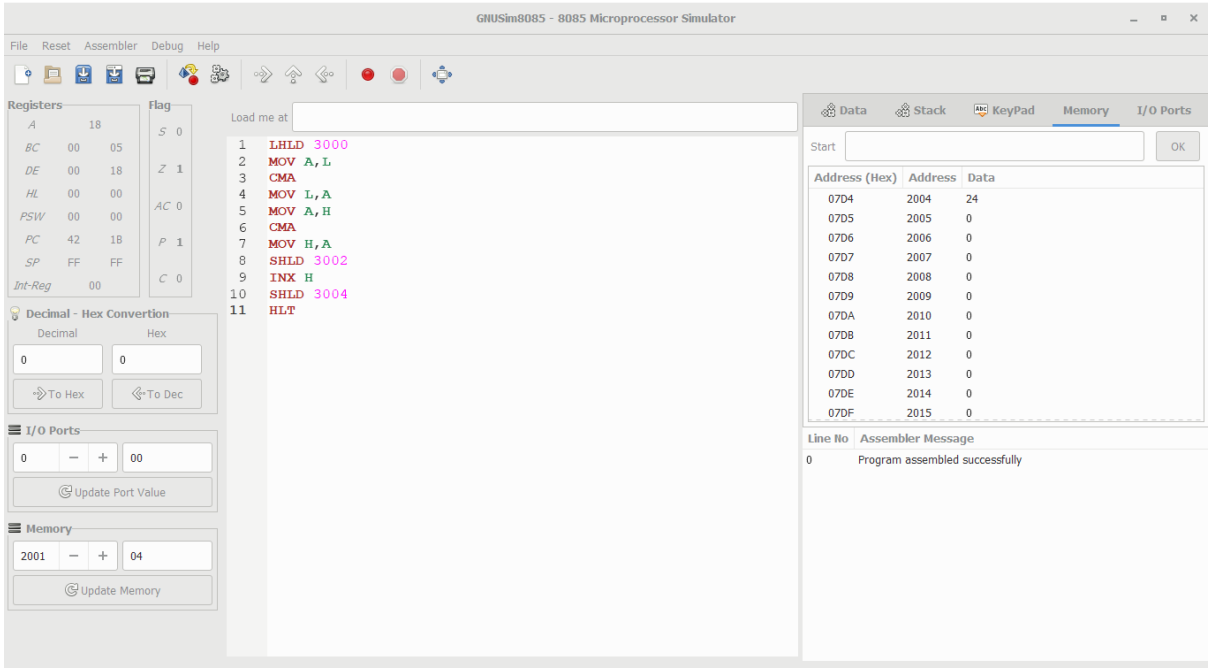
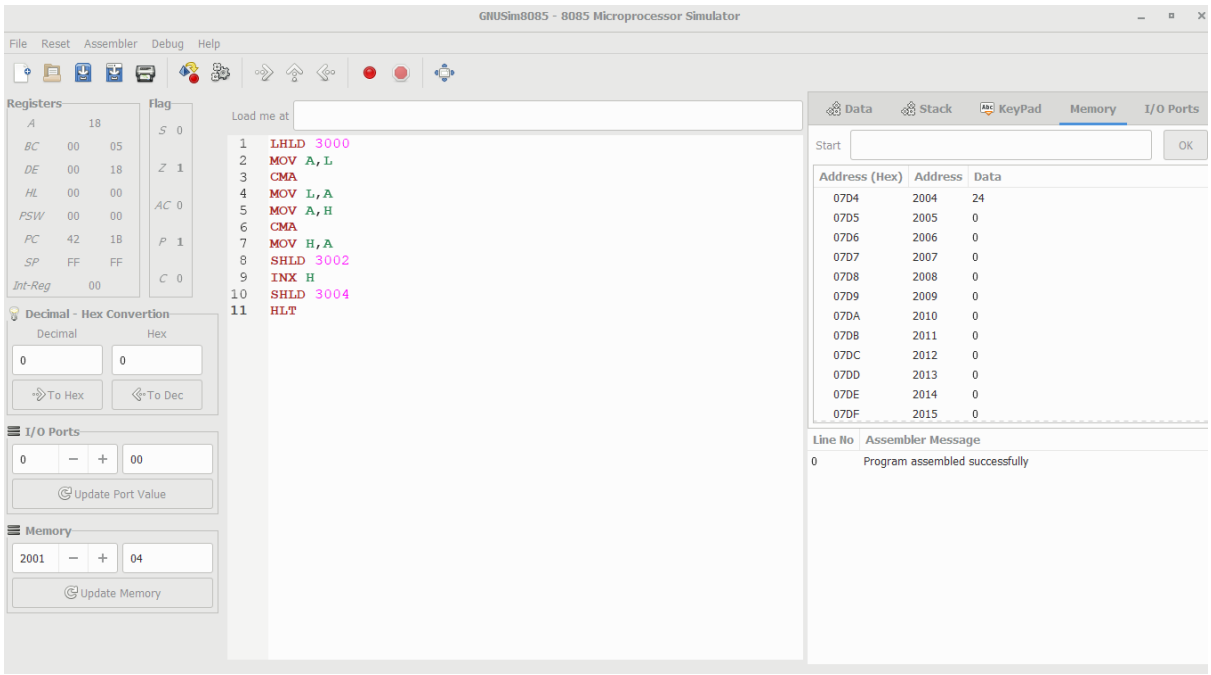
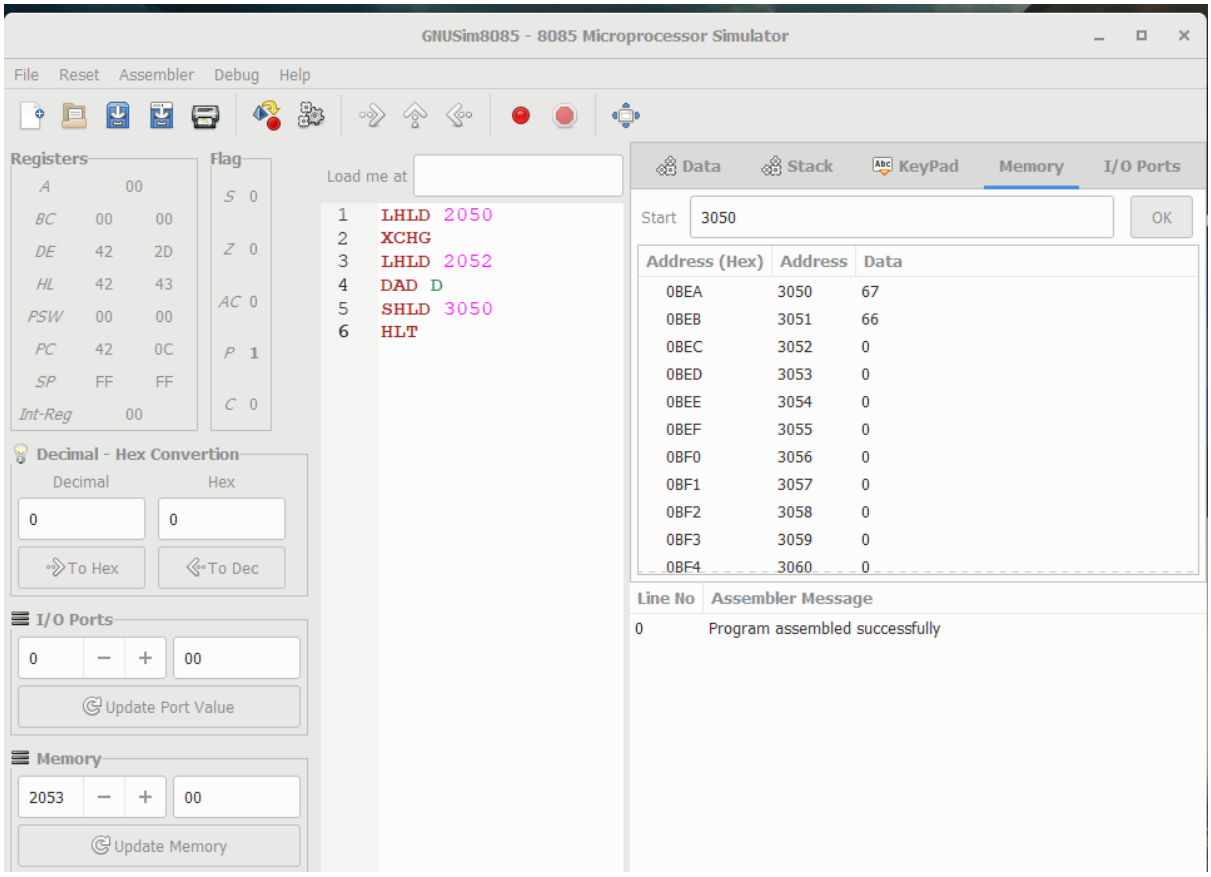
1'S AND 2'S COMPLEMENT FOR 16 BIT



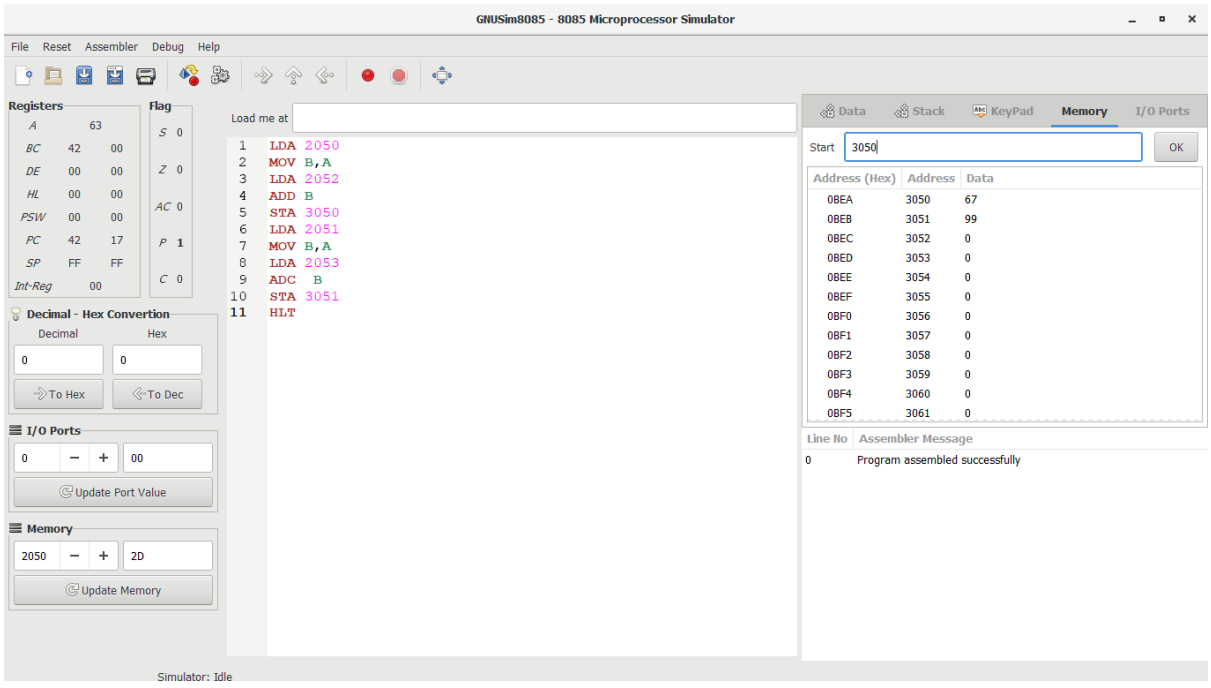
1'S AND 2'S COMPLEMENT FOR 8 BIT



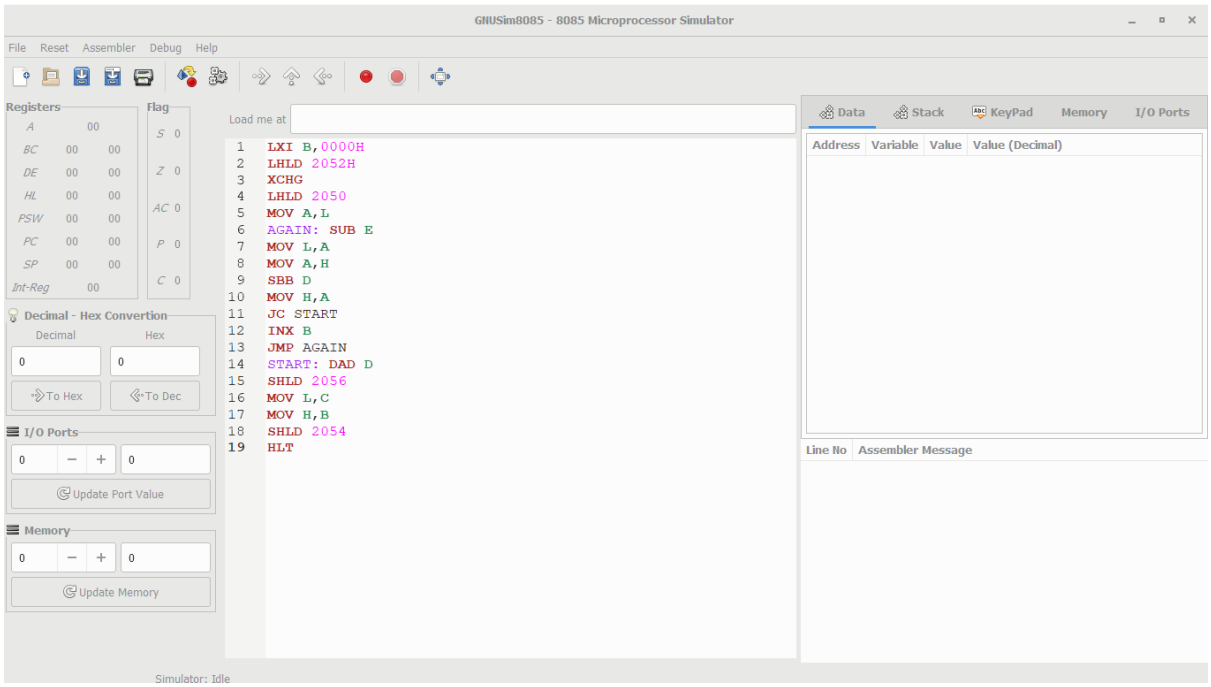
16 BIT ADDITION 2



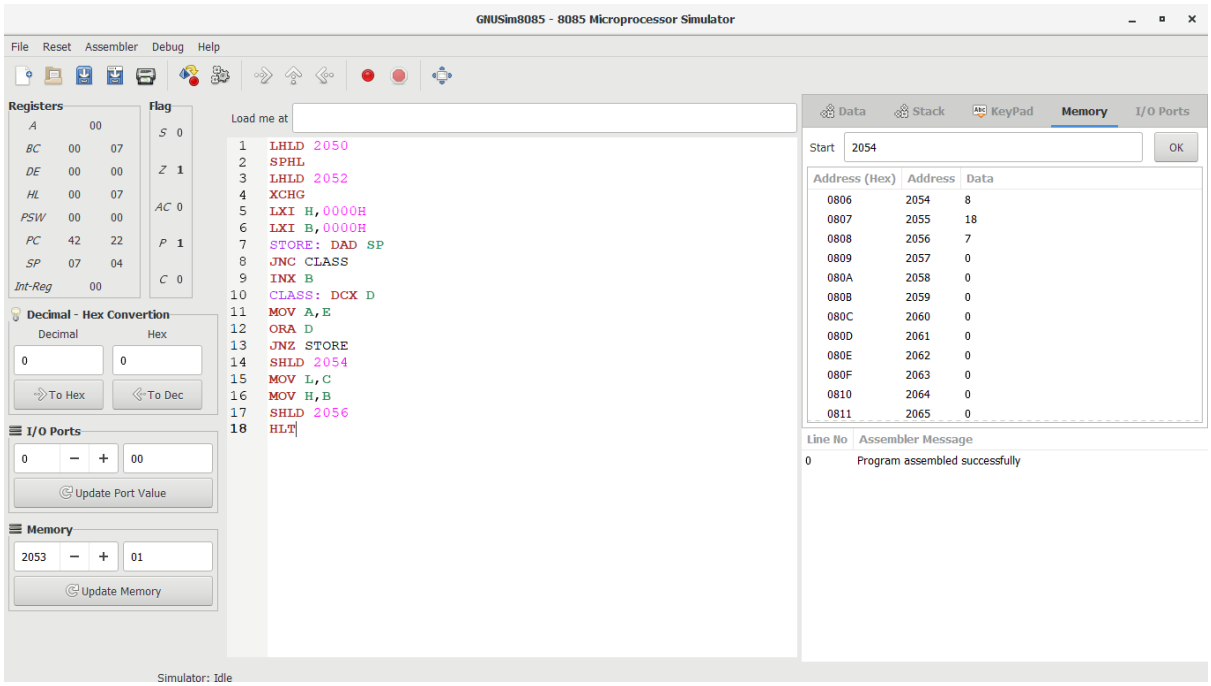
16 BIT ADDITION



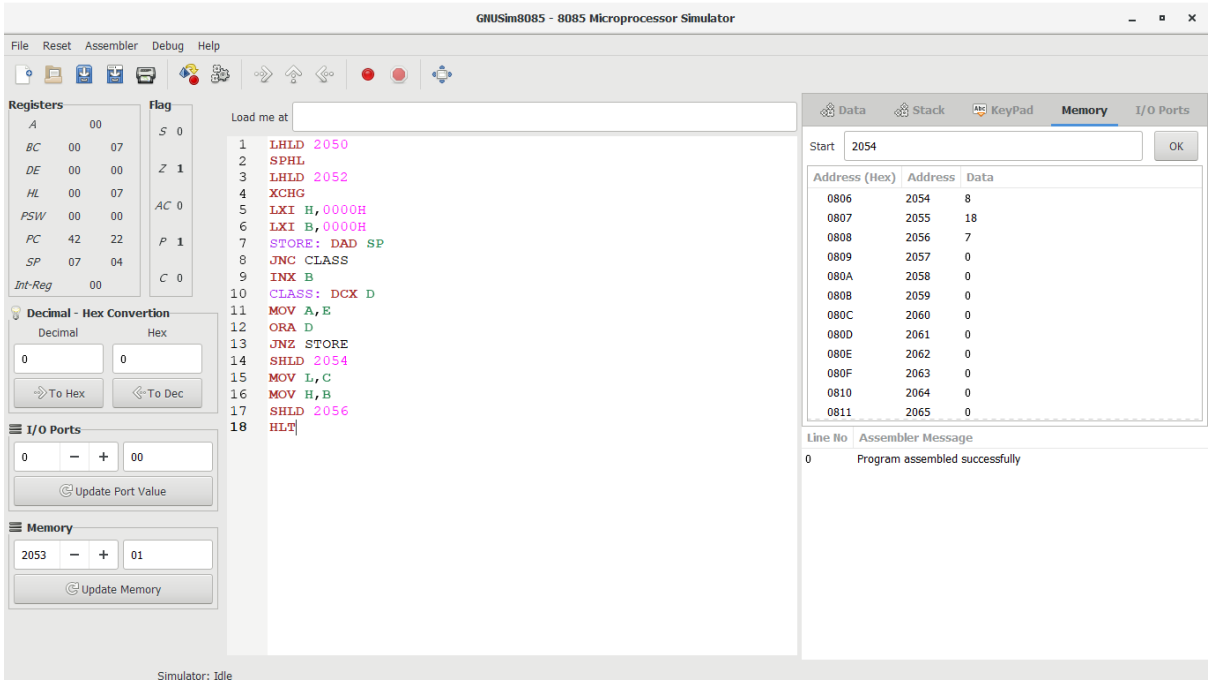
16 BIT DIVISION



16 BIT MULTIPLICATION

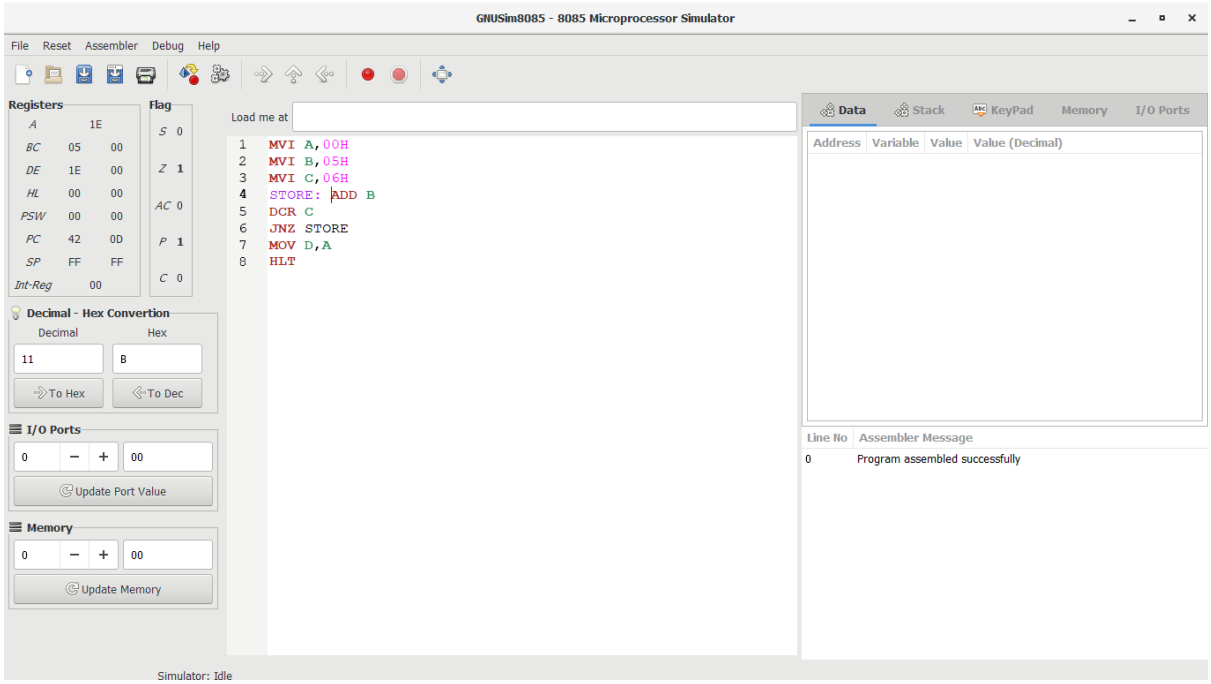


16 BIT SUBTRACTION



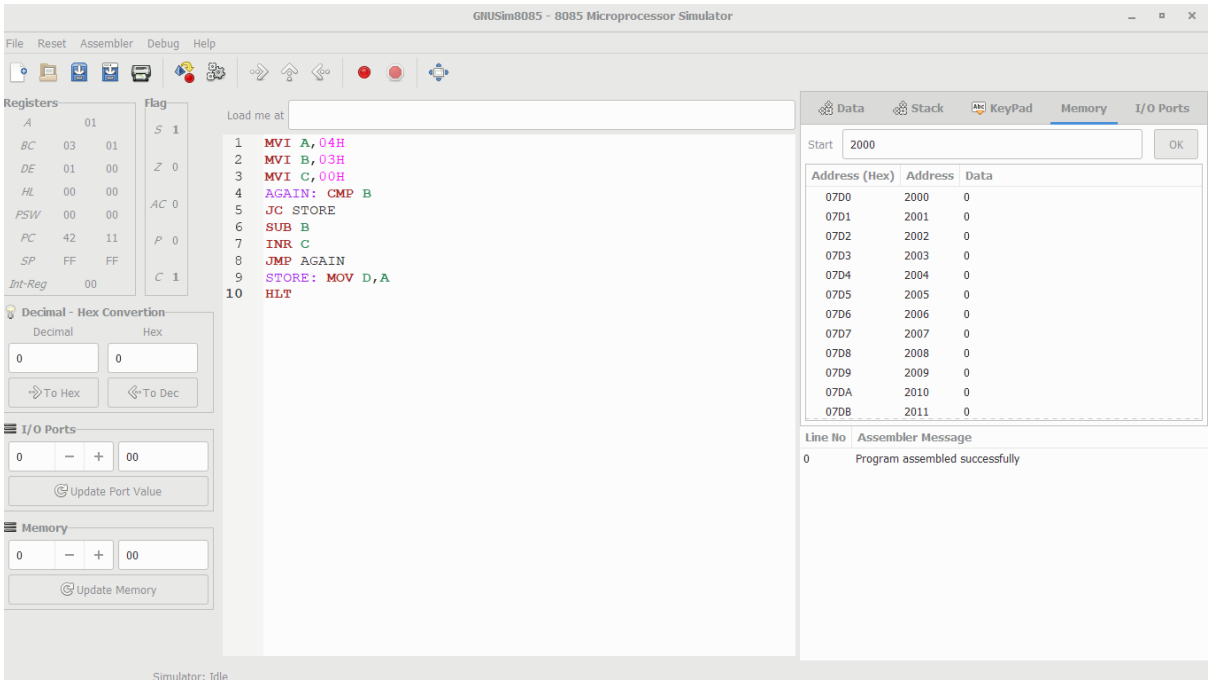

```
1  LHLD 2050
2  SPHL
3  LHLD 2052
4  XCHG
5  LXI H,0000H
6  LXI B,0000H
7  STORE: DAD SP
8  JNC CLASS
9  INX B
10 CLASS: DCX D
11 MOV A,E
12 ORA D
13 JNZ STORE
14 SHLD 2054
15 MOV L,C
16 MOV H,B
17 SHLD 2056
18 HLT
```

8 BIT ADDITION

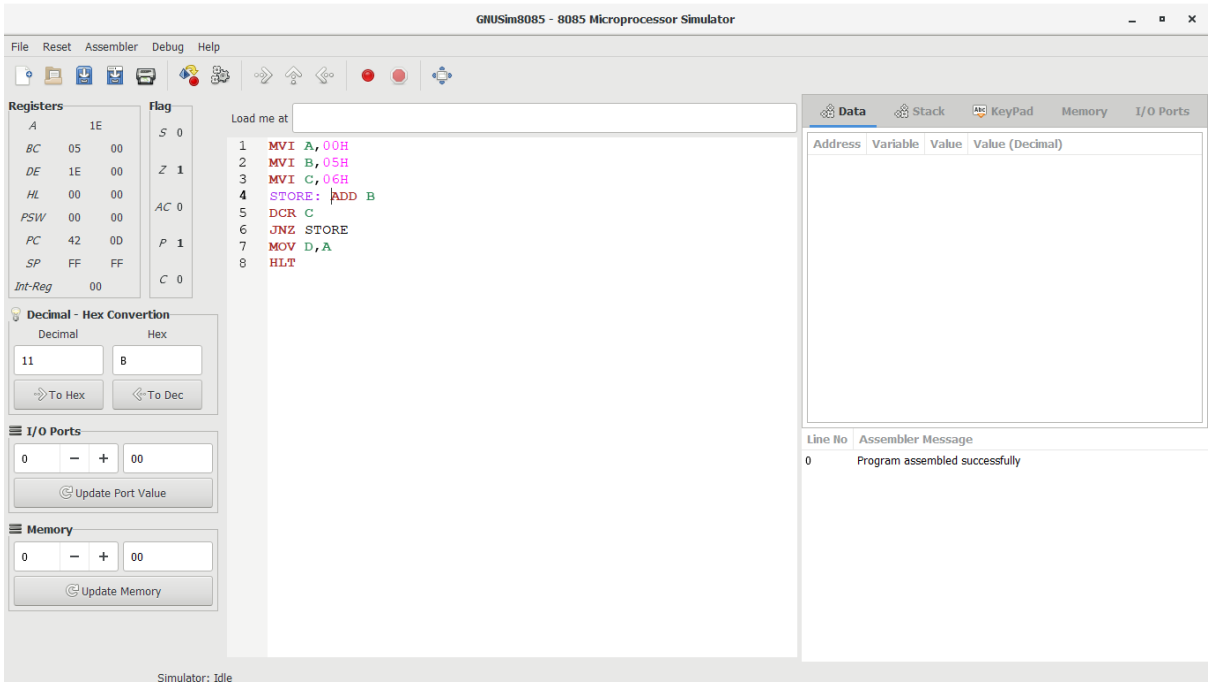



```
1  MVI A,00H
2  MVI B,05H
3  MVI C,06H
4  STORE: ADD B
5  DCR C
6  JNZ STORE
7  MOV D,A
8  HLT
```

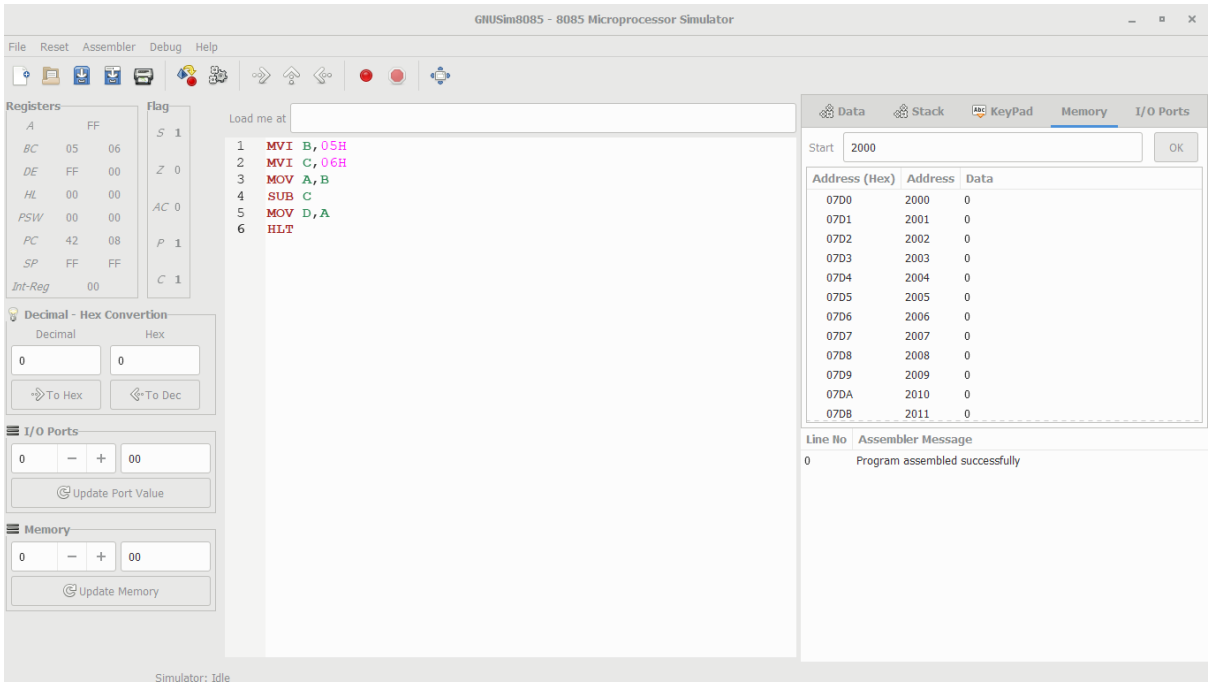
8 BIT DIVISION



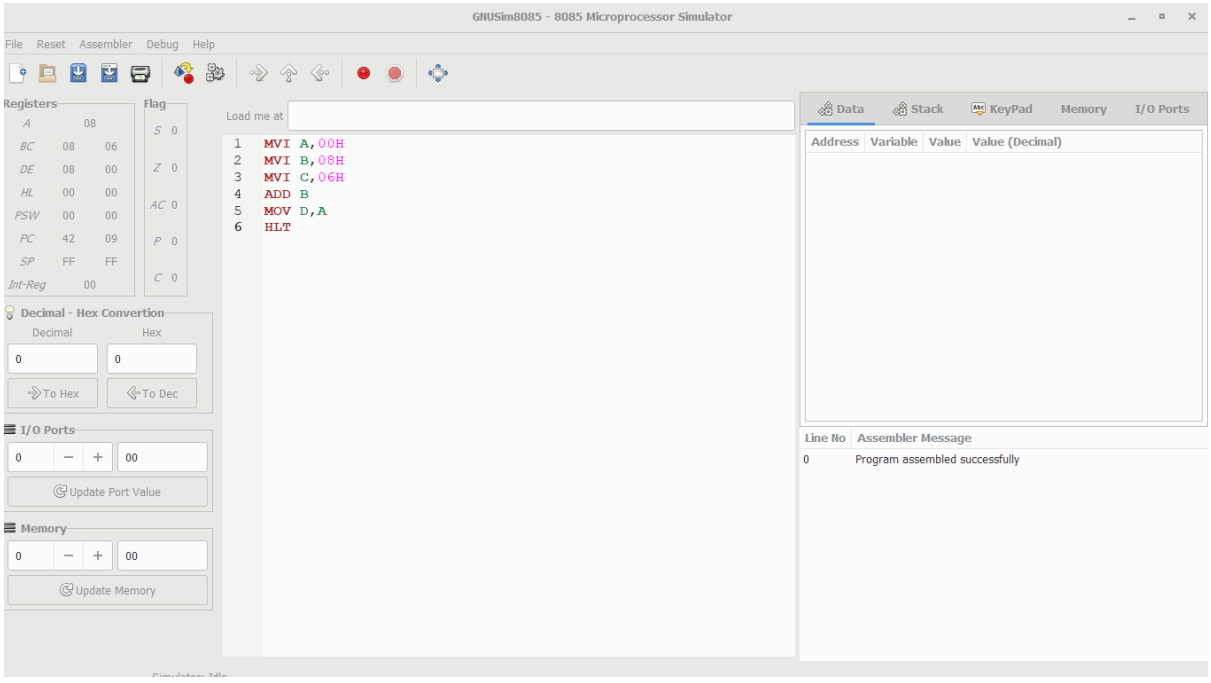
8 BIT MULTIPLICATION



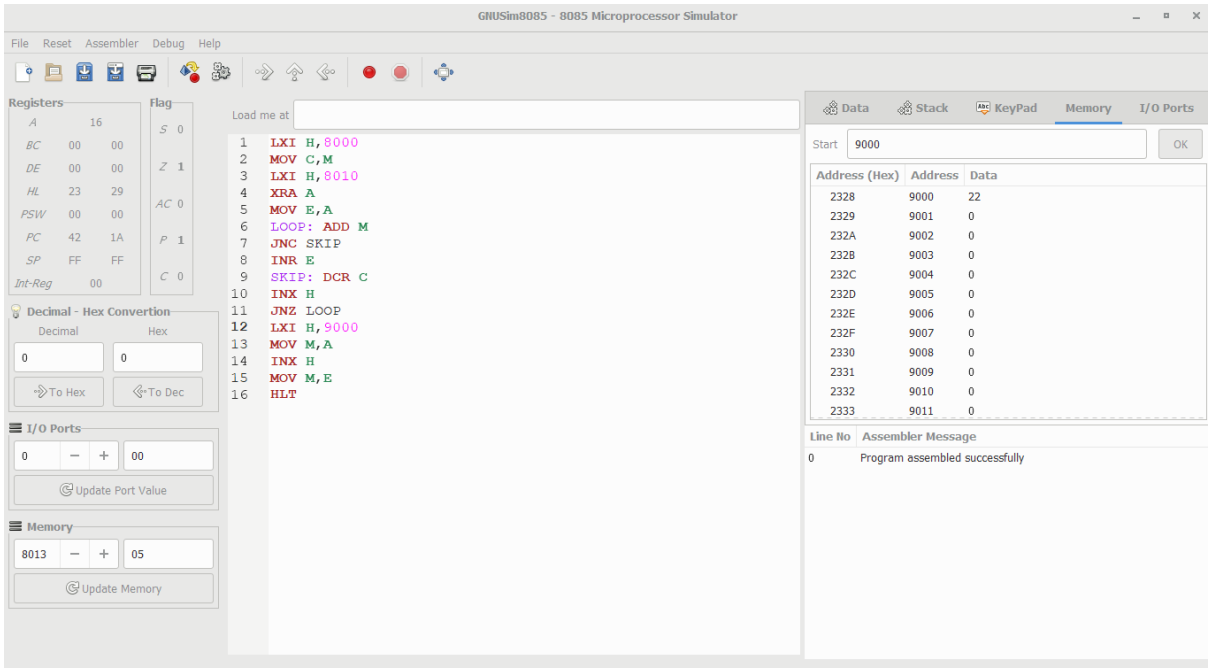
8 BIT SUBTRACTION



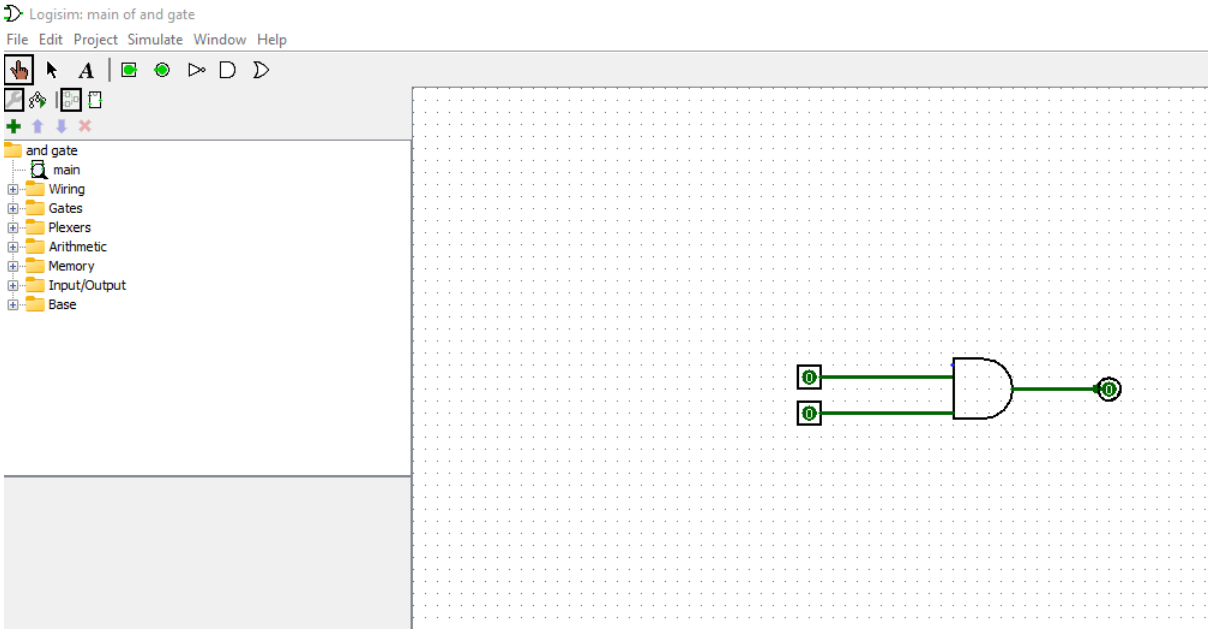
ADDITION FOR 8 BIT



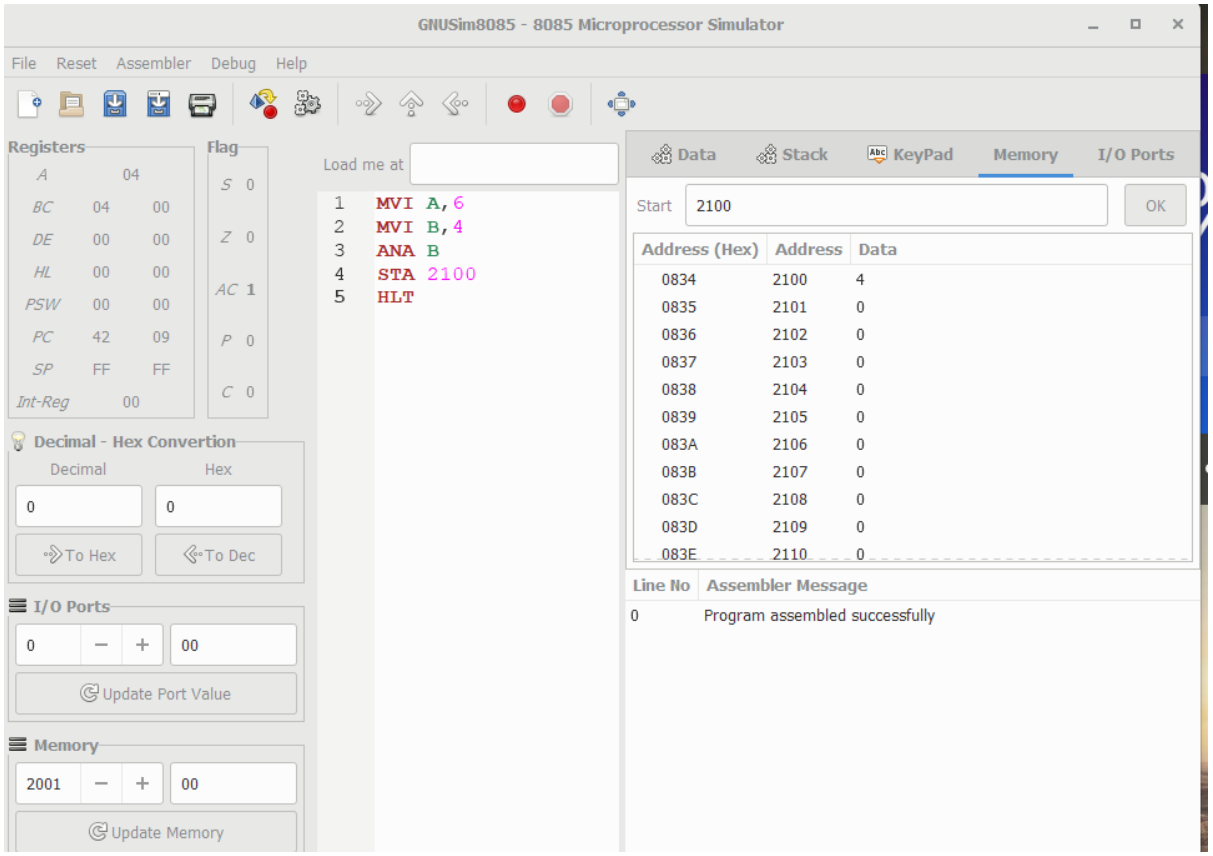
ADDITION OF N NUMBER



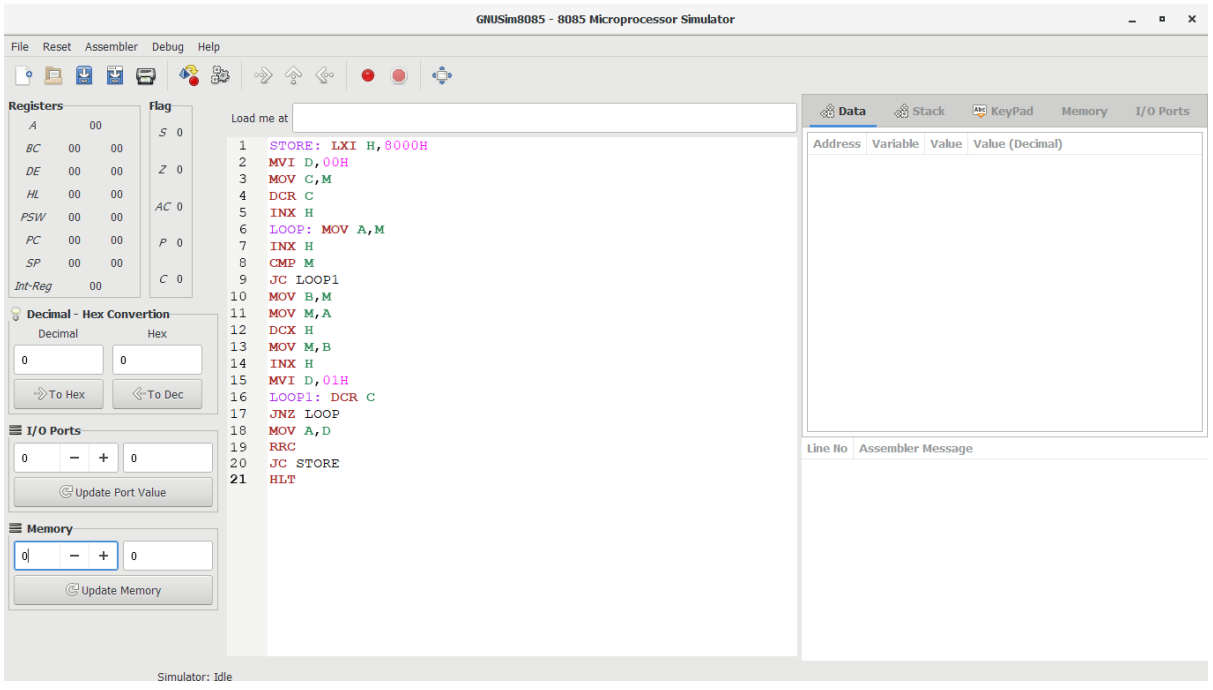
AND GATE OPERATION



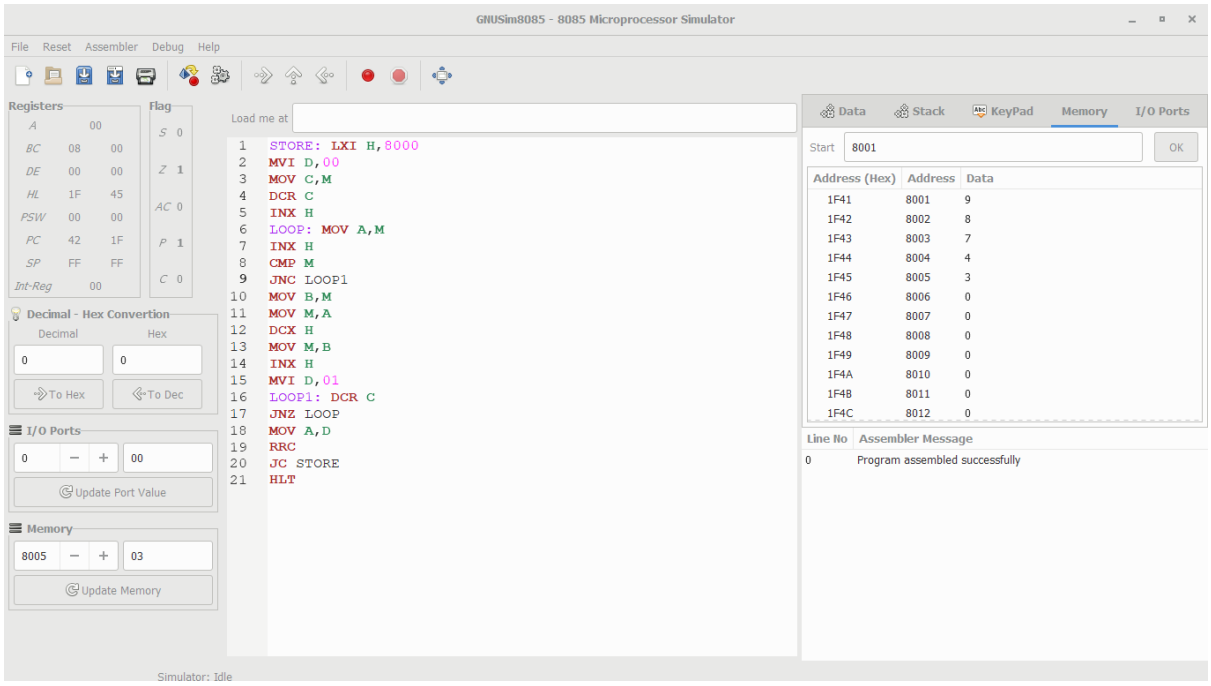
AND OPERATION



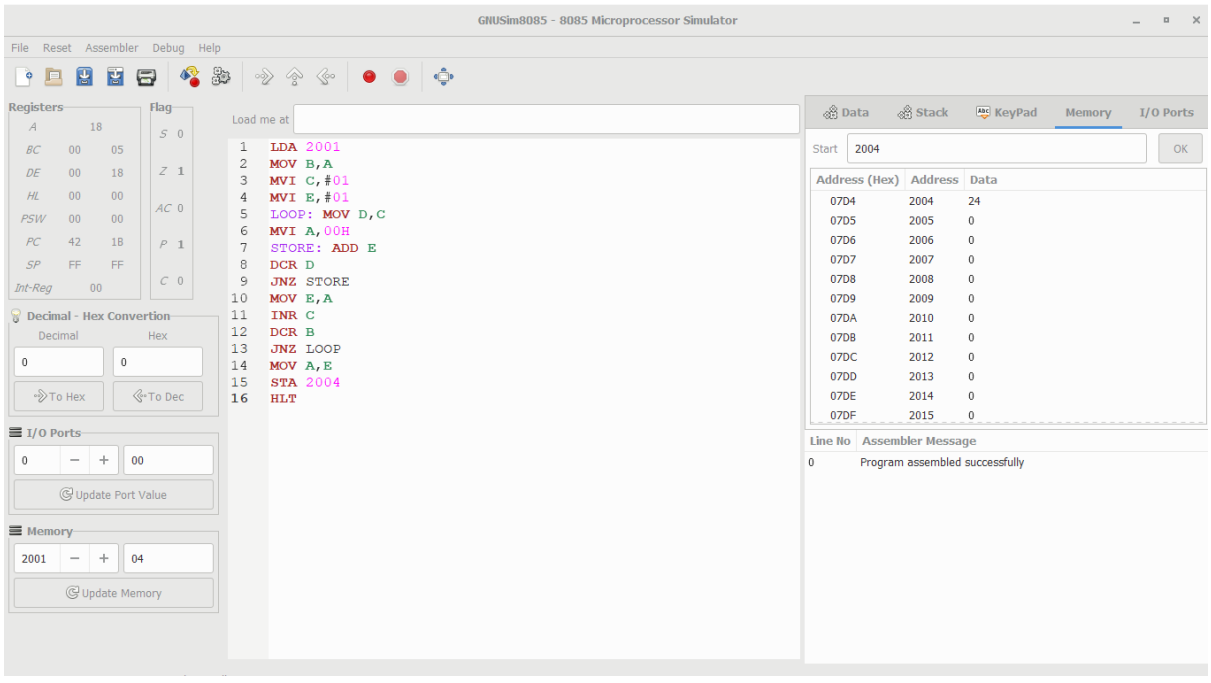
ASCENDING ORDER IN 8085



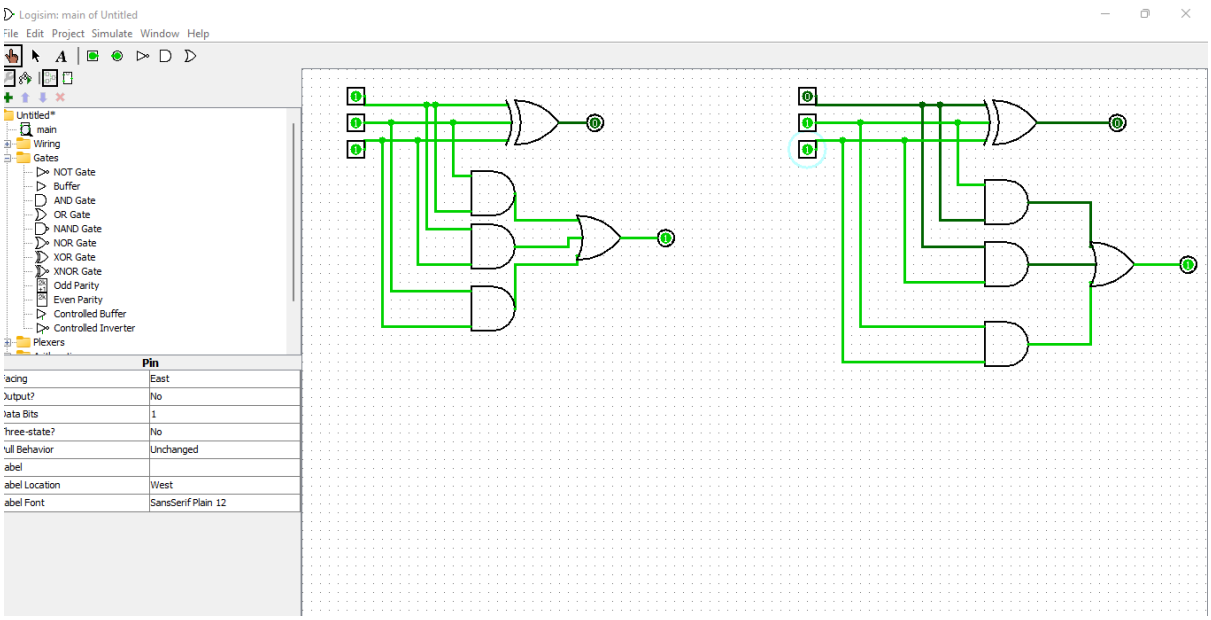
DESCENDING ORDER IN 8085



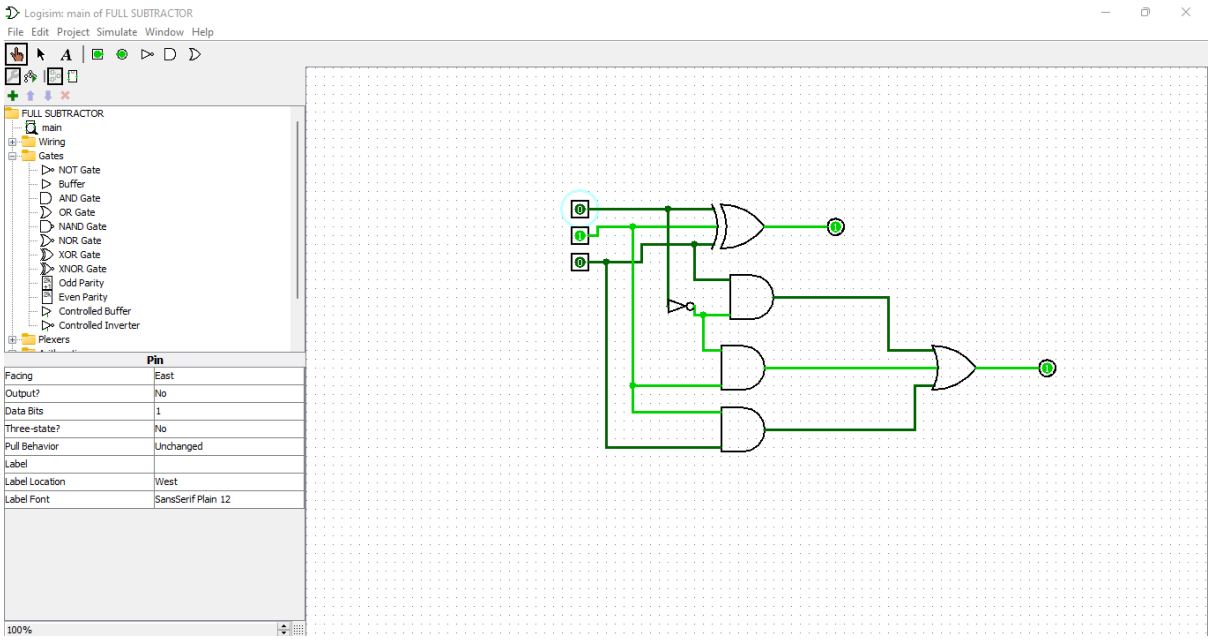
FACTORIAL IN 8085



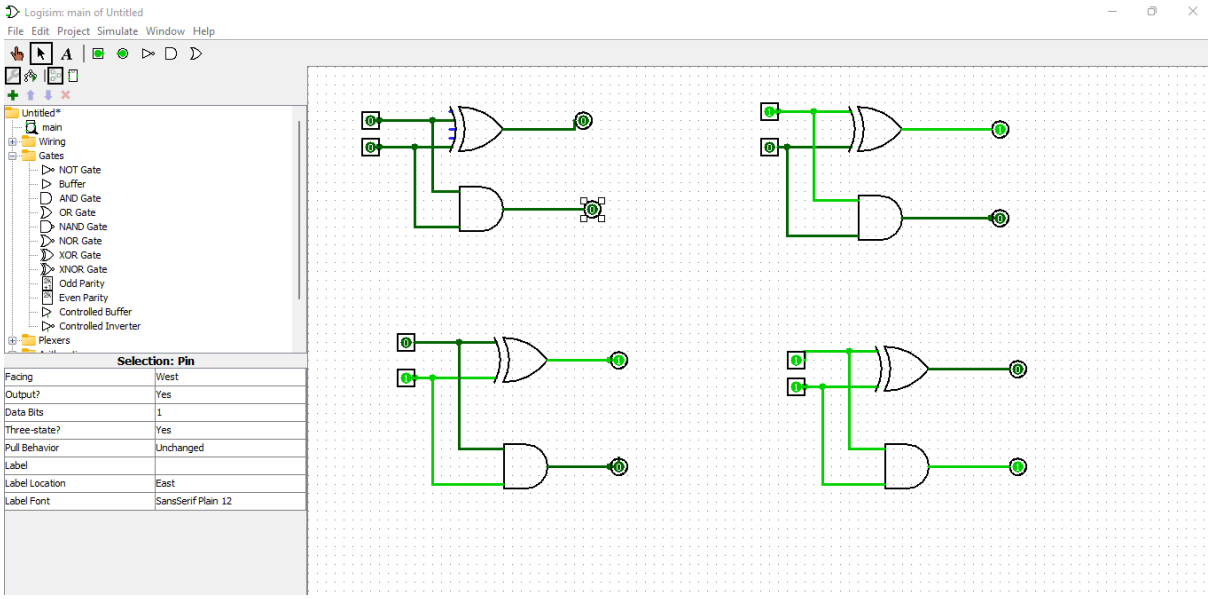
FULL ADDER



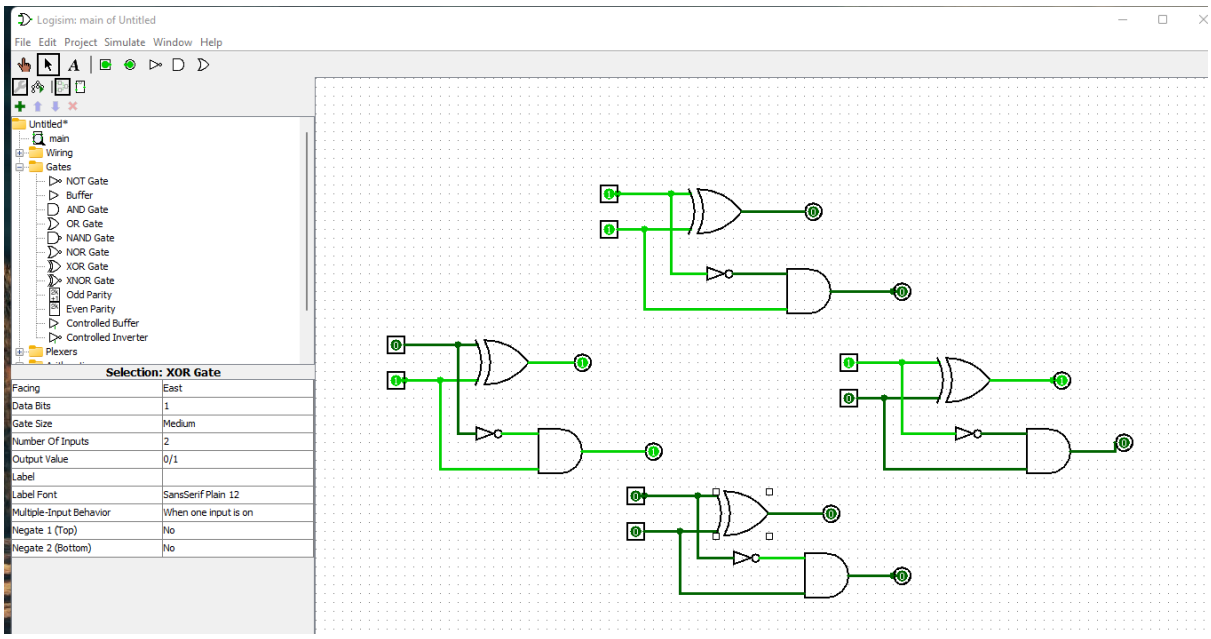
FULL SUBTRACTOR



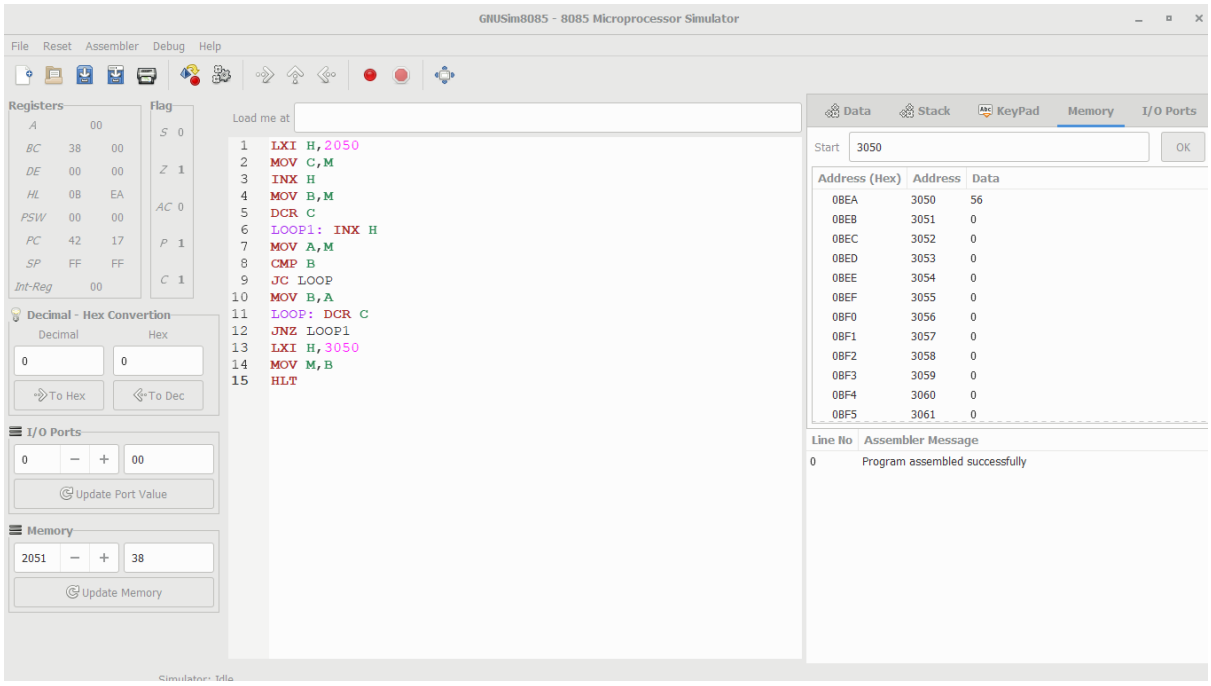
HALF ADDER



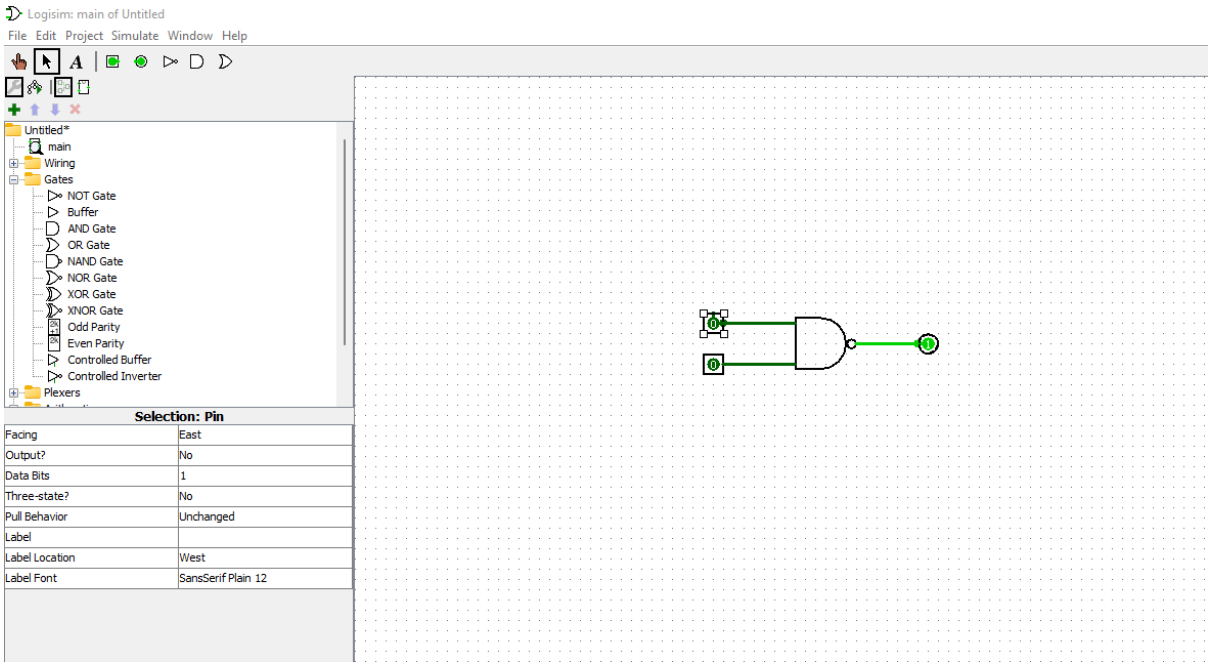
HALF SUBTRACTOR



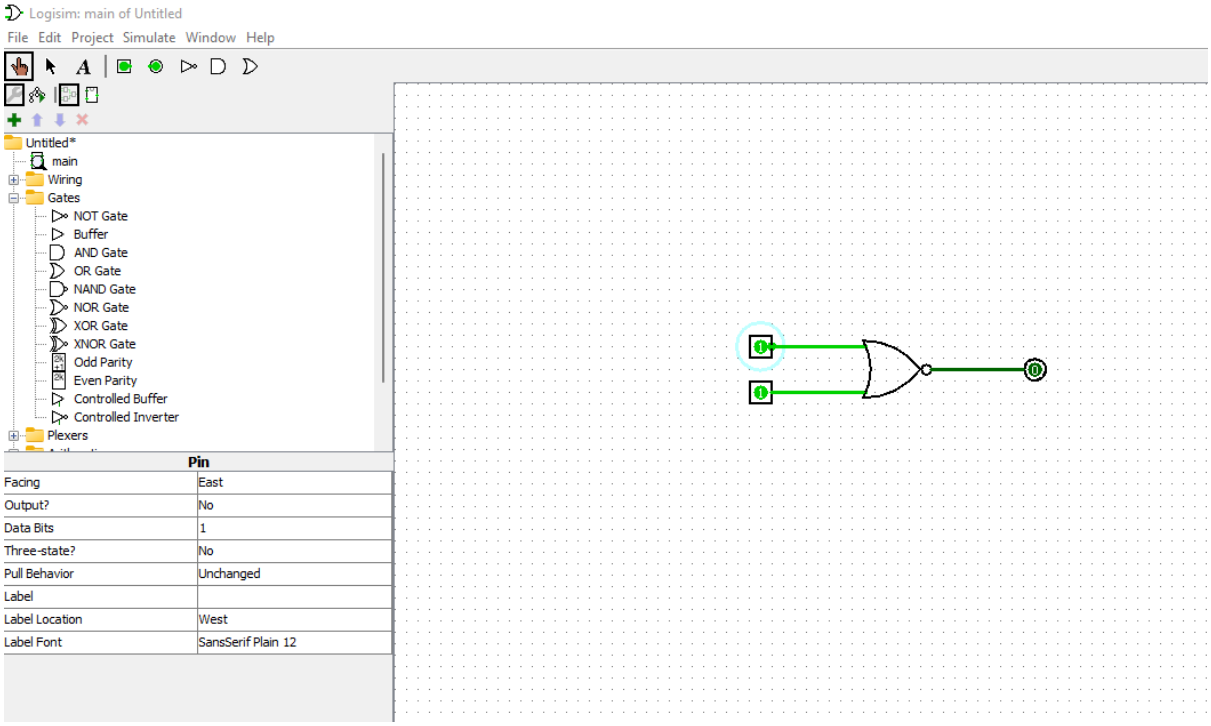
LARGEST NUMBER FOR 2 DIGITS



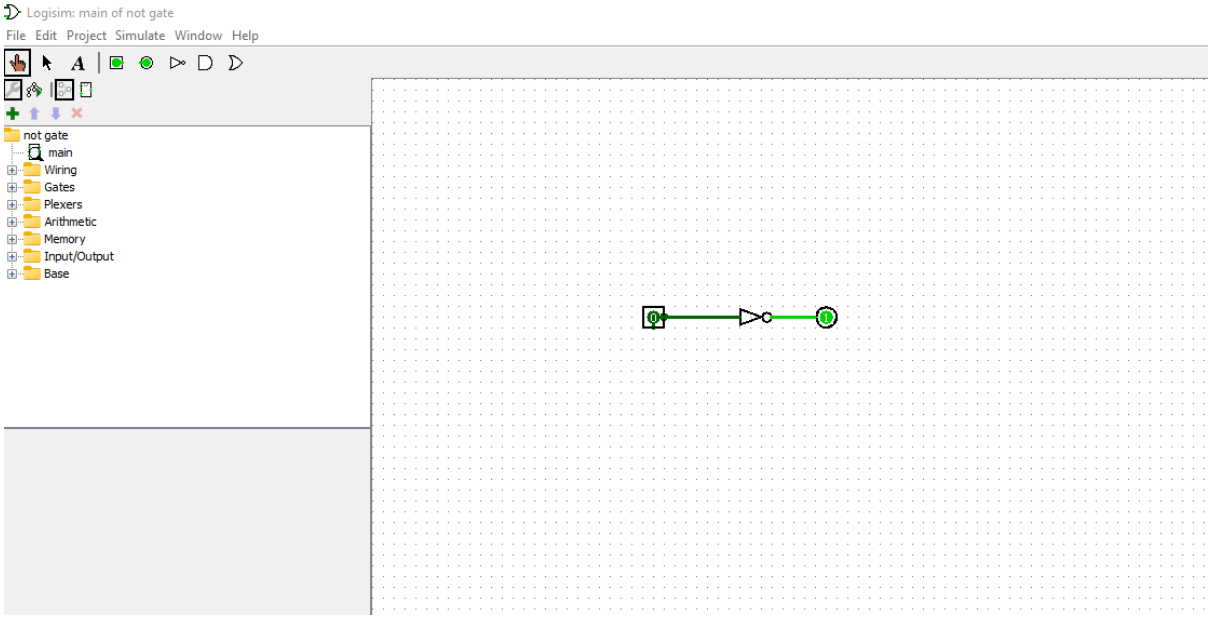
NAND GATE



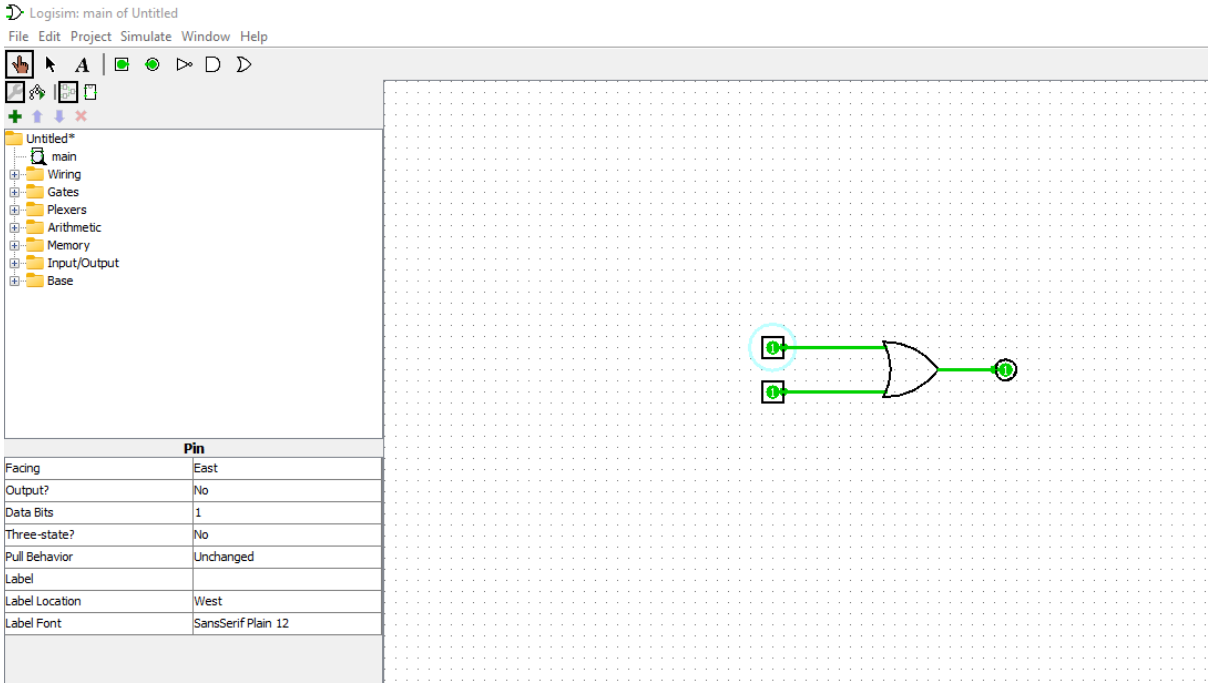
NOR GATE



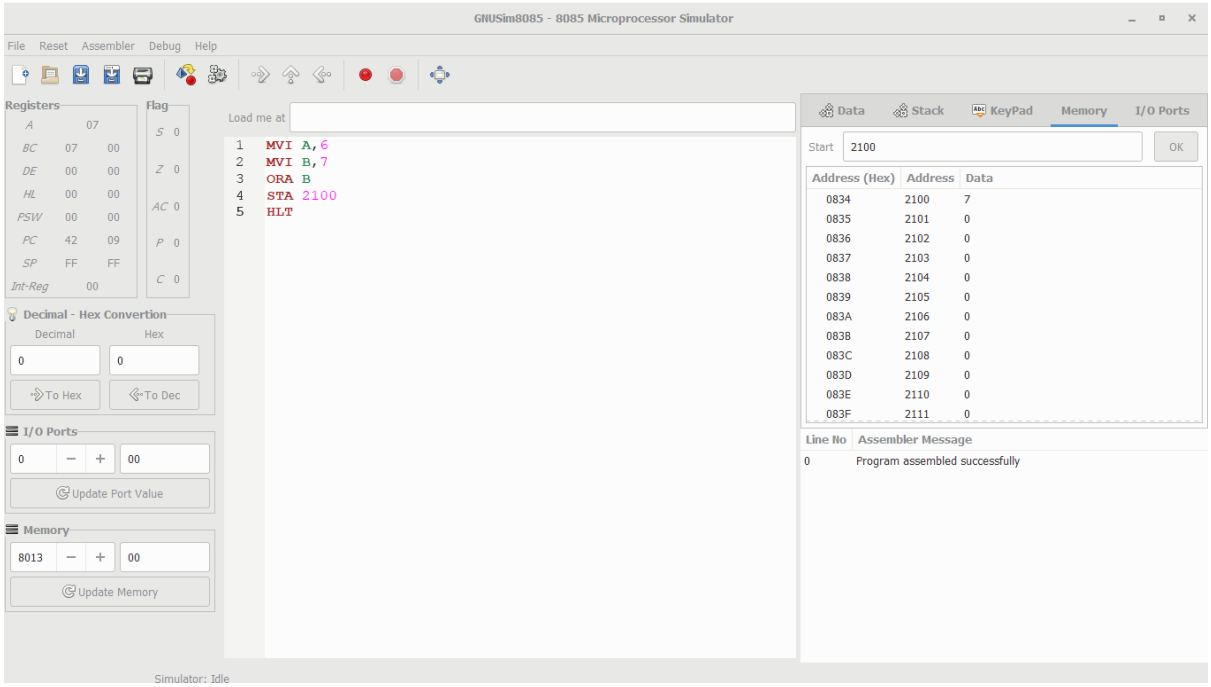
NOT GATE



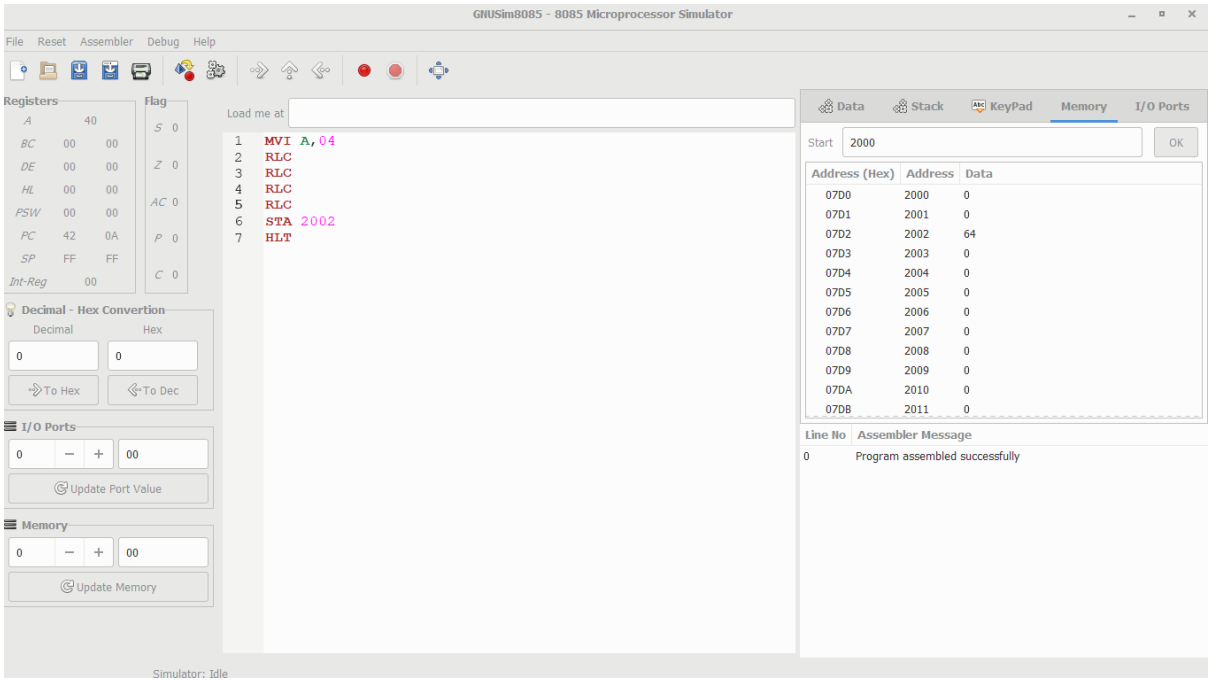
OR GATE OPERATION



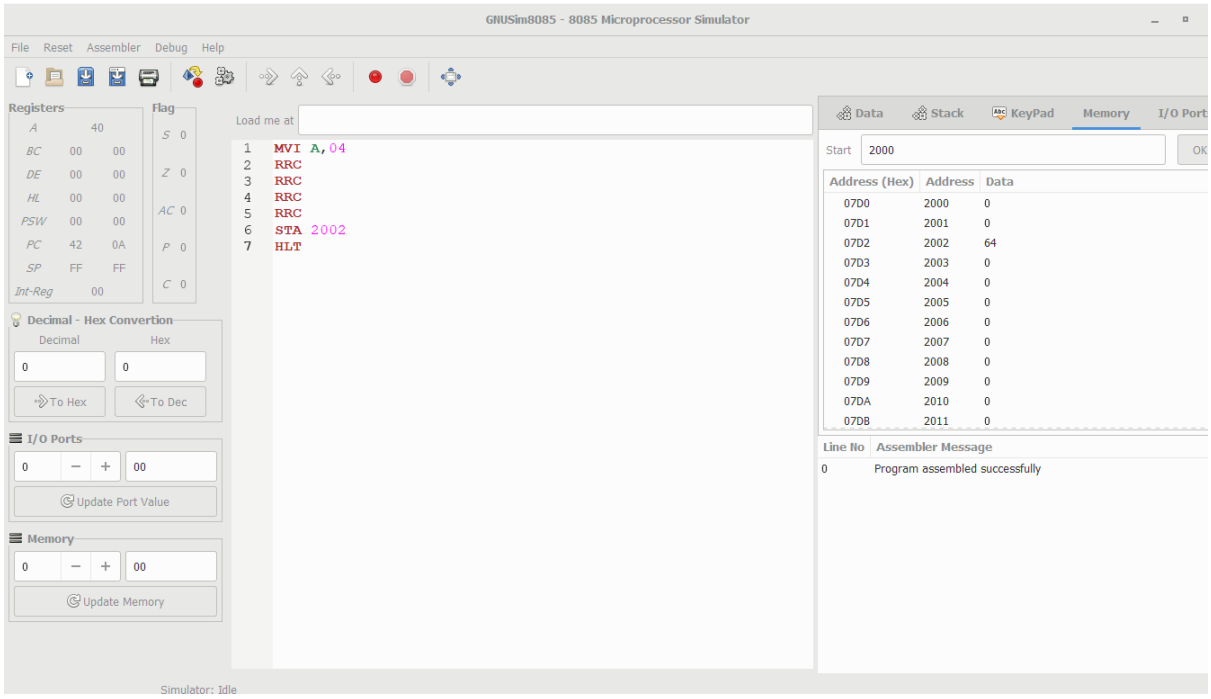
OR OPERATION



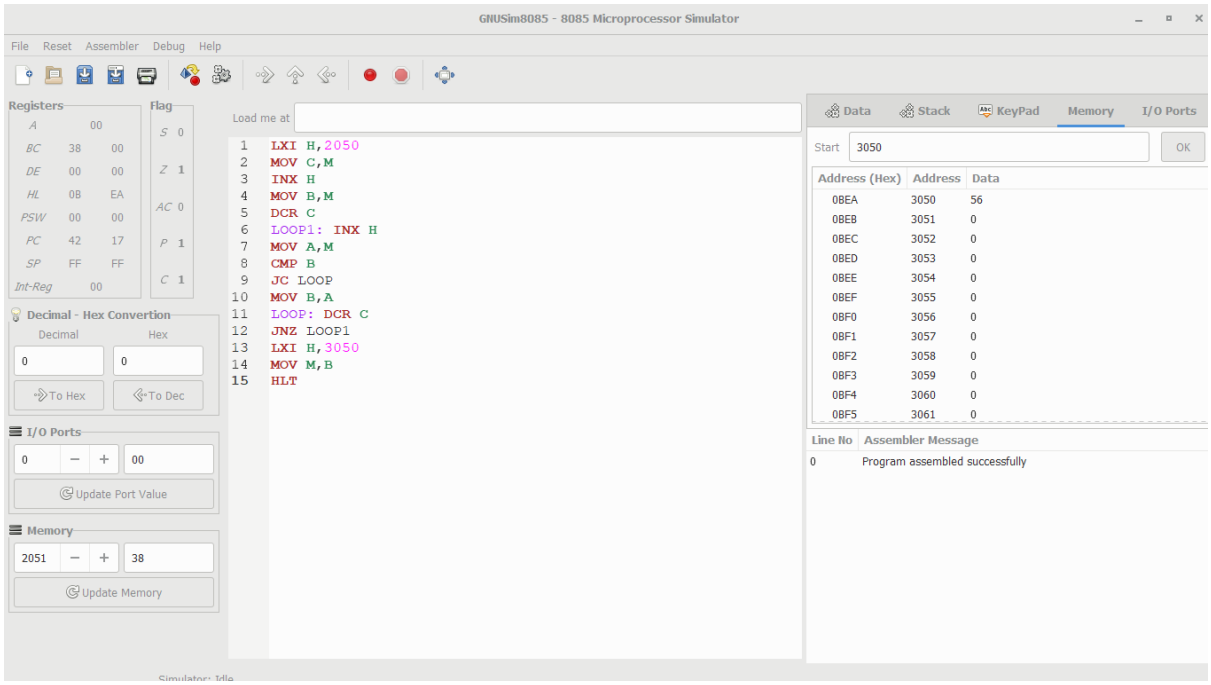
ROTATE LEFT SHIFT OPERATION



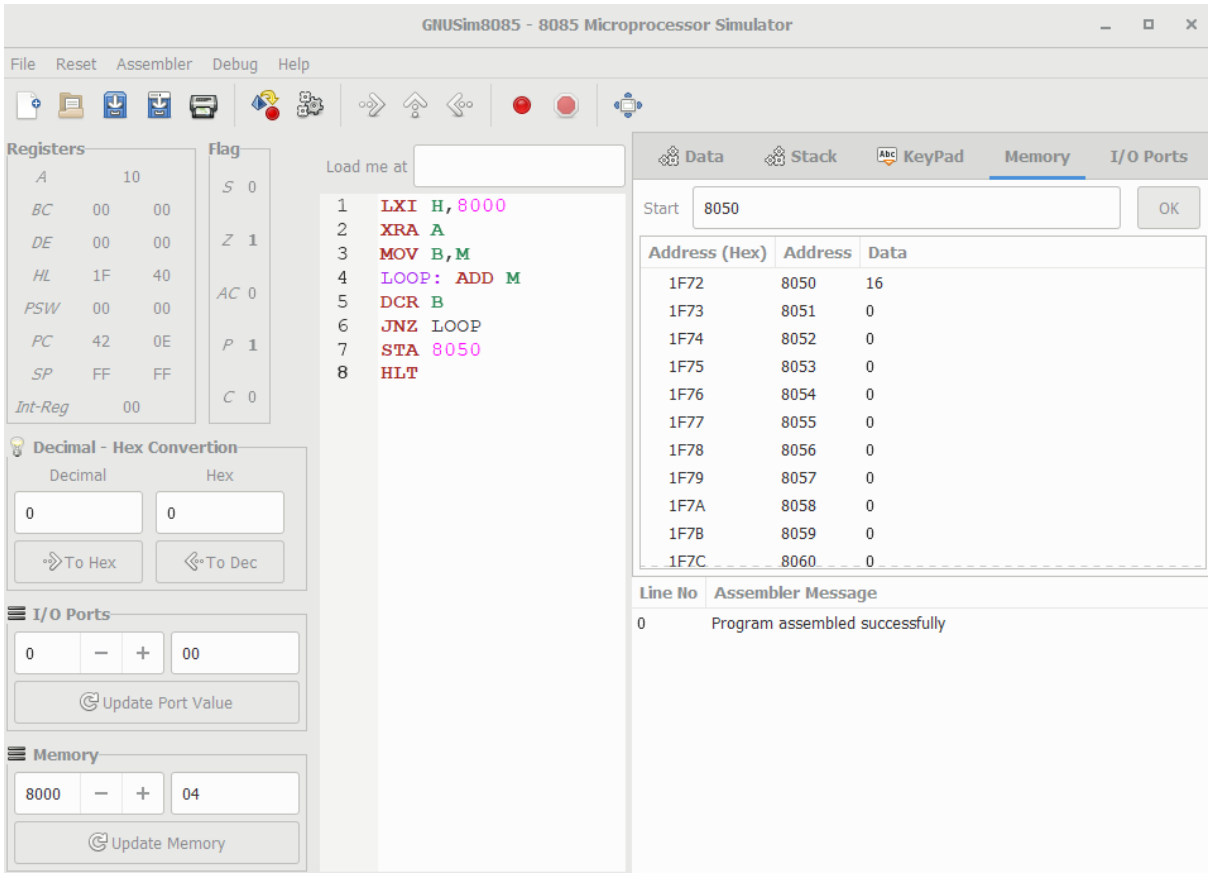
ROTATE RIGHT SHIFT OPERATION



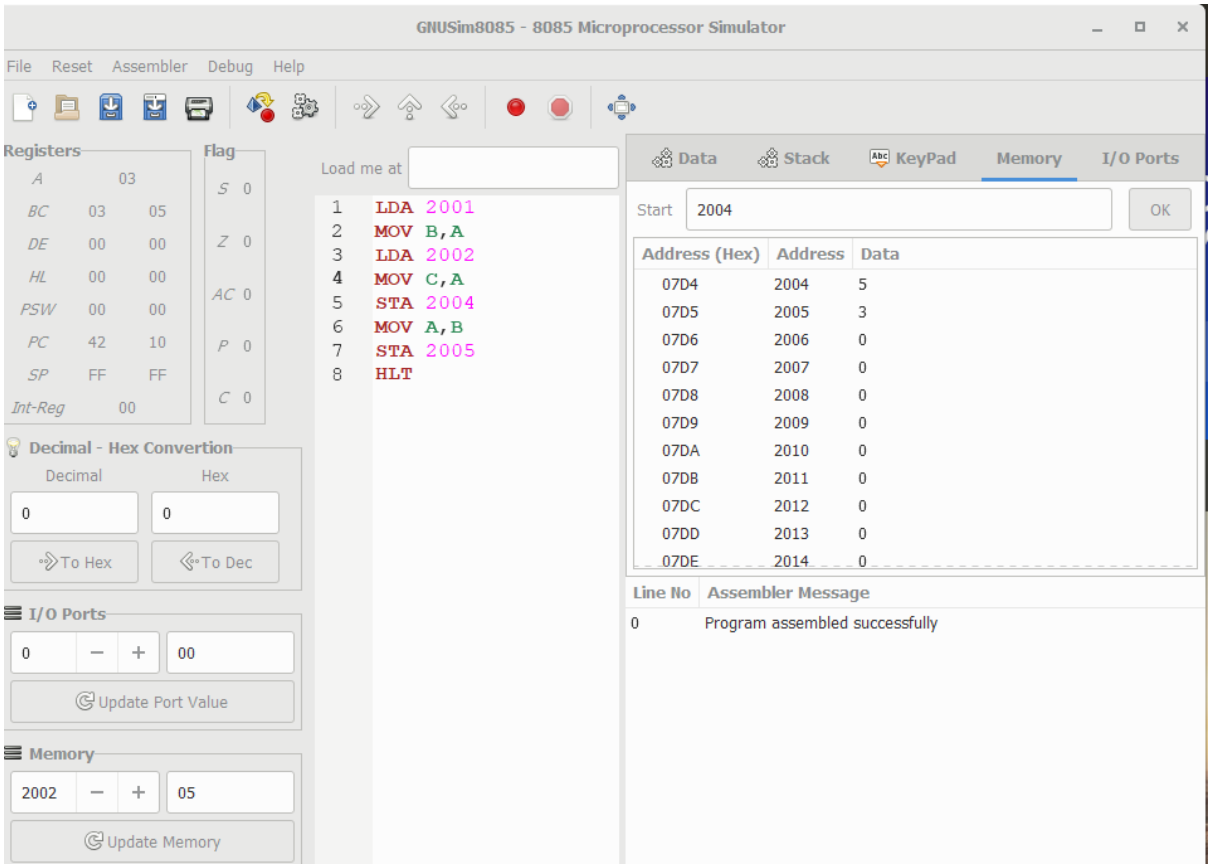
SMALLEST NUMBER IN ARRAY



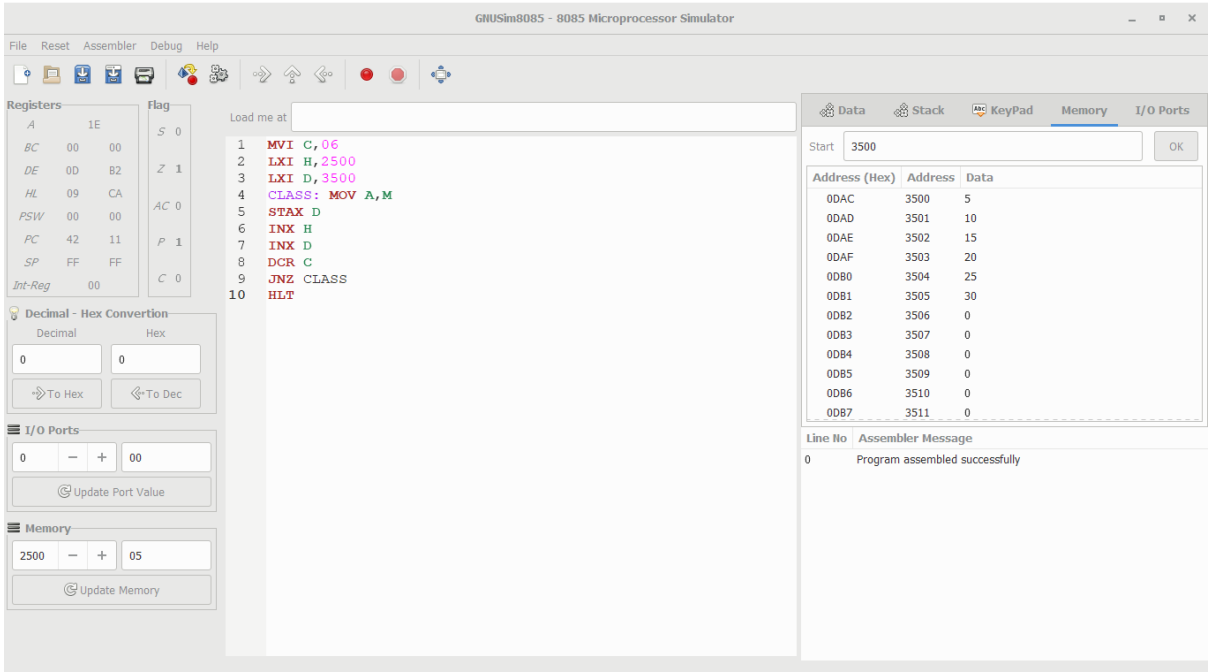
SQUARE OF THE NUMBER



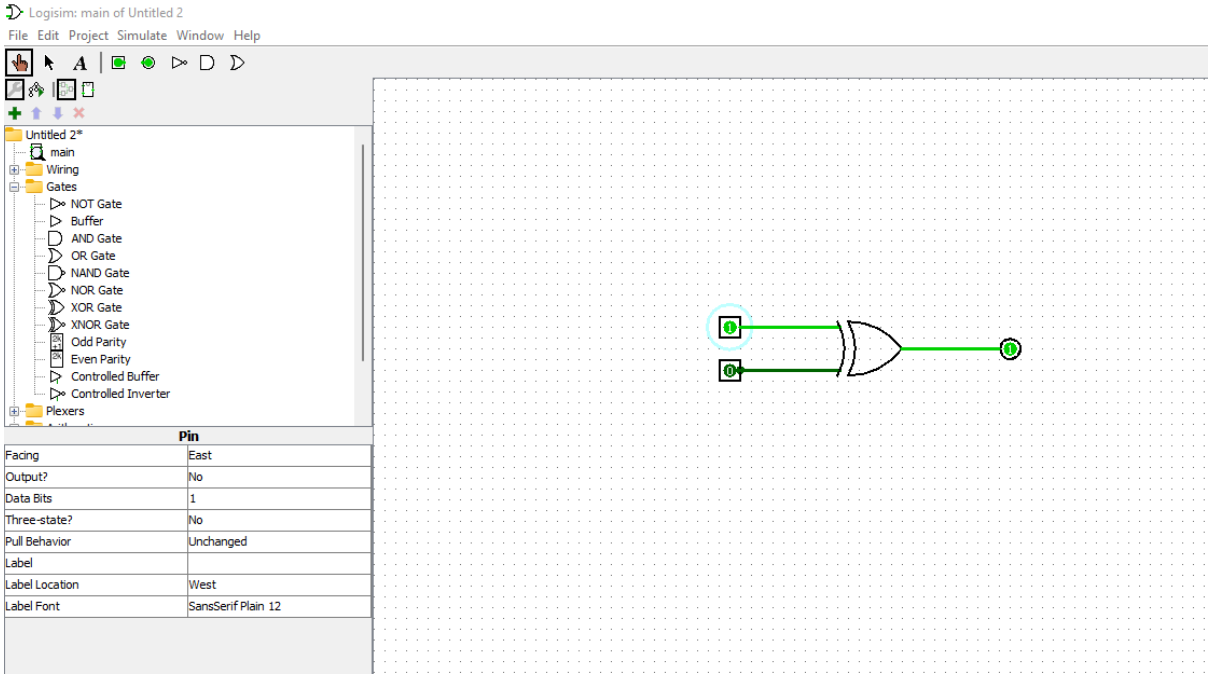
SWAP



TRANSFERRING BLOCKS IN 8085



XOR GATE OPERATION



XOR OPERATION