

1.

(a) `addi x8, x5, -5`

This instruction adds -5 to value in register x_5 and stores the result in x_8 .

(b) `slli x5, x3, 3`

This instruction left shifts the value in register x_3 by 3 positions (which is equivalent to multiplying by 2^3) and stores result in x_5 .

(c) `add x19, x19, x10`

This adds the value in x_{10} to value in x_{19} and stores in x_{19} .

(d) `addi x15, x15, 1`

This adds 1 to value in x_{15} and stores in x_{15} .

(e) `srlr x9, x15, 2`

This right shifts by 2 positions in x_{15} value (which is equivalent to dividing by 2^2) and stores in x_9 .

(f) `addi x12, x10, 24`

This adds 24 to value in x_{10} (which is zero) and stores in x_{12} .

2.

(a) `ld x13, 8x20(x5)``addi x13, x13, 100``sd x13, 12x8(x5)`(b) `ld x21, 8x20(x5)``addi x21, x21, 1``sd x21, 8x20(x5)`

(c) ld $x_6, 40(x_5)$

(d) ld $x_6, 32(x_5)$

ld $x_{13}, 96(x_5)$ and $x_6, x_6, 0x00000000FFFFFFF$

sd $x_6, 96(x_5)$ sd $x_6, 32(x_5)$

sd $x_{13}, 40(x_5)$

11010100 : pointer

00110100 : LBA

addr = $(2^8 + 2^8 + 2^8) = \text{noisier lower 10000}$

11010100 (d)

(e) ld $x_6, 16(x_5)$

sal $x_7, x_6, 32$

sarl $x_6, x_6, 32$

ora x_6, x_6, x_7

sd $x_6, 16(x_5)$

3. (a) Binary representation of 23 is 00010111.

Since, it is a positive number, the 2's complement is the same as above. i.e. 00010111.

(b) Binary representation of -1 is 00000001.

Since, it is a negative number we have to invert the bits and add 1.

By inverting we get 11111110.

By adding 1 we get 11111111.

(c) Binary representation of 255 is 11111111.

Since, it's a positive, the 2's complement representation is the same as above by adding trailing 0's to the left as it is a positive number by which we get 01111111 which 9 bits. (whose significant bit should be 0). Hence, not possible with 8 bits.

(d) Binary representation of -128 is 10000000

Since, it is a negative number we have to invert the bits and add 1.

By inverting we get 01111111

By adding 1 we get 10000000

4(a) 11010100 (8's) 85, 16K 01 01 (3)

Most significant bit is 1, so it's negative. To find 2's complement, invert the bits and add 1.

Inverting: 00101011

Add 1: 00101100

Decimal conversion = $-(2^5 + 2^3 + 2^2) = -44$

(b) 00101011:

Most significant bit is 0, so it's positive.

so, we can directly convert which is $2^5 + 2^3 + 2^1 = +43$

(c) 1111110:

Most significant bit is 1; so it's negative. To find 2's complement, invert the bits and add 1.

Inversion: 00000001

add 1 : 00000010

Decimal conversion = $-(2^7 + 2^6 + 2^5 + 2^4 + 2^3 + 2^2) = -126$

11111111

(c) Binary representation of 126 is 1111110. Since it's a positive number, the 2's complement representation is the same as above. By adding trailing 0's to the left as it is a positive number, we get 01111111 which is 127. Therefore, not possible with 8 bits.

(d) Binary representation of 128 is 10000000. Since it is a negative number, we have to invert the bits and add 1.

11111111
00000001