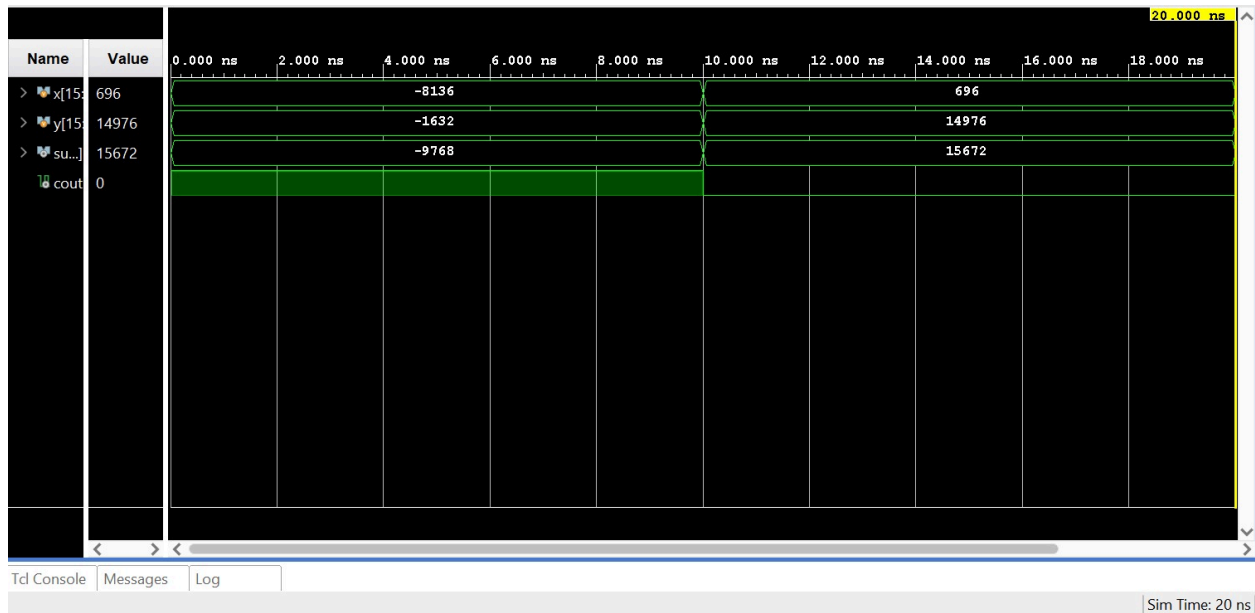


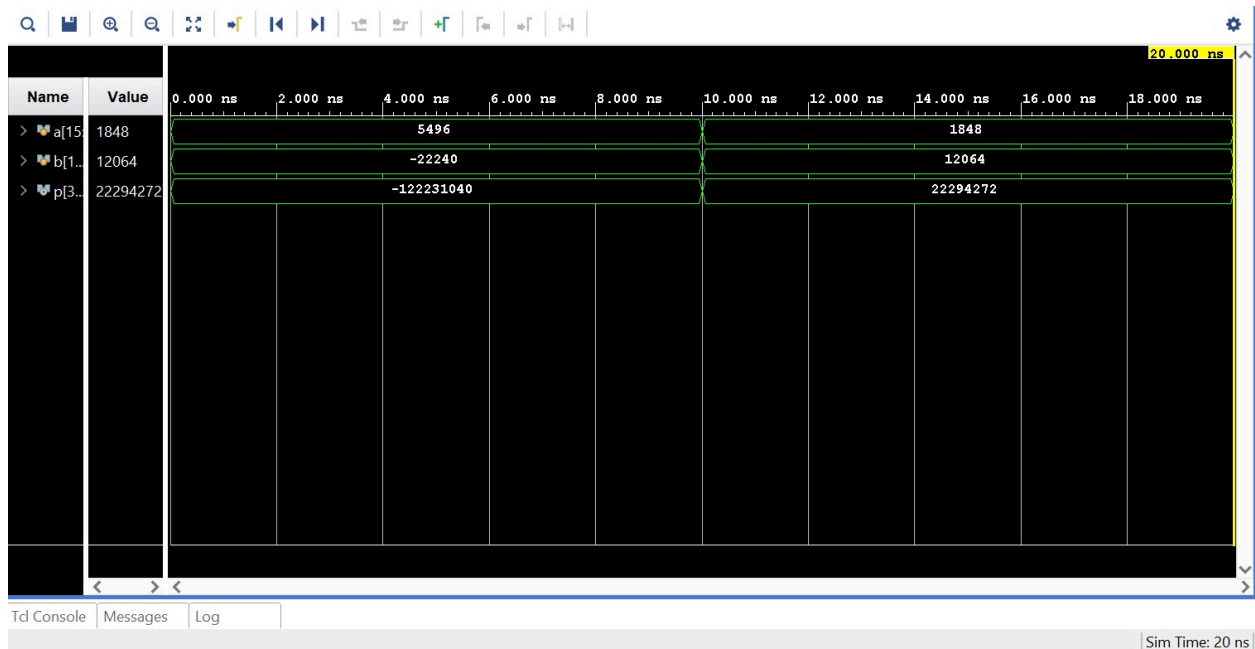
# SIMULATIONS AND IMPLEMENTATIONS

## SIMULATIONS:

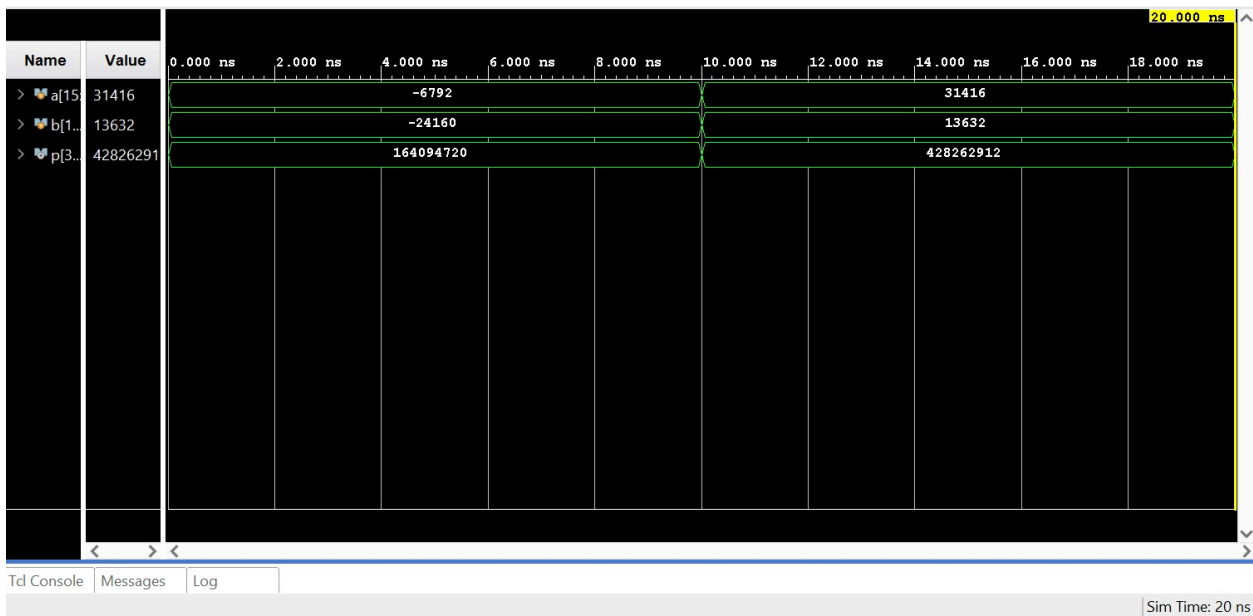
### KOGGE STONE ADDER TESTBENCH FOR 16 BIT:



### WALLACE MULTIPLIER TESTBENCH FOR 16 BIT:



**BOOTH MULTIPLIER TESTBENCH FOR 16 BIT:**

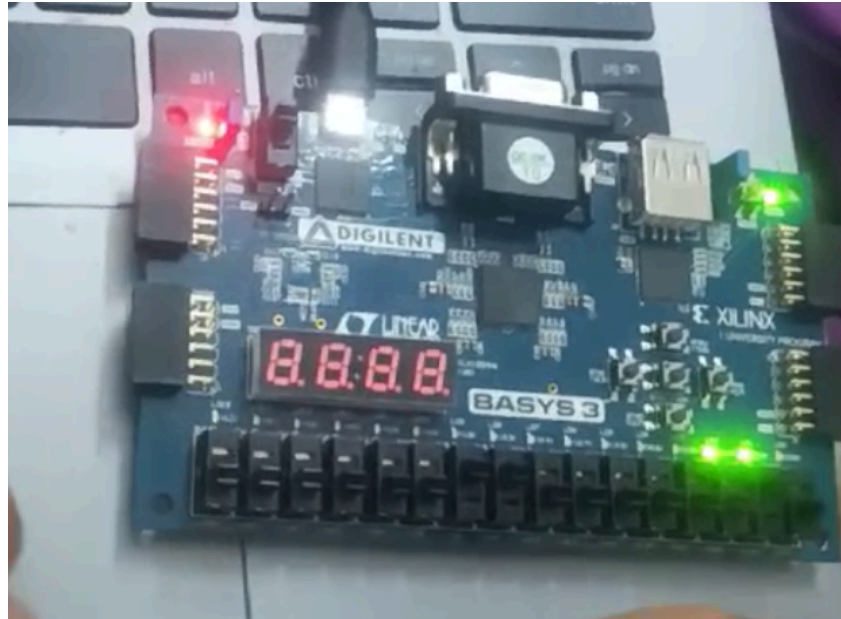


**FPGA IMPLEMENTATIONS:**

**KOGGE STONE ADDER IMPLEMENTATION:**



**WALLACE MULTIPLIER IMPLEMENTATION:**



## BOOTH MULTIPLIER IMPLEMENTATION:

