

### वीर बहादुर सिंह पूर्वाचल विश्वविद्यालय जीनपुर, उ०प्र०, भारत। पिन: 222 003 Veer Bahadur Singh Purvanchal University Jaunpur, U.P., India. Pin: 222 003

MSI:16131100012

### FACULTY OF ENGINEERING & TECHNOLOGY STATEMENT OF MARKS

B.TECH.(INFORMATION TECHNOLOGY)

FIRST SEMESTER EXAMINATION, 2014-15 B.P.

NAME OF CANDIDATE

DHIRAJ SINGH

ROLL No. : 214

FATHER'S/HUSBAND'S NAME

JAI PRAKASH SINGH

ENROLL No. : PU14/210

NAME OF COLLEGE/INSTITUTE

: U.N.S. INSTITUTE OF ENGINEERING & TECHNOLOGY,

V.B.S. PURVANCHAL UNIVERSITY, JAUNPUR

SI.	Papers	Maxim	num Marks	Marks Obtained		Total
No.	EXCLUSION OF A LONG PROPERTY OF THE PARTY.	Exam	Sessional	Exam	Sessional	TOTAL
1	MATHEMATICS-I	100	50	30	31	61
2	ENGG. PHYSICS-I	50	25	16	18	34
3	ENGG. MECHANICS	100	50	19	42	61
4	COMPUTER CONCEPTS & PROGRAMMING IN C	100	50	27	37	64
5	PROFESSIONAL COMMUNICATION	100	50	51	33	84
6	ENVIRONMENT & ECOLOGY	50	25	25	20	45
7	REMEDIAL ENGLISH LANGUAGE*	50	aleum as	32		32
Pra	ctical/Training/Project	gas Barry	35 11 2 3 11 2	Blat et a	A LEGIS	
8	ENGG. MECHANICS LAB	30	20	22	17	39
9	COMPUTER PROGRAMMING LAB	30	20	21	13	34
10	COMPUTER AIDED ENGG. GRAPHICS	30	20	21	18	39
11	PROFESSIONAL COMMUNICATION LAB	IN HOUR	50	URIVE	28	28
12	GENERAL PROFICIENCY	MI MILES	50	CIERLY.	35	35

TOTAL

FIVE HUNDRED TWENTY-FOUR

1000

524

Result

PASS

68643367

Date : 23/01/2017

Checked By

Controller of Exams



### वीर बहादुर सिंह पूर्वाचल विश्वविद्यालय जीनपुर, उ०प्र०, भारत। पिन: 222 003 Veer Bahadur Singh Purvanchal University Jaunpur, U.P., India. Pin : 222 003

MSI:18132200027

### **FACULTY OF ENGINEERING & TECHNOLOGY** STATEMENT OF MARKS

B.TECH.(INFORMATION TECHNOLOGY)

B.TECH, SECOND SEMESTER EXAMINATION, 2015 B.P.

NAME OF CANDIDATE

: DHIRAJ SINGH

FATHER'S/HUSBAND'S NAME

: JAI PRAKASH SINGH

ENROLL No. : PU14/210

U.N.S. INSTITUTE OF ENGINEERING & TECHNOLOGY, NAME OF V.B.S. PURVANCHAL UNIVERSITY, JAUNPUR COLLEGE/INSTITUTE

SL	Papers	Maximu	ım Marks	Marks	Obtained	Total
No.		Exam	Sessional	Exam	Sessional	Total
1	ELECTRONICS ENGINEERING	100	50	32	32	64
2	ENGG, PHYSICS -II	50	25	18	19	37
3	ELECTRICAL ENGG.	100	50	28	40	68
4	MATHEMATICS-II	100	50	41	31	72
5	ENGG CHEMISTRY	100	50	44	38	82
6	MANUFACTURING PROCESS	50	25	06	21	27
Pra	ctical/Training/Project	THE HOUSE TORS	-	m 122.76		Contr.
7	ENGG, CHEMISTRY LAB	30	20	22	18	40
8	ELECTRICAL ENGG. LAB	30	20	18	13	31
9	WORKSHOP PRACTICE	30	20	24	18	42
10	PHYSICS LAB	30	20	21	17	38
11	GENERAL PROFICIENCY		50		38	38

FIVE HUNDRED THIRTY-NINE

Result



### वीर बहादुर सिंह पूर्वाचल विश्वविद्यालय जौनपुर, उ०प्र०, भारत। पिन: 222 003 Veer Bahadur Singh Purvanchal University Jaunpur, U.P., India. Pin : 222 003

MSI 16133100006

#### **FACULTY OF ENGINEERING & TECHNOLOGY** STATEMENT OF MARKS

B. TECH (INFORMATION TECHNOLOGY) THIRD SEMESTER EXAMINATION, 2015-16 B.P.

NAME OF CANDIDATE

**DHIRAJ SINGH** 

ROLL No.

FATHER'S/HUSBAND'S NAME

JAI PRAKASH SINGH

ENROLL No. : PU14/210

COLLEGE/INSTITUTE

U.N.S. INSTITUTE OF ENGINEERING & TECHNOLOGY,

V.B.S. PURVANCHAL UNIVERSITY, JAUNPUR

SI.	Papers	Maximum Marks		Marks Obtained		Total
No.		Exam	Sessional	Exam	Sessional	Total
1	DIGITAL LOGIC DESIGN	100	50	23	41	64
2	MATHEMATICS-III	100	50	27	28	55
3	INDUSTRIAL SOCIOLOGY	50	25	24	18	42
4	DATA STRUCTURES USING C	100	50	49	40	89
5	OBJECT ORIENTED SYSTEMS	100	50	42	38	80
6	IT INFRASTRUCTURE AND ITS MANAGEMENT	100	50	37	35	72
7	HUMAN VALUES AND PROFESSIONAL ETHICS	50	25	24	18	42
Pra	ctical/Training/Project	CALL SERVICE	VALUE OF STREET	(F8)/I	BUTCH	(Maria)
8	LOGIC DESIGN LAB	30	20	26	16	42
9	DATA STRUCTURES LAB	30	20	23	17	40
10	OOPS LAB	15	10	11	07	18
11	GENERAL PROFICIENCY		50	The same	. 42	42

TOTAL

FIVE HUNDRED FORTY-FOUR

1000

Result

PASS

GR-5



### वीर बहादुर सिंह पूर्वाचल विश्वविद्यालय जीनपुर, उ०प्र०, भारत। पिन: 222 003 Veer Bahadur Singh Purvanchal University Jaunpur, U.P., India. Pin : 222 003

MSI 16134200002

#### **FACULTY OF ENGINEERING & TECHNOLOGY** STATEMENT OF MARKS

B. Tech. (Information Technology)

B.TECH. FOURTH SEMESTER EXAMINATION, 2016



DHIRAJ SINGH

FATHER'S/HUSBAND'S NAME

JAI PRAKASH SINGH

ENROLL No.: PU14/210

NAME OF

COLLEGE/INSTITUTE

U.N.S. INSTITUTE OF ENGINEERING & TECHNOLOGY,

V.B.S. PURVANCHAL UNIVERSITY, JAUNPUR

SI. No.	Papers	Maxim	Maximum Marks		Marks Obtained	
NO.		Exam	Sessional	Exam	Sessional	Total
1	DATABASE MANAGEMENT SYSTEM	100	50	28	40	68
2	WEB TECHNOLOGY	100	50	49	41	90
3	COMPUTER ORGANIZATION	100	50	40	40	80
4	POLYMER SCIENCE & TECHNOLOGY	100	50	52	40	92
5	INDUSTRIAL PSYCHOLOGY	50	-25	25	18	43
6	SOFTWARE ENGINEERING	100	50	50	39	89
Pra	ctical/Training/Project	ELERCACION (NUT	VI. 803 (N)	USHKO	I CUE	
7	DBMS LAB	30	20	21	19	40
8	COMPUTER ORGANIZATION LAB	15	10	11	8	19
9	SOFTWARE ENGINEERING LAB	30	20	22	18	40
10	GENERAL PROFICIENCY	The state of the	50		40	40

TOTAL

SIX HUNDRED ONE

Result

PASS



## वीर बहादुर सिंह पूर्वांचल विश्वविद्यालय जीनपुर, उ०प्र०, भारत। पिन: 222 003 Veer Bahadur Singh Purvanchal University Jaunpur, U.P., India. Pin : 222 003

MSI:16135100002

### **FACULTY OF ENGINEERING & TECHNOLOGY** STATEMENT OF MARKS

B. Tech (Information Technology)

FIFTH SEMESTER EXAMINATION, 2016-17

NAME OF CANDIDATE

DHIRAJ SINGH

FATHER'S/HUSBAND'S NAME

JAI PRAKASH SINGH

ENROLL No. : PU14/210

NAME OF

COLLEGE/INSTITUTE

U.N.S. INSTITUTE OF ENGINEERING & TECHNOLOGY.

V.B.S. PURVANCHAL UNIVERSITY, JAUNPUR

SL	Papers	Maxim	Maximum Marks		Obtained	No state
No.		Exam	Sessional	Exam	Sessional	Total
1	ENGINEERING & MANAGERIAL ECONOMICS	100	50	47	41	88
2	OPERATING SYSTEM	100	50	37	40	77
3	DESIGN AND ANALYSIS OF ALGORITHMS	100	50	40	42	82
4	E-COMMERCE	100	50	44	42	86
5	COMPUTER GRAPHICS	50	25	20	17	37
6	INFORMATION SECURITY AND CYBER LAWS	50	25	28	23	51
Pra	ctical/Training/Project	ISHIE RA	ivo contra	REAL PROPERTY.	HEIDEL	
7	OPERATING SYSTEM LAB	25	25	19	20	39
8	ALGORITHMS LAB	25	25	19	21	40
9	MINI PROJECT USING WEB TECHNOLOGY 4	25	25	14	20	34
10	COMPUTER GRAPHICS LAB	25	25	21	20	41
11	GENERAL PROFICIENCY		50		40	40

TOTAL

SIX HUNDRED FIFTEEN



### वीर बहादुर सिंह पूर्वाचल विश्वविद्यालय जौनपुर, उ०प्र०, भारत। पिन: 222 003 Veer Bahadur Singh Purvanchal University Jaunpur, U.P., India. Pin: 222 003

MSI:17138200002

## FACULTY OF ENGINEERING & TECHNOLOGY STATEMENT OF MARKS

B. Tech. Information Technology

B TECH. SIXTH SEMESTER EXAMINATION, 2017

NAME OF CANDIDATE

DHIRAJ SINGH

LL No. : 214302

FATHER'S/HUSBAND'S NAME

JAI PRAKASH SINGH

ENROLL No. : PU14/210

NAME OF COLLEGE/INSTITUTE

U.N.S. INSTITUTE OF ENGINEERING & TECHNOLOGY, V.B.S. PURVANCHAL UNIVERSITY, JAUNPUR, U.P.

SI.	Papers	Maxim	Maximum Marks		Marks Obtained	
NO.	BERNAND BUT DRIVENING A TALBUTT	Exam	Sessional	Exam	Sessional	Total
1	ERP SYSTEM	50	25	37	20	57
2	INDUSTRIAL MANAGEMENT	100	50	62	32	94
3	COMPUTER NETWORK	100	50	42	41	83
4	SOFTWARE PROJECT MANAGEMENT	100	50	52	31	83
5	SOFTWARE TESTING	100	50	53	33	86
6	GRAPH THEORY	50	25	24	19	43
Pra	ctical/Training/Project					
7	SOFTWARE PROJECT MANAGEMENT LAB	25	25	18	18	36
8	COMPUTER NETWORK LAB	25	25	20	20	40
9	MINI PROJECT USING WEB TECHNOLOGY-2	25	25	19	20	39
10	SEMINAR	Y SHOW	50	MAN TON	41	41
11	GENERAL PROFICIENCY	CO STREET	50		43	43

TOTAL

SIX HUNDRED FORTY-FIVE

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645

Result

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Date 02/06/2017

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### वीर बहादुर सिंह पूर्वाचल विश्वविद्यालय जौनपुर, उ०प्र०, भारत। पिन: 222 003 Veer Bahadur Singh Purvanchal University Jaunpur, U.P., India. Pin: 222 003

MSI 18137100002

#### FACULTY OF ENGINEERING & TECHNOLOGY STATEMENT OF MARKS

B. Tech. (Information Technology)

B.TECH, SEVENTH SEMESTER EXAMINATION, 2017-18

NAME OF CANDIDATE

DHIRAJ SINGH

ROLL No. :

214302

FATHER'S/HUSBAND'S NAME

JAI PRAKASH SINGH

ENROLL No. :

PU14/210

NAME OF COLLEGE/INSTITUTE

U.N.S. INSTITUTE OF ENGINEERING & TECHNOLOGY, V.B.S. PURVANCHAL UNIVERSITY, JAUNPUR, U.P.

SI.	Papers	Maxim	Maximum Marks		Marks Obtained	
1	CONTRACTOR OF THE ORIGINAL OF	Exam	Sessional	Exam	Sessional	Total
1	QUALITY MANAGEMENT	100	50	54	43	97
2	CRYPTOGRAPHY & NETWORK SECURITY	100	50	49	39	88
3	ARTIFICIAL INTELLIGENCE	100	50	47	38	85
4	THEORY OF AUTOMATA AND FORMAL LANGUAGES	100	50	62	44	106
5	DATA MINING & DATA WAREHOUSING	100	50	68	39	107
Pra	ctical/Training/Project					
6	CRYPTOGRAPHY & NETWORK SECURITY LAB	25	25	20	22	42
7	ARTIFICIAL INTELLIGENCE LAB	25	25	17	21	38
8	PROJECT	N CONT	50	HANN'S	40	40
9	INDUSTRIAL TRAINING VIVA-VOCE	N THOUGH	50	-We-w	40	40
10	GENERAL PROFICIENCY	BROK	50.		43	43

TOTAL

SIX HUNDRED EIGHTY-SIX

1000

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Result

PASS

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Date: 13/01/2018



### वीर बहादुर सिंह पूर्वाचल विश्वविद्यालय जौनपुर, उ०प्र०, भारत। पिन: 222 003 Veer Bahadur Singh Purvanchal University Jaunpur, U.P., India. Pin: 222 003

MSI:18138200251

# FACULTY OF ENGINEERING & TECHNOLOGY STATEMENT OF MARKS

B. Tech. (Information Technology)

B.TECH. EIGHTH SEMESTER EXAMINATION, 2018

NAME OF CANDIDATE

DHIRAJ SINGH

NI No - 214302

FATHER'S/HUSBAND'S NAME

JAI PRAKASH SINGH

LL ITO.

MOTHER'S NAME

UMA DEVI

ENROLL No. : PU14/210

NAME OF COLLEGE/INSTITUTE

U.N.S. INSTITUTE OF ENGINEERING & TECHNOLOGY, V.B.S. PURVANCHAL UNIVERSITY, JAUNPUR, U.P.

SI.	Papers	Maximum Marks		Marks Obtained		
NO.	Uniquesta Surviva Considera	Exam	Sessional	Exam	Sessional	Total
1	NON CONVENTIONAL ENERGY RESOURCES	100	50	49	42	91
2	MOBILE COMPUTING	100	50	58	42	100
3	DISTRIBUTED SYSTEMS	100	50	36	41	77
4	MULTIMEDIA SYSTEMS	100	50	51	41	92
Pra	ctical/Training/Project	S. L. S. LUS				THE P
5	DISTRIBUTED SYSTEMS LAB	25	25	21	22	43
6	PROJECT	200	100	180	89	269
7	GENERAL PROFICIENCY		50		42	42

TOTAL

SEVEN HUNDRED FOURTEEN

1000

714

#### STATEMENT OF CONSOLIDATED MARKS OBTAIND IN B.TECH EXAMINATION

SEMESTER	MAXIMUM MARKS	MARKS OBTAINED
TOTAL OF 4th YEAR 100%	2000	1400
TOTAL OF 3rd YEAR 75%	1500	945.00
TOTAL OF 2nd YEAR 50%	1000	572.50
TOTAL OF 1st YEAR 25%	500	265.75
GRAND TOTAL	5000	3184

Result

PASSED

Division:

SECOND

66433628

Date: 01/08/2018

Checked By

Controller of Exams